

Curriculum Vitae

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Personal information:

Full Name: **Dinh Tuan NGUYEN**
(in Vietnamese: **Nguyễn Đình Tuấn**)

Date of Birth: September 16, 1985

Gender: Male

Nationality: Vietnamese

Marital Status: Married

Current Position: Lecturer, Department of Microelectronics & Microprocessing
Le Quy Don Technical University, Hanoi, Vietnam

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Education:

- **Master of Applied Mathematics and Physics**, specialization on Electronic Computers, Computer Engineering, in Moscow Institute of Physics and Technology (State University), Russia Federation (2012). Grade: *Excellent*, GPA: **4.9/5.00** (in Russian grade system), Major: Electronic Engineering.
- **Bachelor of Applied Mathematics and Physics**, specialization on Radio Engineering and Cybernetics, in Moscow Institute of Physics and Technology (State University), Russia Federation (2010). Grade: *Good*, GPA: **4.5/5.0** (in Russian grade system).

Research interests:

- Hardware security and hardware implementation of cryptography algorithms
- Internet of Things (IoT) Systems and Implementations
- Embedded system design for smart monitoring systems
- Digital ASIC design, FPGA-based and reconfigurable hardware design

Professional records:

- June 2016 - Now: Lecturer, Department of Microelectronics & Microprocessing, Faculty of Radio-Electronics, Le Quy Don Technical University, Hanoi, Vietnam.
- March 2013 - May 2016: Teaching Assistant at the Faculty of Radio-Electronics, Le Quy Don Technical University, Hanoi, Vietnam.

Research Projects:

1. "Research on ultra-low power, high-speed authenticated encryption hardware cores for emerging wireless networks". National level project (Funded by NAFOSTED). Role: Key member.

2. "Research on digital background calibration of channel mismatches for high speed time-interleaved ADCs in wideband RF direct receivers". National level project (Funded by NAFOSTED). Role: Key member.
3. "Design and manufacture the control and simulation system of the URAN-E missile for training mission". University level project (Funded by Le Quy Don Tech. Uni., 2013). Position: Key member.
4. "Research on low power IP cores for applications in IoT and wireless networks". University level project (Funded by Le Quy Don Tech. Uni., 2017). Role: Key member.

Publication - Journal papers:

1. **Dinh Tuan Nguyen**, "*Increasing the speed of data transmission CAN package*" ("Повышение скорости передачи данных CAN пакета" in Russian), Journal of Science and Technology, Le Quy Don Technical University, ISSN: 1859-0209, no.4, pp. 32-45, October 2015.
2. Van-Phuc Hoang, **Dinh Tuan Nguyen**, Yuta Kato, "*A linearity optimization method for CMOS DAC using R-2R ladder network*", Submitted to Journal of Science and Technology, Le Quy Don Technical University, ISSN: 1859-0209.
3. Van-Lan Dao, Anh-Thai Nguyen, **Dinh Tuan Nguyen** and Van-Phuc Hoang, "*A Low Area, Low Power AES-CCM Authenticated Encryption Core with Silicon Demonstration in 180nm CMOS Process*", Submitted to International Journal of Electrical and Computer Engineering (IJECE), ISSN: 2088-8708.
4. Nguyen Anh Thai, Nguyen Van Tinh, **Nguyen Dinh Tuan**, Hoang Van Phuc, Pham Tuan Giao, "Design of high performance AES-GCM authenticated encryption hardware core in 90nm CMOS process", Accepted for Journal of Military Science and Technology Research, ISSN: 1859-1043.

Publication - International conference papers:

1. **Dinh Tuan Nguyen**, Kholopov Yuri Alekseevich, "*Increasing the speed of data transmission CAN package*" ("Повышение скорости передачи данных CAN пакета" in Russian), Vietnam - Russian International Scientific Conference, April 2015.

Honors and Awards:

- Innovative Youth Prize, Signal and Communication Arm 2015, with the initiative: "Software supporting and ensuring in regional technical assistance".
- Vietnamese Government Scholarship for Bachelor and Master Courses in Moscow Institute of Physics and Technology (State University), Russia Federation.
- Second Prize in Chemistry in the Provincial Contest of 12th grade High School Excellent pupils of the school year of 2002-2003, awarded by Director of Department of Education and Training, Hai Duong Province, Vietnam.

Technical skills:

- HDL (VHDL and Verilog)
- FPGA design (Xilinx ISE, Modelsim for Simulation)
- ASIC design (Synopsys, Cadence)
- Design Printed circuits in Altium designers
- Electronics Simulation/Analysis: MicroCap, Proteus Professional
- Good at C++, C#, and Python

Language skills:

- Vietnamese: Native language
- English: Fluently
- Russian: Intermediate level

References:

- **Prof. Xuan-Nam Tran**

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- **Prof. Van-Phuc Hoang**

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