

BUS1  
RC2014 LARGE BOARD

A15	A15
A14	A14
A13	A13
A12	A12
A11	A11
A10	A10
A9	A9
A8	A8
A7	A7
A6	A6
A5	A5
A4	A4
A3	A3
A2	A2
A1	A1
A0	A0
GND	GND
VCC	VCC
RESET	RESET
CLK2	CLK
BUSACK	INT
HALT	MREQ
BUSRO	WR
WAIT	RD
MMI	IOREQ
D8	D0
D9	D1
D10	D2
D11	D3
D12	D4
D13	D5
D14	D6
D15	D7
FX2	TX
RX2	RX
USR5	USR1
USR6	USR2
USR7	USR3
USR8	USR4

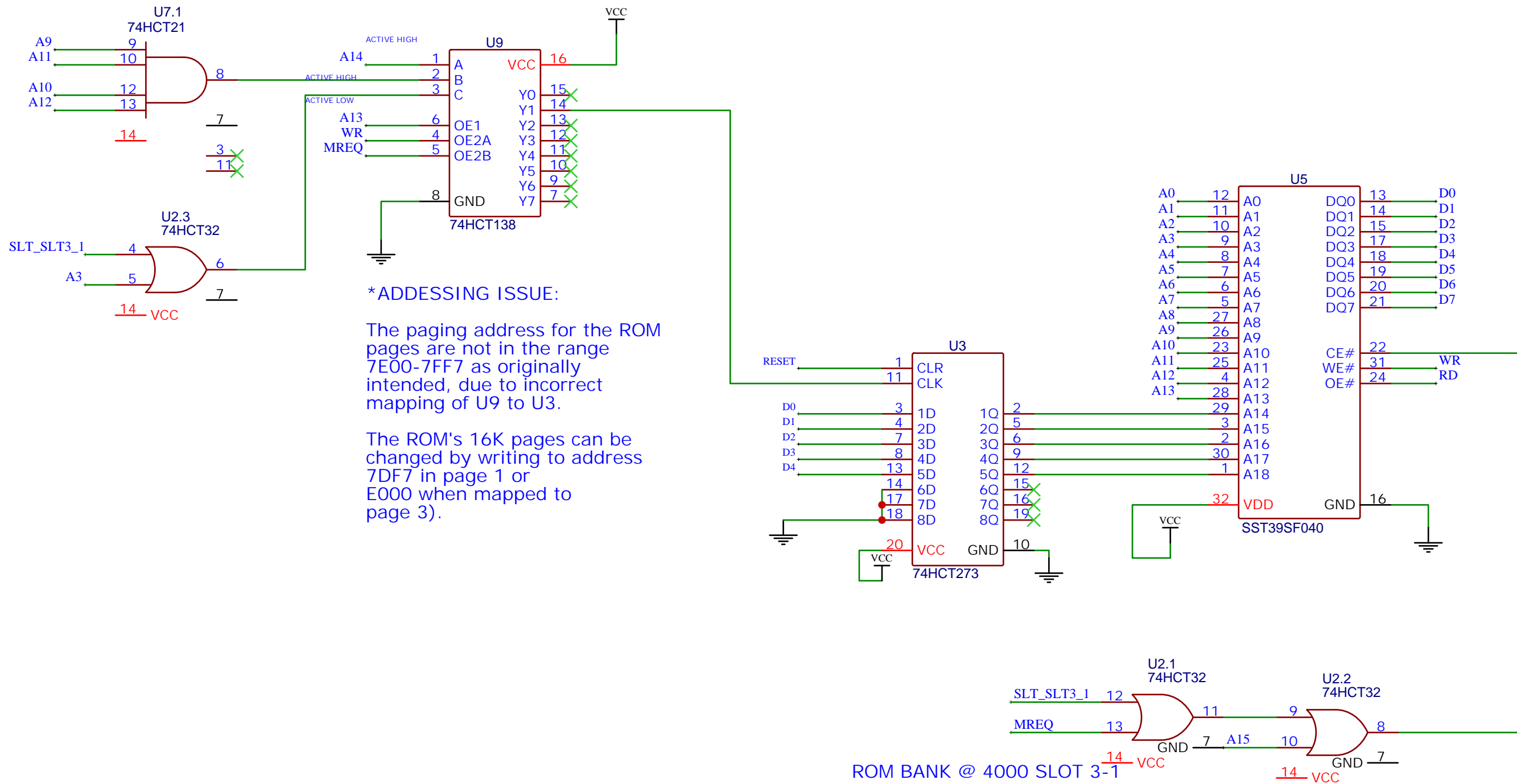
J1  
HDR-M-2.54\_1x2

1	SLT_SLT3_1
2	

C3 0.1uF C4 0.1uF C5 0.1uF C14 0.1uF C15 0.1uF C16 0.1uF

<https://raw.githubusercontent.com/RBSC/F4-FMPAC/master/Doc/schematics.png>  
<https://i0.wp.com/www.msxinfo.net/wp-content/uploads/2014/03/fmpak.jpg>  
[https://arduinoplusplus.files.wordpress.com/2019/11/ym2413\\_schematic.png](https://arduinoplusplus.files.wordpress.com/2019/11/ym2413_schematic.png)  
<http://www.waveguide.se/?article=46&file=mc3-io-ym2413-sch.png> (<http://www.waveguide.se/?article=the-ym2413-fm-opll>)

TITLE: RC2014-MSX-MUSIC		REV: 1.7
	Company: Dino Boards	Sheet: 1/2
	Date:	Drawn By: dean.netherton



### \*ADDESSING ISSUE:

The paging address for the ROM pages are not in the range 7E00-7FF7 as originally intended, due to incorrect mapping of U9 to U3.

The ROM's 16K pages can be changed by writing to address 7DF7 in page 1 or E000 when mapped to page 3).

ROM BANK @ 4000 SLOT 3-1

(Mirrored @ 0000 SLOT 3-1 - though would not typically be mapped)

ROM can not be enabled for pages 2 & 3 (0x8000-0xFFFF), but the paging register U3 can be addressed via page 3, to facilitate flashing of ROM.

TITLE: RC2014-MSX-MUSIC-ROM		REV: 1.7
	Company: DINO BOARDS	Sheet: 2/2
	Date:	Drawn By: dean.netherton