Dinura Dissanayake

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☐ Dinura Dissanayake
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I'm a 3rd year computer engineering undergraduate with a keen interest in machine learning, computer vision and embedded systems.

Education

University of Peradeniva

BSc Engineering (Hons) — Faculty of Engineering

GPA - 3.77 / 4.0

D.S. Senanayake College

G.C.E. Advance Level Examination

December 2009 – August 2017

December 2018 - May Present

Comb. Maths (A), Physics (B), Chemistry (B)

Relevant Coursework

- Data Structures
- Algorithms Analysis
- Artificial Intelligence
- Systems Programming

- Software Methodology
- Database Management
- Internet Technology
- Computer Architecture

Projects

Analysis Tool for Industrial Images | Group Project

January 2022 - Present

- An analysis tool for increasing the performance of an injection mold maintenance device which uses image processing techniques to identify stuck plastic particles on molds.
- Technologies React JS, Node JS, Express, Python
- Project Page Link

Remote Keyboard Tutoring System | Group Project

July 2021 - Dec 2021

- A web-based system interactively learn, play or teach in combination with the aid of a hardware unit and a web
 application.
- Technologies React JS, Node JS, Express, Flutter, Embedded Systems
- Project Page Link

Visitors Management System | Group Project

November 2020

- A web based system to manage visitors to any institution.
- Technologies SQL, HTML, CSS, JavaScript
- Project Repository Link

8-bit single cycle processor | Group Project

2020

- An 8-bit single cycle CPU with associated memory hierarchy. The processor includes an ALU, register file, control logic, forwarding unit, data memory, data cache, instruction memory and instruction cache.
- Technologies Verilog-HDL
- Project Repository Link

Compiler for Cool Language | Group Project

2022

- The combination of a lexer, parser, semantic analyser, and code generator that can be used to compile programs written in Cool programming language.
- Technologies C++
- Project Repository Link

Image Processing techniques to detect damaged fruit | Group Project

2019

- Image Filtering with OpenCV was used to create an algorithm to detect the deformities of fruit. Created application using python to continuously monitor given set of images.
- Technologies Python, OpenCV, Kanny, Sobel
- Project Repository Link

Technical Skills

Languages: Python, Java, C, HTML/CSS, JavaScript, SQL, C++, ARM Assembly, Verilog-HDL

Frameworks: React JS, Express, Node JS, Flutter, Jekyll

Graphic Designing: Photoshop, Illustrator, Blender, Premier Pro

Experience

Teaching Assistance - CO 222 - Programming Methodology

May 2020 - September 2020

- Supervised weekly 2hr long online lab sessions
- Created questions for online quizzes based on the C programming Language

Teaching Assistance - CO 226 - Database Systems

November 2021 - January 2022

• Supervised weekly 2hr long online lab sessions

Computer Engineering Department Web Development Volunteer

November 2022 - Present

Achievements

Mini Hackathon - ICDS

2021

- Data Science Hackathon organized by University of Colombo
- 5th Place(Out of 100+ teams)

ACES Coders

2020

- Overnight inter-university coding competition organized by University of Peradeniya
- 14th Place(Out of 100+ teams)

Sri Lankan Physics Olympiad organized by the Institute of Physics, Sri Lanka

2017

• Bronze Medalist

Leadership / Extracurricular

AIESEC in University of Peradeniya

Marketing Manager - Incoming Global Volunteer

Business to Business Manager - Incoming Global Volunteer

Business to Business Team Leader - Incoming Global Volunteer

Value Delivery Team Leader - Incoming Global Volunteer

Dec 2019 - February 2022

September 2021 - February 2022

March 2021 - August 2021

September 2020 - February 2021

March 2021 - August 2021

AIESEC in Sri Lanka

Incoming Global Volunteer Task Force Member

Media Specialist(Radio) - Public Relations Team - Youth4 Youth

Dec 2019 – Present

September 2020 - December 2021

February 2022 - Present

Recommendation

Prof. Roshan G. Ragel

Head of Department,

Department of Computer Engineering,

Faculty of Engineering,

University of Peradeniya, Sri Lanka

Dr. Isuru Nawinne

Senior Lecturer,

Department of Computer Engineering,

Faculty of Engineering,

University of Peradeniya, Sri Lanka