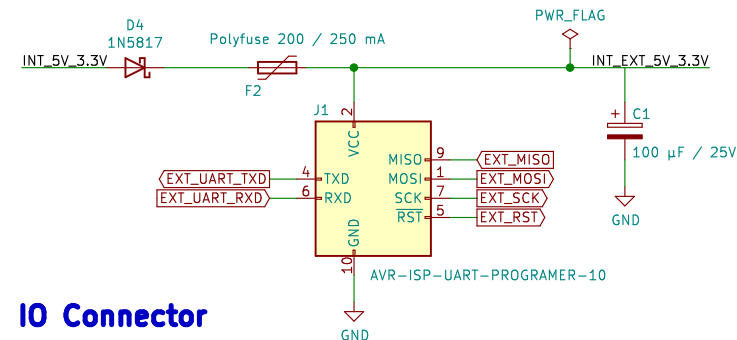
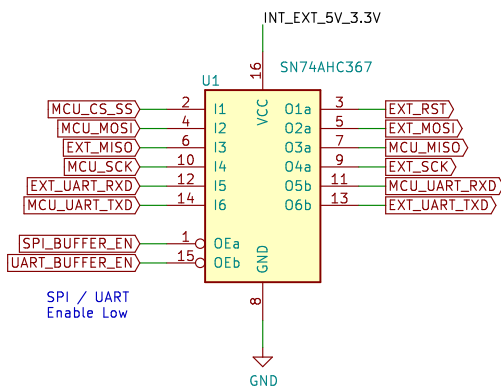
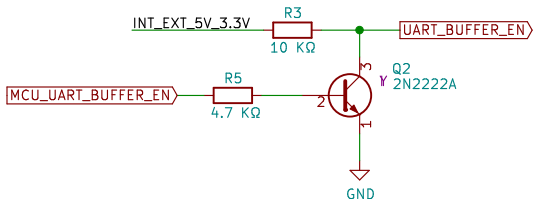
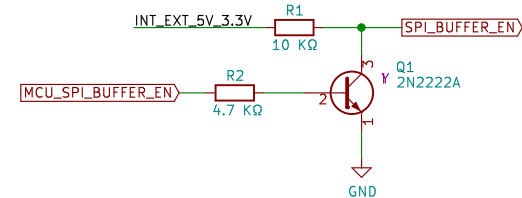
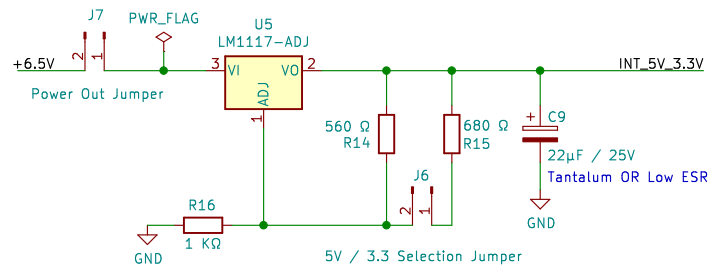


H1 MountingHole
H2 MountingHole
H3 MountingHole
H4 MountingHole

SPI / UART BUFFER

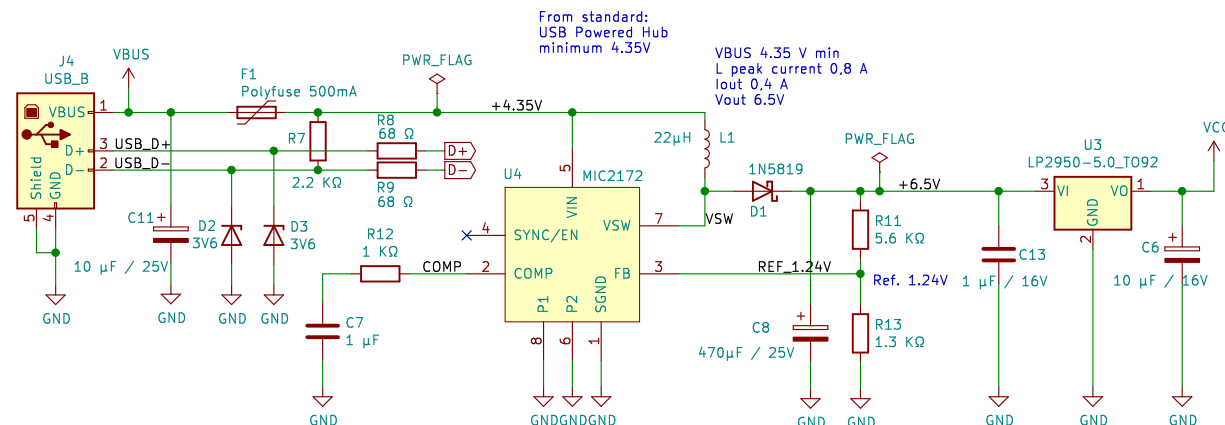


IO Connector

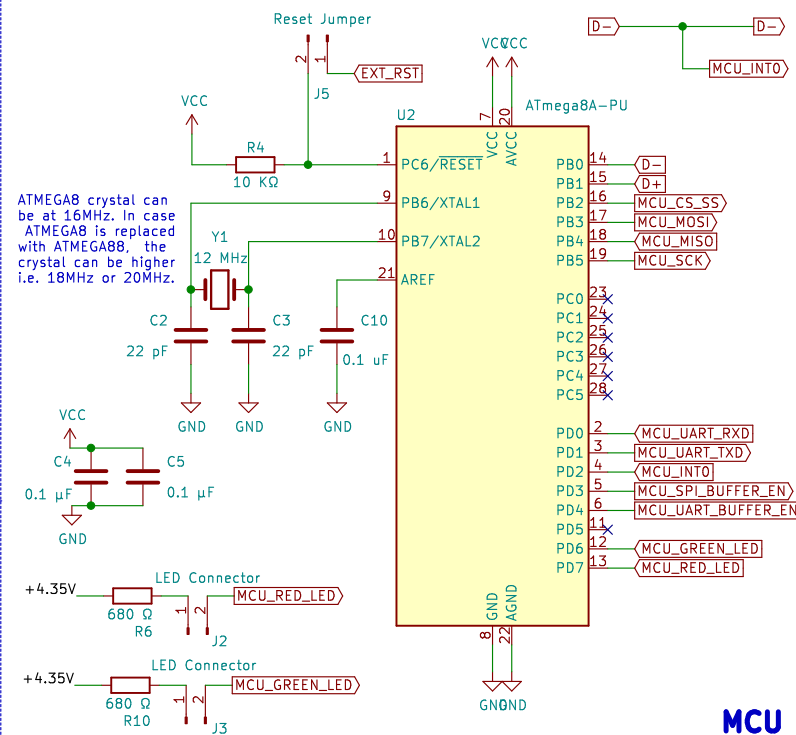


$V_{out} = 1.25 * (1 + R_{Low}/R_{High})$
(consider voltage drop
of PTC F2 and Diode D4)
RLow = R16
J6 Open: RHigh = R14
J6 Close: RHigh = R14, R15 in parallel

Target Power



USB Power



MCU

USBaisp with UART
www.nephelae.eu

Sheet: /
File: usbaispuart.sch

Title: USBaispuart (CERN-OHL-S-2.0 licenced)

Size: A4 Date: 2023-04-03
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Rev: 5b
Id: 1/1