4th Lab work – Project: Intelligent control of traffic flow[1]

Goal

The goal of this project is the implementation of a modular, automatic and distributed control of traffic lights at intersections. The system is able to adapt the control of the traffic lights to the variations in traffic, to let traffic flow faster on the busiest roads.

The overall system has some degree of fault tolerance and fail safe behavior.

In the laboratory traffic lights will be replaced by LEDs, and traffic sensors by buttons or switches. Otherwise the system to be implemented exhibits realistic behavior. All the peripherals on an intersection are connected to a controller. Intersections exchange information through an I2C bus.

Description

The purpose of the project is the design and development of a traffic lights system to control traffic at intersections.

Each intersection has the layout depicted in Fig. 1 forming a simples cross. Each access accepts vehicles in both directions and there are no restrictions to the movement of vehicles in the intersection, beyond the basic rules of right-hand traffic. Accesses are identified by their orientation:

- N – North (upper access);
- S – South (lower access);
- E – East (right access);
- W – West (left access).

Each intersection has two traffic lights at orthogonal accesses. In the figure, TL_S controls accesses of vehicles approaching the intersection from South, and TL_W controls vehicles

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[1] V1.0, 7 November 2019.

approaching from West. Traffic flow in these accesses is monitored by inductive loop detectors (ILD, or LD) embedded in the road pavement and simulated by some form of switch (any device able to toggle a signal from low-high-low – a press button, a potentiometer, a jumper).

To simplify the hardware assembly we will not implement the traffic lights and sensors in the remaining two accesses, and will evaluate traffic flow based on the information gathered in the in the two accesses emulated.
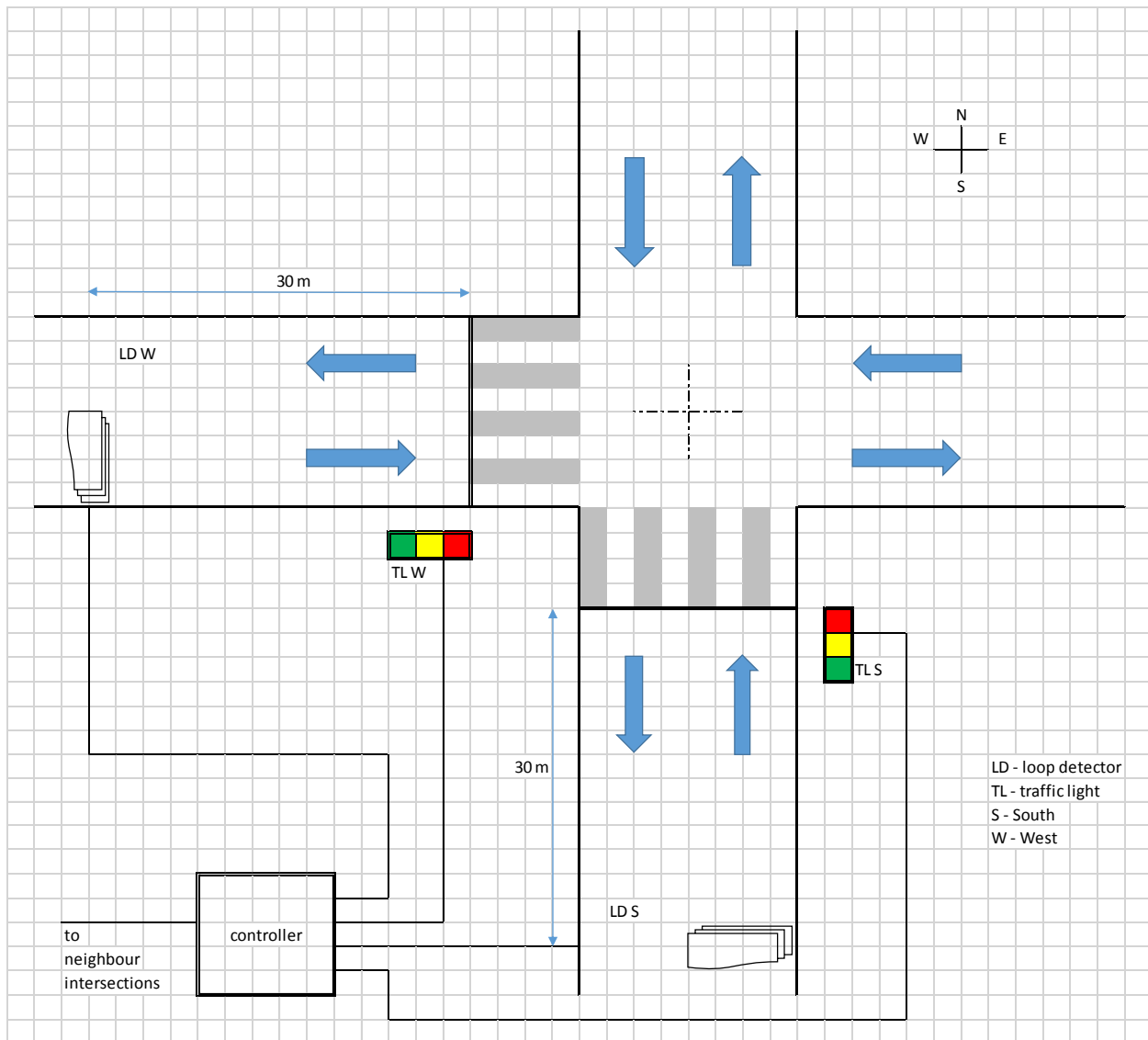

Fig. 1 – Intersection.

Traffic Light

Every traffic light must have 3 LEDs – green, yellow, and red. The intersection controller must be able to detect the failure of any red LED.

Loop Detector

Inductive loop detectors (LD) embedded in the pavement of the road are a common sensor used to detect the passage of the vehicles over them. The detection mechanism is based on the variation of the frequency of a resonant circuit when the metallic mass of a vehicle passes over the coil embedded in the pavement and changes its equivalent inductance.

Fig. 2 – Inductive loop detectors for cars and bikes.[2]

Considering that an LD generates a voltage HIGH when a car is passing over and a voltage LOW otherwise, its function can be simulated by any device which is able to toggle a signal from low-high-low – a press button, a potentiometer, a jumper, etc. LDs can be used to count vehicles, and sets of LDs can measure speed and classify the type of vehicle by evaluating its size.

Controller:

The controller must be designed as an Arduino UNO with the I/O devices attached.
- 2 × 3 LEDs, the traffic lights;
- 2 LD simulators (press buttons, potentiometers, etc,);
- the I2C bus interface to communicate with the controllers of neighbor intersections.

The identification of the intersection is specified by its (x, y) coordinates in space. These are setup by wire jumpers connected to input ports of the controller:
- x = a3, a2, a1, a0 (4 bits)
- y = b3, b2, b1, b0 (4 bits)

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[2] Induction Loop. https://en.wikipedia.org/wiki/Induction_loop . Acessed Nov. 2019.

Requirements

1) **Initial state** of the system (at an intersection) – all traffic lights blinking yellow with a 2 seconds period (ON + OFF cycle time) during 6 seconds. After this the system enters normal operation.
2) When a malfunction is detected (red LED failure, software fault) the system will go to a **standby state** stopping all functions except blinking the yellow LEDs with a 2 seconds period (Fig. 3).
3) **Normal operation** has three modes of functionality:
   a) **Mode 0** – Traffic lights switch between red and green with a fixed duty cycle, without any consideration of traffic density.

      Period = 20 seconds, with 50% duty cycle.

   b) **Mode 1** - Traffic lights switch between red and green with a duty cycle adjusted according to the relative levels of traffic in each access.

      Period = 20 seconds

      Duty_cycle_S = cars_S / (cars_S + cars_W)
      Duty_cycle_W = cars_W / (cars_S + cars_W)

      bounded in
      5/20 = 25% ≤ Duty cycle ≤ 15/20 = 75%
      to avoid blocking one road due to traffic on the other.

   c) **Mode 2** - Traffic lights switch between red and green with a duty cycle adjusted according to the relative levels of traffic in each access and the information gathered from the neighbor intersections.

      Besides varying the duty cycle of traffic lights according to the traffic densities – like in mode 1 – in mode 2 the controller tries to adjust the phase of lights to enable along a street with a high level of traffic the movement of vehicles without "disturbing" red lights.

   Modes 0 and 1 are purely local modes which evaluate only information from local LDs. (Mode 0 does not even read LDs.) Mode 2 is a distributed control mode in which all the controllers in an area cooperate to improve traffic flow.

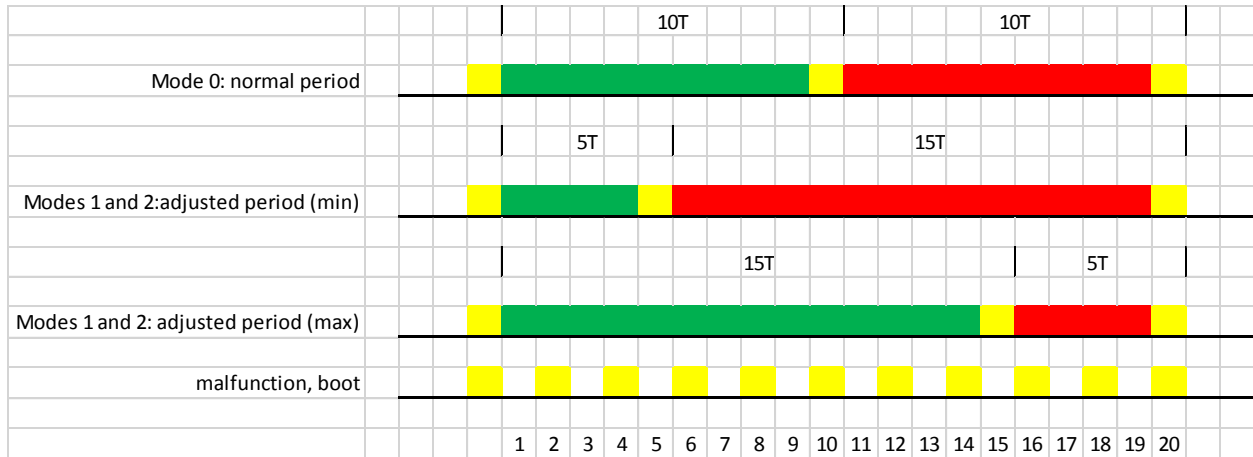4) All communications between intersections must be performed using I2C protocol.

Fig. 3 – Traffic lights operation.

Data Interchange

Each intersection interchanges data with the surrounding cells through an I2C bus in multi-master mode. Every traffic lights period the controller of an intersection sends messages to its neighbors informing its state. The I2C frame has an addressing block and a data block.

The addressing block records the destination of the message:
- Destination (8 bits) - (x, y) identifier of the intersection to which the frame is sent.

The data block has the following format:
- Source (8 bits): (x, y) identifier of the intersection which generates the frame.
- Event (8 bits): Type of event or action signaled
  - 0 – R2G S (red to green in the South access); in fact corresponds to yellow_after_red to green in Fig. 3;
  - x – R2G W, and other events, are not signaled due to the simplification of the installation of the intersection.
- Time (32 bits): Time stamp associated with the event. The time unit is 100 ms.

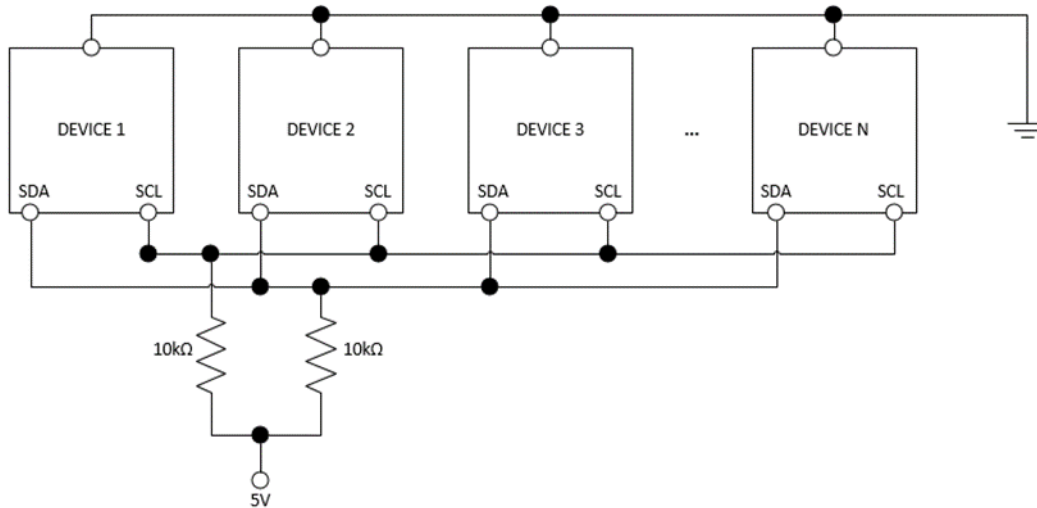The physical connections to implement the I2C bus are depicted in Fig. 4.

Fig. 4 – I2C Multi-master diagram.

Recommendations

In order to fulfill your work with security and not damaging the hardware involved, remember to carry out the recommendations below. As you are working fill the boxes to be certain that you fulfill all security measures.

Always work with the circuit disconnect from the source.	
Call the professor or responsible for the laboratory, before you connect the circuit to the source.	
Make sure the circuit is well connected (resistors, capacitors, etc.) to prevent a short circuit, or damage the hardware.	

Plan and Deliverables

Week	Activity
1	Project presentation.
2	Requirements analysis and specification of the data interchange formats of I2C messages. Project design and development.
3	Specification of the data interchange format. Project design and development.
4	Demonstration of two contiguous intersections by each group. Delivery of the project report (printed report, and e-mail), including a. design of the circuits of the controller, traffic lights, and sensors, b. overall architecture of the software in the controller, c. safety and fault-tolerance measures adopted, d. "intelligent" functions implemented, e. programs (properly commented) implementing the controller and traffic lights. (The report includes interoperable functionalities to be demonstrated in week 5.)
5	Demonstration of the integrated system controlling a city area by sets of student groups to be defined. Each set will have 3 – 4 groups of students, each group handling two intersections. Each group delivers (printed report, and e-mail) a brief evaluation report of the integration test, to complete, correct, or confirm the report delivered in week 4.
6	Oral evaluation of the groups. Covers all laboratory works and the project.