Time	0 100 ns 200 ns 300 ns 400 ns 500 ns 600 ns 700 ns 800 ns 1 us 1100 ns 1200 ns 1300 ns 1300 ns
clock	
done	
enable	
:0]	$+\lambda_{01}$
	(uu X01
lcd_en	
lcd_rs	
lcd_rw	
rs	
runsim	