Model Advisor Report – prct_model_IMV.slx

Simulink version: 9.1 **Model version:** 1.37

System: prct_model_IMV/HPP_IMV **Current run:** 04-Jul-2021 12:08:40

Treat as Referenced Model: off 5 items with a timestamp different than 04-Jul-2021 12:08:40

Run Summary

Pass	Fail	Warning	Not Run	Total
6	0	0	0	6

Simulink Design Verifier

Design Error Detection 5 0 0 0

Detect Dead Logic (04-Jul-2021 11:59:09)

Identify dead logic in model 'prct_model_IMV/HPP_IMV' with Simulink Design Verifier.

Passed

No **dead logic** found in 'prct_model_IMV/HPP_IMV'. To extend the results of this check, use Simulink Design Verifier design error detection to identify active logic as well. From the Simulink Editor, select **Analysis** > **Design Verifier** > **Options**. In the Design Error Detection pane, select both **Dead logic** and **Identify active logic**.

Simulink Design Verifier Results Summary

Detect Integer Overflow (04-Jul-2021 11:59:09)

Identify integer overflow in model 'prct_model_IMV/HPP_IMV' with Simulink Design Verifier.

Passed

No **integer overflow** found in 'prct_model_IMV/HPP_IMV'.

Simulink Design Verifier Results Summary

Detect Division By Zero

Identify division by zero in model 'prct_model_IMV/HPP_IMV' with Simulink Design Verifier.

Passed

No **division by zero** found in 'prct_model_IMV/HPP_IMV'.

Simulink Design Verifier Results Summary

Detect Out Of Bound Array Access (04-Jul-2021 11:59:09)

Identify out of bound array access in model 'prct_model_IMV/HPP_IMV' with Simulink Design Verifier.

Passed

No **out of bound array access** found in 'prct_model_IMV/HPP_IMV'.

Simulink Design Verifier Results Summary

Detect Violation of Specified Intermediate Minimum and Maximum Values (04-Jul-2021 11:59:09)

Identify violation of specified intermediate minimum and maximum values in model 'prct_model_IMV/HPP_IMV' with Simulink Design Verifier.

Passed

No **violation of specified intermediate minimum and maximum values** found in 'prct_model_IMV/HPP_IMV'.

Simulink Design Verifier Results Summary

Check compatibility with Simulink Design Verifier (04-Jul-2021 11:59:09)

Check compatibility of model 'prct_model_IMV/HPP_IMV' with Simulink Design Verifier.

Passed

'prct_model_IMV/HPP_IMV' is **compatible** with Simulink Design Verifier.
