Simplified Stack Machine Assembler Manual

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Abstract

This document defines the assembly language for the Simplified Stack Machine VM, which is used in the Systems Software class (COP 3402) at UCF. It also defines the interface of the assembler and disassembler.

1 Overview

The assembler for the Simplified Stack Machine (SSM) is simple and has no macro facilities. However, it does let one assemble the instructions that make up a program, resolving symbolic names for jump targets, and it can define the starting address of a program and the program's (static) data section. This assembly language adopts some conventions from the MIPS processor's assembly language [1].

The assembler assumes that the SSM has 32-bit (4-byte) words and is word-addressable.

1.1 Inputs and Outputs

1.1.1 Assembler

The assembler is passed a single file name as its only command line argument; this file should be the name of a (readable) assembler program; its output (sent to standard output) is a binary object file.

For example, if the program is contained in the file myProg.ssm, assuming that the assembler's executable is named asm (and both these files are in the current directory), then the assembler can be invoked as follows in the Unix shell to produce a binary object file on standard output.

```
./asm myProg.ssm
```

Thus, to put the assembled version of myProg.ssm into the file myProg.bof, one would use a Unix command that redirects the output of the assembler into myProg.bof, as follows.

```
./asm myProg.ssm > myProg.bof
```

In addition to the binary object file name, the assembler can be given one command line argument. These options are used for debugging the assembler's code: -1 to print the lexical tokens read, -u to unparse the input's abstract syntax tree (after constructing it), and -s to print the symbol table for the file. A binary object file name must always be given after using an option. Only one option can be used for any given invocation of the assembler, and when the -1 or -u options are used, no binary object file is produced.

1.1.2 Disassembler

The disassembler is the opposite of the assembler. It is passed a single file name, but that file names a (readable) binary object file, and it produces, on standard output, an assembly language source program.

For example, if the binary object file is found in prog.bof, assuming that the disassembler is named disasm, (and both these files are in the current directory), then the disassembler can be invoked as follows in the Unix shell to produce (on standard output) an assembly language program that would compile to prog.bof.

```
./disasm prog.bof
```

The assembly language program can be redirected into the file prog.ssm as follows.

```
./disasm prog.bof > prog.ssm
```

1.2 Error Outputs

All error messages (e.g., for file permission errors or syntax errors) are sent to standard error output (stderr).

1.3 Exit Codes

When the either program halts normally, it exits with a zero error code (which indicates success on Unix). However, when asm or disasm encounters an error, it halts and exits with a non-zero exit code (which indicates failure on Unix).

2 Assembly Language Syntax

2.1 Lexical Grammar

Tokens in the assembler are described by the (regular) grammar of Figure 1. Note that line endings are significant in the context-free grammar of the assembler, as each instruction must be specified on a single line. Lines may be ended either by a newline character ($\langle \text{newline} \rangle$ in Figure 1) or by a combination of a carriage-return ($\langle \text{cr} \rangle$) followed by a newline. Comments ($\langle \text{comment} \rangle$) start with a $\langle \text{comment-start} \rangle$ character (i.e., #) and continue to the end of a line. White space is needed to separate tokens, but is otherwise ignored.

The lexical grammar (in Figure 1) uses a terminal font for terminal symbols. Note that an underbar (_) and all ASCII letters (a-z and A-Z) are included in the production for \langle letter \rangle . Curly brackets, such as $\{x\}$, mean an arbitrary number of (i.e., 0 or more) repetitions of x. Note that curly braces are not terminal symbols in the grammar. Some character classes are described in English, these are described in a Roman font between double quotation marks (" and "). Note that all characters matched by the nonterminal \langle ignored \rangle are ignored by the lexer. However, the characters that are part of an \langle eol \rangle token (i.e., carriage returns and newlines) are not ignored immediately following a pound-sign (which starts a comment) \langle reserved-opcode \rangle or \langle reserved-data-size \rangle , although they are ignored in all other contexts.

2.2 Context-Free Grammar

The syntax of the SSM's assembly language is defined by the (context-free) grammar in Figure 2 and Figure 3. The grammar uses a typewriter font for terminal symbols.

```
⟨section-mark⟩ ::= .text | .data | .end
⟨reserved-opcode⟩ ::= NOP | ADD | SUB | CPW | AND | BOR
        NOR | XOR | LWR | SWR | SCA | LWI | NEG | LIT | ARI | SRI
      | MUL |DIV | CFHI | CFLO | SLL | SRL | JMP | CSI | JREL
      | ADDI | ANDI | BORI | NORI | XORI | BEQ | BGEZ | BGTZ | BLEZ
      | BLTZ | BNE | JMPA | CALL | RTN | EXIT | PSTR | PCH
      | RCH | STRA | NOTR
⟨reserved-data-size⟩ ::= WORD | CHAR | STRING
\langle ident \rangle ::= \langle letter \rangle \{\langle letter-or-digit \rangle\} "but not a \langle reserved-opcode \rangle or \langle reserved-data-size \rangle"
\langle letter \rangle ::=  _ | a | b | ... | y | z | A | B | ... | Y | Z
\langle \text{letter-or-digit} \rangle ::= \langle \text{letter} \rangle \mid \langle \text{dec-digit} \rangle
\langle unsigned-number \rangle ::= \langle dec-digit \rangle \{\langle dec-digit \rangle\}
      0x \langle hex-digit \rangle \{\langle hex-digit \rangle\}
(dec-digit) ::= 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9
\langle hex-digit \rangle ::= \langle dec-digit \rangle \mid a \mid A \mid b \mid B \mid c \mid C \mid d \mid D \mid e \mid E \mid f \mid F
(oct-digit) ::= 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7
⟨reg⟩ ::= $ ⟨oct-digit⟩ | $qp | $sp | $fp | $r3 | $r4 | $r5 | $r6 | $ra
⟨char-literal⟩ ::= ' ⟨char-elem⟩ '
\langle char-elem \rangle ::= \langle char \rangle "but not an unescaped ' character"
\langle char \rangle ::= \langle c\text{-escape-seq} \rangle \mid \langle printable\text{-ASCII-char} \rangle
\langle c\text{-escape-seq} \rangle ::= \langle backslash \rangle \langle escape-code \rangle
⟨backslash⟩ ::= "A backslash character (ASCII 92)"
\langle escape-code \rangle ::= n \mid r \mid f \mid t \mid v \mid \langle backslash \rangle \mid ' \mid " \mid 0 \mid a \mid b
     | x \langle hex-digit \rangle \langle hex-digit \rangle | 0 \langle oct-digit \rangle \langle oct-digit \rangle \langle oct-digit \rangle | s | d | e
\langle \text{string-literal} \rangle ::= " \{\langle \text{string-elem} \rangle \} "
⟨string-elem⟩ ::= ⟨char⟩ "but not an unescaped " character"
\langle eol \rangle ::= \langle newline \rangle \mid \langle cr \rangle \langle newline \rangle
⟨newline⟩ ::= "A newline character (ASCII 10)"
⟨cr⟩ ::= "A carriage return character (ASCII 13)"
\langle ignored \rangle ::= \langle blank \rangle \mid \langle tab \rangle \mid \langle vt \rangle \mid \langle formfeed \rangle \mid \langle comment \rangle
⟨blank⟩ ::= "A space character (ASCII 32)"
⟨tab⟩ ::= "A horizontal tab character (ASCII 9)"
⟨vt⟩ ::= "A vertical tab character (ASCII 11)"
⟨formfeed⟩ ::= "A formfeed character (ASCII 12)"
\langle comment \rangle ::= \langle comment-start \rangle \{\langle non-nl \rangle\}
⟨comment-start⟩ ::= #
⟨non-nl⟩ ::= "Any character except a newline"
```

Figure 1: Lexical grammar of the SSM assembler.

```
⟨program⟩ ::= ⟨text-section⟩ ⟨data-section⟩ ⟨stack-section⟩ .end
\langle \text{text-section} \rangle ::= .\text{text} \langle \text{entry-point} \rangle \{\langle \text{asm-instr} \rangle\} \langle \text{asm-instr} \rangle
\langle \text{entry-point} \rangle ::= \langle \text{addr} \rangle
\langle addr \rangle ::= \langle label \rangle \mid \langle unsigned-number \rangle
\langle label \rangle ::= \langle ident \rangle
\langle asm-instr \rangle ::= \langle label-opt \rangle \langle instr \rangle \langle eol \rangle
\langle label-opt \rangle ::= \langle label \rangle : | \langle empty \rangle
⟨empty⟩ ::=
\langle instr \rangle ::= \langle no-arg-instr \rangle \mid \langle two-reg-comp-instr \rangle \mid \langle no-target-offset-instr \rangle \mid \langle no-source-offset-instr \rangle
        | \langle one-reg-offset-arg-instr \rangle | \langle one-reg-arg-instr \rangle | \langle one-reg-offset-instr \rangle | \langle shift-instr \rangle
            \langle arg-only-instr \rangle | \langle immed-arith-instr \rangle | \langle immed-bool-instr \rangle | \langle branch-test-instr \rangle |
        | \langle jump-instr \rangle | \langle syscall-instr \rangle
⟨no-arg-instr⟩ ::= NOP | JMP | RTN
\langle two\text{-reg-comp-instr}\rangle \ ::= \ \langle two\text{-reg-comp-op}\rangle \ \langle reg\rangle \ \ \text{,} \ \ \langle offset\rangle \ \ \text{,} \ \ \langle offset\rangle
(two-reg-comp-op) ::= ADD | SUB | CPW | AND | BOR | NOR | XOR | SCA | LWI | NEG
\langle offset \rangle ::= \langle number \rangle
\langle \text{number} \rangle ::= \langle \text{sign} \rangle \langle \text{unsigned-number} \rangle
\langle \text{sign} \rangle ::= + | - | \langle \text{empty} \rangle
\langle \text{no-target-offset-instr} \rangle ::= \langle \text{no-target-offset-op} \rangle \langle \text{reg} \rangle, \langle \text{reg} \rangle, \langle \text{offset} \rangle
\langle no\text{-target-offset-op} \rangle ::= LWR
\langle \text{no-source-offset-instr} \rangle ::= \langle \text{no-source-offset-op} \rangle \langle \text{reg} \rangle, \langle \text{offset} \rangle, \langle \text{reg} \rangle
\langle \text{no-source-offset-op} \rangle ::= SWR
\langle \text{one-reg-offset-arg-instr} \rangle ::= \langle \text{one-reg-offset-arg-op} \rangle \langle \text{reg} \rangle, \langle \text{offset} \rangle, \langle \text{arg} \rangle
⟨one-reg-offset-arg-op⟩ ::= LIT
\langle arg \rangle :
                 (number)
\langle one-reg-arg-instr \rangle ::= \langle one-reg-arg-op \rangle \langle reg \rangle, \langle arg \rangle
⟨one-reg-arg-op⟩ ::= ARI | SRI
\langle arg \rangle : \langle number \rangle
\langle \text{one-reg-offset-instr} \rangle ::= \langle \text{one-reg-offset-op} \rangle \langle \text{reg} \rangle, \langle \text{offset} \rangle
⟨one-reg-offset-op⟩ ::= MUL | DIV | CFHI | CFLO | JMP | CSI
\langle \text{shift-instr} \rangle ::= \langle \text{shift-op} \rangle \langle \text{reg} \rangle, \langle \text{offset} \rangle, \langle \text{shift} \rangle
\langle \text{shift-op} \rangle ::= \text{SLL} \mid \text{SRL}
\langle \text{shift} \rangle ::= \langle \text{unsigned-number} \rangle
\langle arg-only-instr \rangle ::= \langle arg-only-op \rangle \langle arg \rangle
⟨arg-only-op⟩ ::= JREL
\langle \text{immed-arith-instr} \rangle ::= \langle \text{immed-arith-op} \rangle \langle \text{reg} \rangle, \langle \text{offset} \rangle, \langle \text{immed} \rangle
⟨immed-arith-op⟩ ::= ADDI
\langle immed \rangle ::= \langle number \rangle
\langle immed-bool-instr \rangle ::= \langle immed-bool-op \rangle \langle reg \rangle, \langle offset \rangle, \langle uimmed \rangle
⟨immed-bool-op⟩ ::= ANDI | BORI | NORI | XORI
\langle uimmed \rangle ::= \langle unsigned-number \rangle
\langle branch-test-instr \rangle ::= \langle branch-test-op \rangle \langle reg \rangle, \langle offset \rangle, \langle immed \rangle
(branch-test-op) ::= BEQ | BGEZ | BGTZ | BLEZ | BLTZ | BNE
\langle \text{jump-instr} \rangle ::= \langle \text{jump-op} \rangle \langle \text{addr} \rangle
⟨jump-op⟩ ::= JMPA | CALL
```

Figure 2: The (context free) grammar of the SSM assembler, part 1 of 2.

```
 \langle syscall-instr \rangle ::= \langle offset-only-syscall \rangle \mid \langle reg-offset-syscall \rangle \mid \langle no-arg-syscall \rangle \langle offset-only-syscall \rangle ::= EXIT \langle offset \rangle \langle reg-offset-syscall \rangle ::= EXIT \langle offset \rangle \langle reg-offset-syscall \rangle ::= \langle reg-offset-syscall-op \rangle \langle reg \rangle, \langle offset \rangle \langle reg-offset-syscall-op \rangle ::= PSTR \mid PCH \mid RCH \rangle \langle no-arg-syscall \rangle ::= STRA \mid NOTR \rangle \langle data-section \rangle ::= .data \langle static-start-addr \rangle \{ \langle static-decl \rangle \} \langle static-start-addr \rangle ::= \langle unsigned-number \rangle \langle static-decl \rangle ::= \langle data-size \rangle \langle ident \rangle \langle initializer-opt \rangle \langle eol \rangle \langle data-size \rangle ::= WORD \mid CHAR \mid STRING [ \langle unsigned-number \rangle ] \langle initializer-opt \rangle ::= = \langle number \rangle \mid \langle char-literal \rangle \mid \langle string-literal \rangle \mid \langle empty \rangle \langle stack-section \rangle ::= .stack \langle stack-bottom-addr \rangle \langle stack-bottom-addr \rangle ::= \langle unsigned-number \rangle
```

Figure 3: The (context free) grammar of the SSM assembler, part 2 of 2.

3 Initial Values

The initial value of the program counter (PC) is set to the address of the program's entry point (i.e., the value of $\langle \text{entry-point} \rangle$), which is declared at the beginning of the $\langle \text{text-section} \rangle$.

The start of the global data in memory is at the word address given by the data section's static data start address (i.e., the value of \langle static-start-addr \rangle), declared at the beginning of the \langle data-section \rangle ; this value is used as the initial value of the \$gp register. The data declared in the data section all have offsets from this address that are computed in declaration order, with WORD sized data and CHAR sized data both declaring a word of storage (i.e., 4 bytes) and STRING data taking the number of *words* declared for it.

The "bottom" of the runtime stack is given in a declaration in the stack section ($\langle stack\text{-section} \rangle$); it is the value of $\langle stack\text{-bottom-addr} \rangle$ that follows the .stack keyword. This must be strictly greater than the static data start address; it is also the initial value put in the \$fp and \$sp registers at the start of a program's execution.

4 Constraints on Assembly Code

There are some constraints on programs that the assembler checks; the assembler considers violations of these constraints to be an error.

The program's entry point must be strictly less than the static data start address and the static data start address must be strictly less than the stack bottom address.

Furthermore, immediate operands and offsets must fit in the number of bits allowed by the instruction's format.

References

[1] Gerry Kane and Joe Heinrich. MIPS RISC architectures. Prentice-Hall, Inc., 1992.