

# Blank Quiz

Total points 4/10

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✓ An EREW shared memory SIMD computer consists of two processing elements (PE1 and PE2). At time  $t$  the following simultaneous access to memory locations  $l$  and  $k$  is not permissible. 2/2

- ☐ PE1 reads  $k$  PE2 reads  $l$
- ☐ PE1 writes  $l$  PE2 writes to  $k$
- ☒ PE1 and PE2 read  $l$
- ☐ PE1 reads  $l$  PE2 reads  $k$



✓ In a  $4^2 \times 3^2$  delta network, number of  $4 \times 3$  elementary switches required is 2/2

- ☒ 7
- ☐ 9
- ☐ 12
- ☐ 16



✗ The instruction execution flow in the pipeline processor is represented by 0/2

- ☒ data-flow diagram
- ☐ dependence graph
- ☐ reservation table
- ☐ state diagram

✗

Correct answer

- ☒ reservation table

✗ In which category of Flynn's classification of computers do the pipelined computers fall? 0/2

- ☐ MIMD
- ☐ SISD
- ☐ SIMD
- ☒ MISD

✗

Correct answer

- ☒ SISD



✗ In an MIMD machine M, there are 8 processors and 6 memory modules. 0/2  
The memory modules and processors in M are connected through crossbar multiple shared bus interconnection network N. The total number of elementary switches in N is 42. Find the number of shared bus in N.

☒ 6

✗

☐ 8

☐ 42

☐ 48

Correct answer

☒ 42

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