

E

D

C

B

A

6

5

4

3

2

1

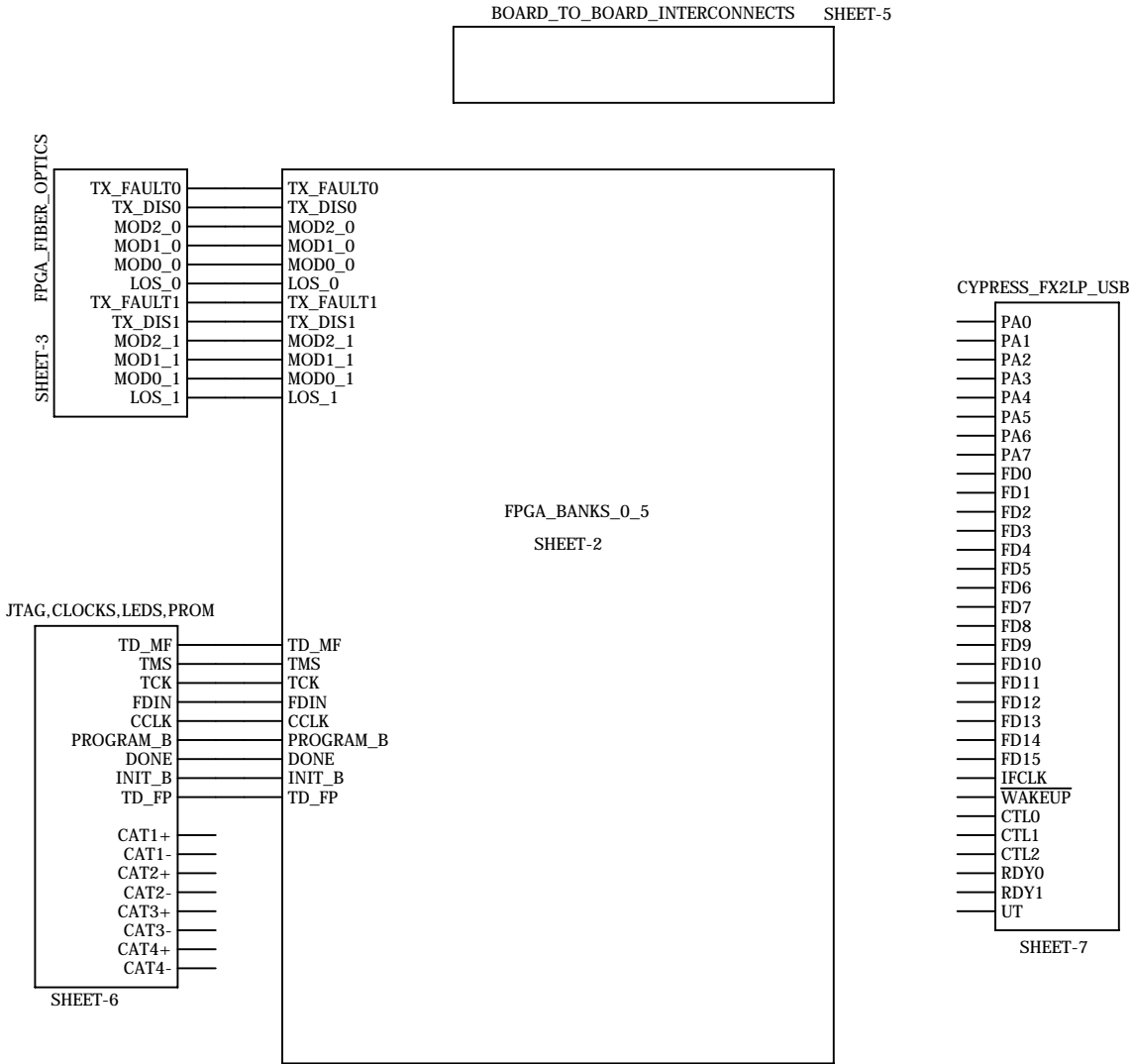
E

D

C

B

A

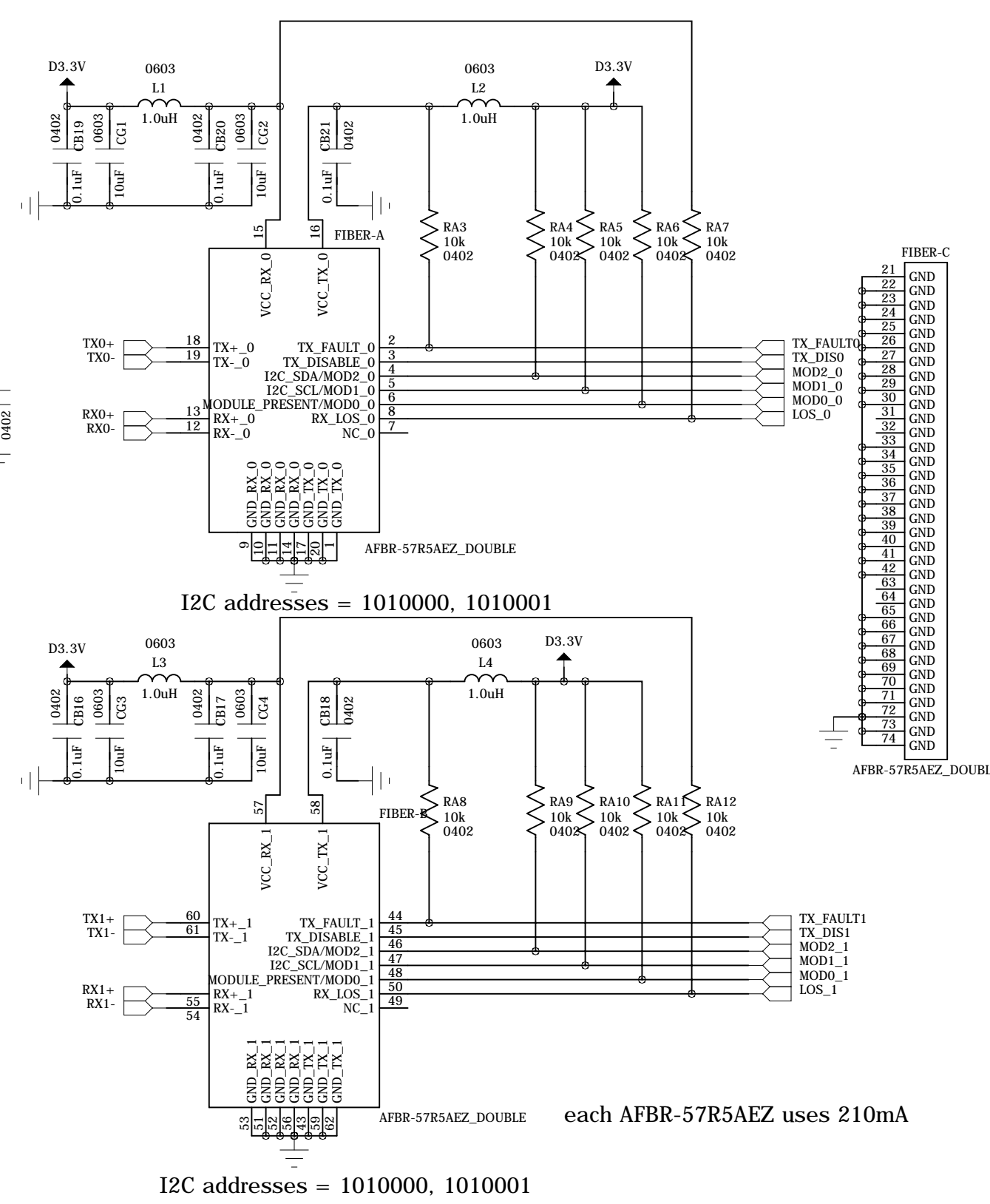
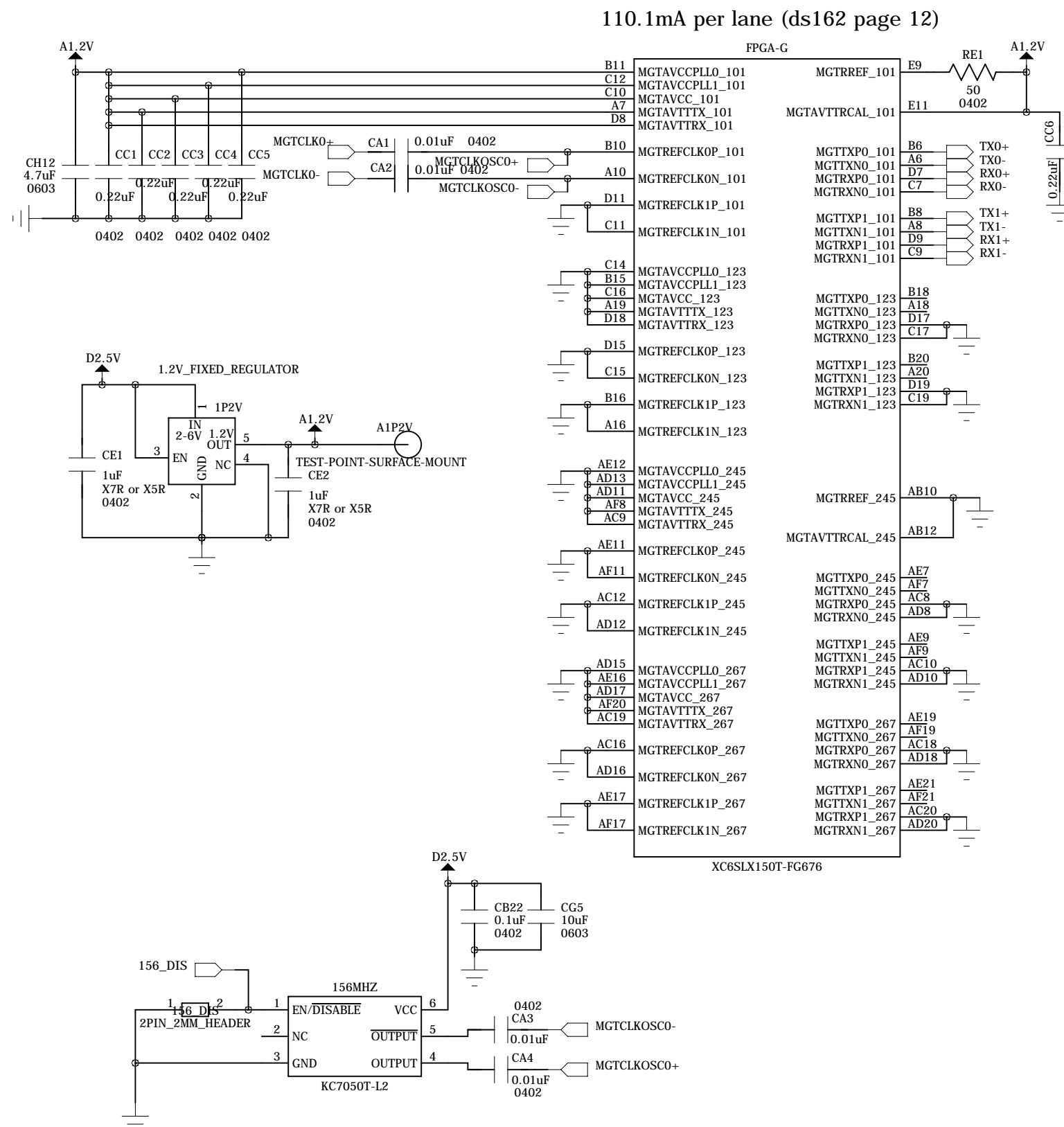


Assembly instructions:
install 2mm 2 pin jumper header 156_DIS and 250_DIS
install GND test points

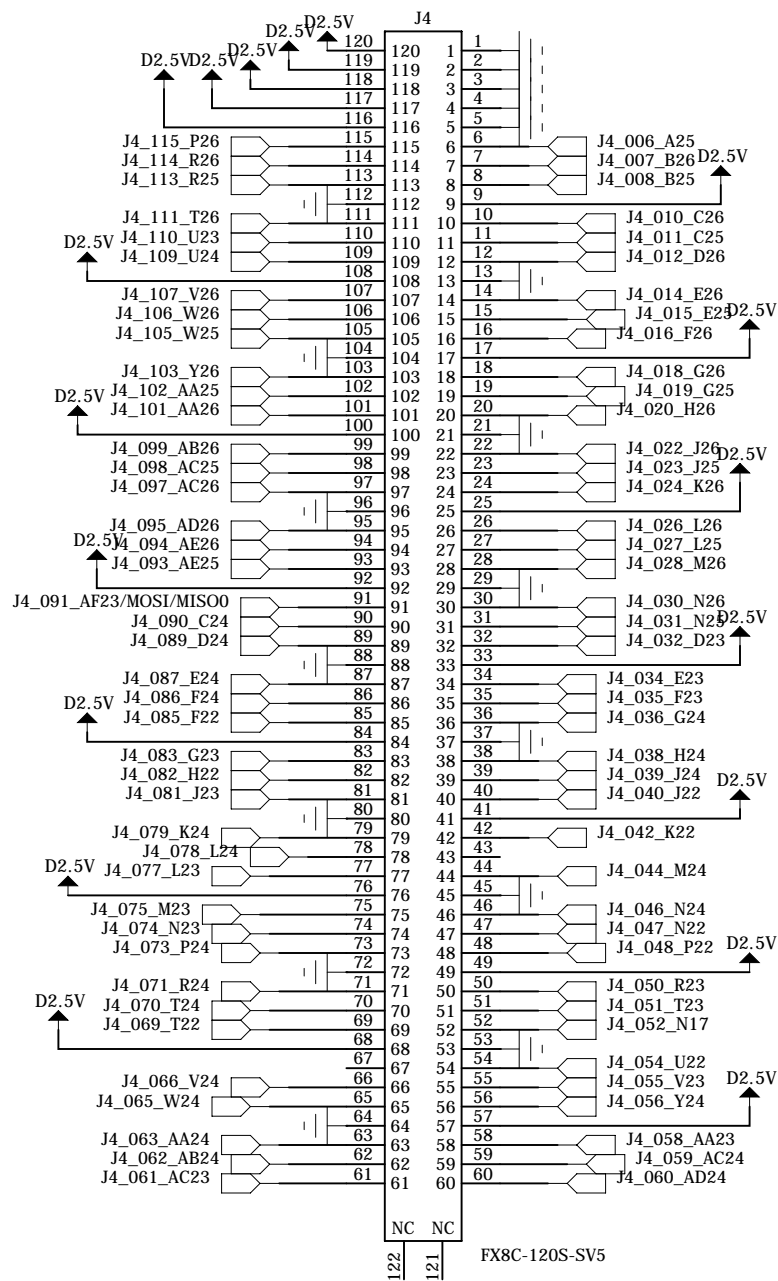
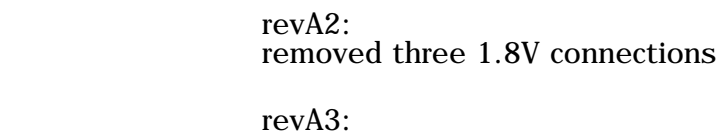
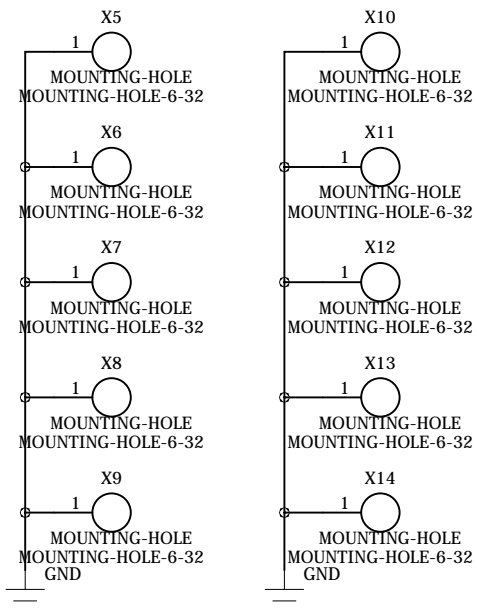
notes updated 2012-05-16
added size/package notes to schematic

notes updated 2012-10-05
added notes about bank voltages and N18/M18/L10/K10/J2/M10

institution:	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Lab
title:	SCROD
revision:	A3
IDLAB design #:	IDL_13_037
circuit design:	LJR, MZA, GSV, KAN
PCB design:	LJR, MZA
sheet #:	1 of 7
sheet description:	block diagram
date last modified:	2013-08-27



institution:	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Lab
title:	SCROD
revision:	A3
IDLAB design #:	IDL_13_037
circuit design:	LJR, MZA, GSV, KAN
PCB design:	LJR, MZA
sheet #:	3 of 7
sheet description:	fiber transceiver, fiber clock
date last modified:	2013-08-27



institution:	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Lab
title:	SCROD
revision:	A3
IDLAB design #:	IDL_13_037
circuit design:	LJR, MZA, GSV, KAN
PCB design:	LJR, MZA
sheet #:	5 of 7
sheet description:	board-to-board interconnects
date last modified:	2013-08-27

E

D

C

B

A

E

D

C

B

A

6

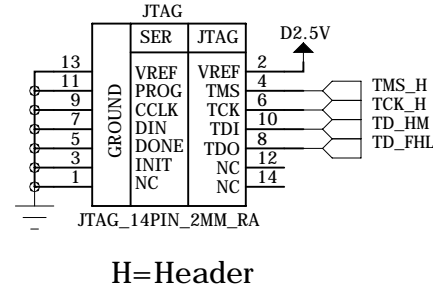
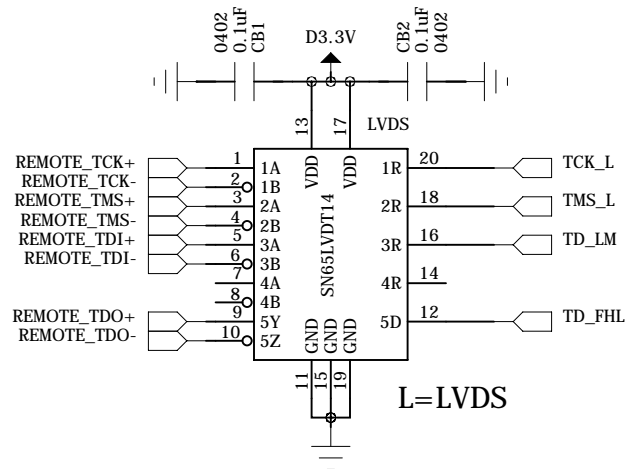
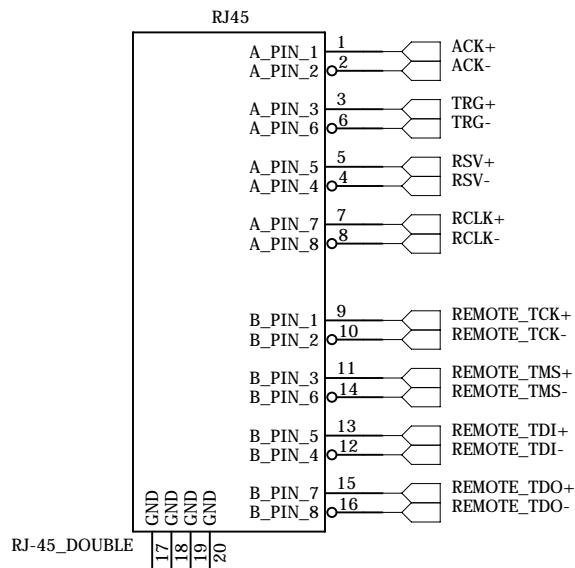
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4

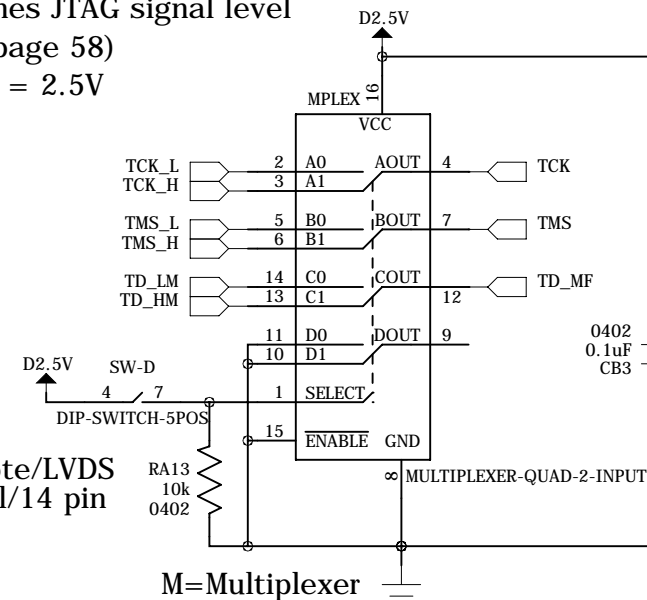
3

2

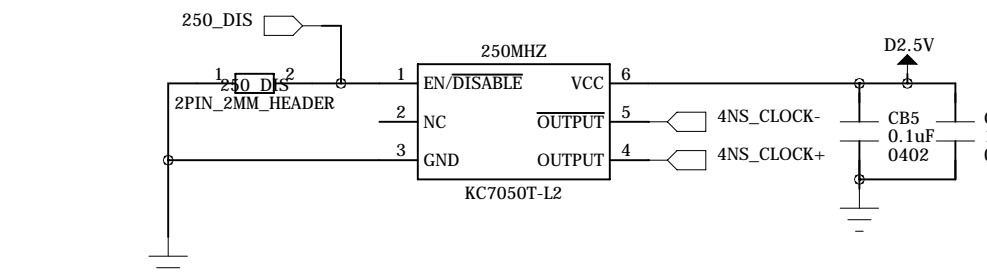
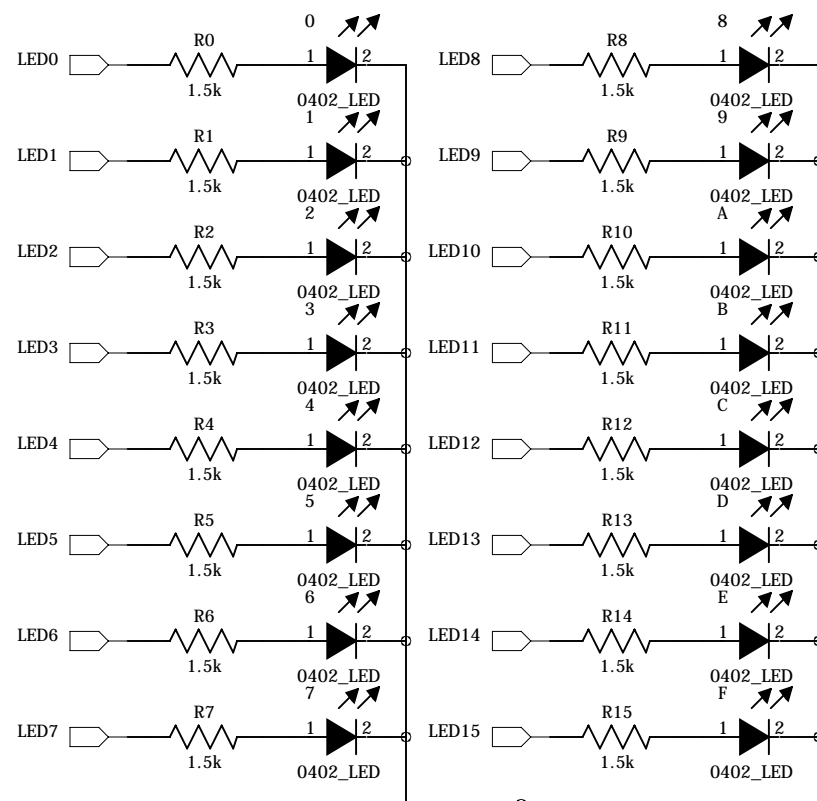
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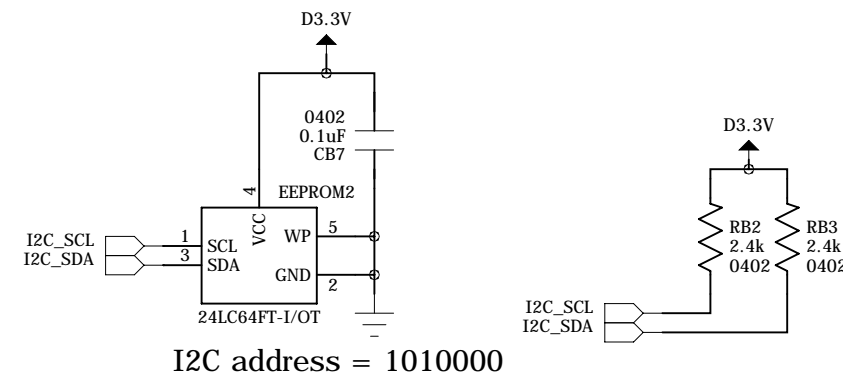
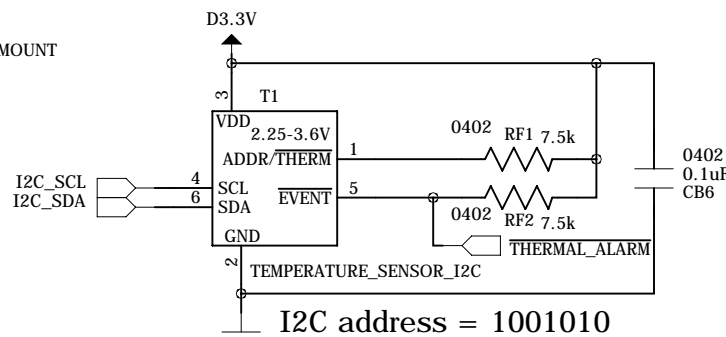
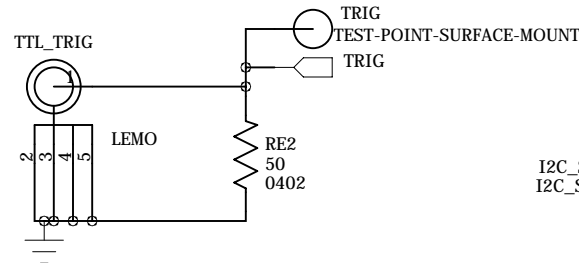
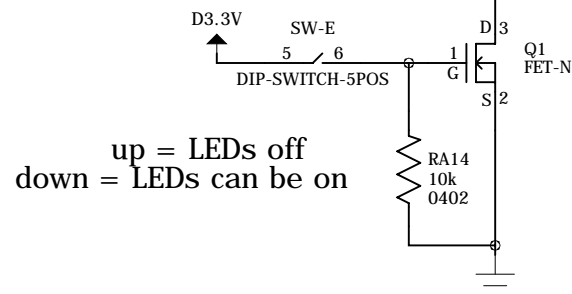
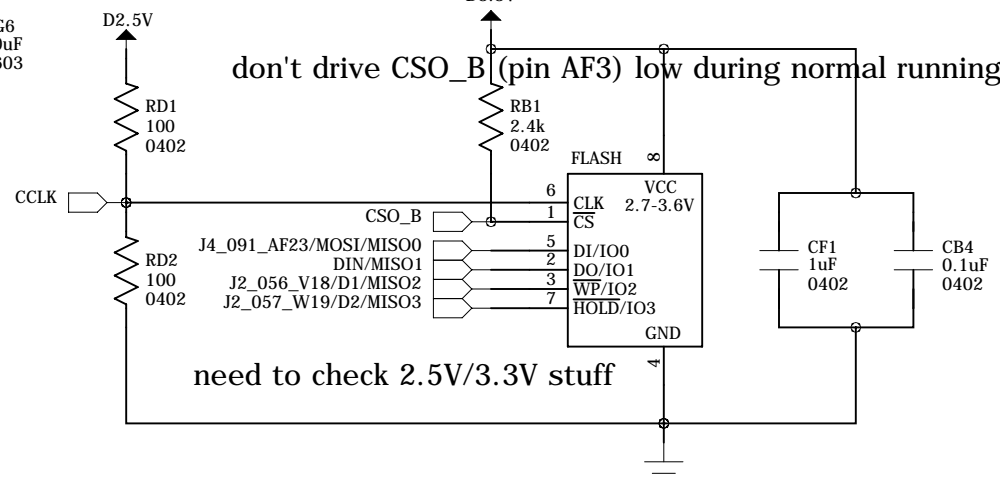
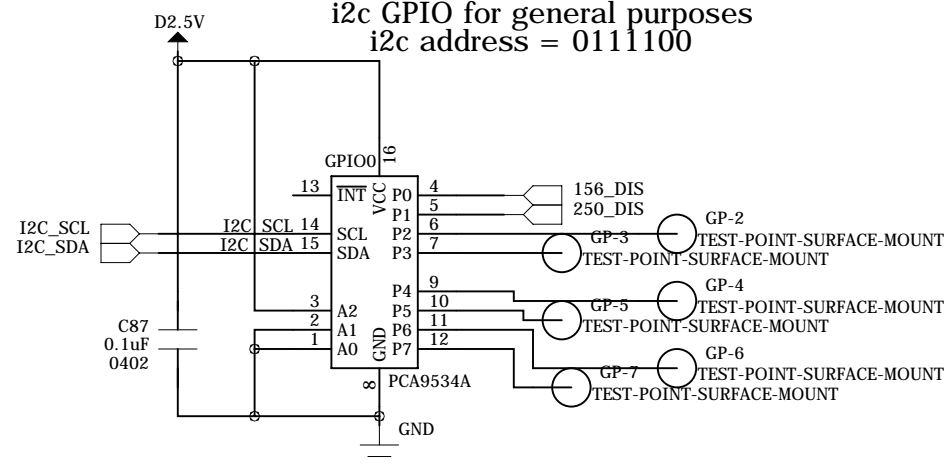
VCCAUX determines JTAG signal level
(ug380 page 58)
VCCAUX = 2.5V



up = remote/LVDS
down = local/14 pin



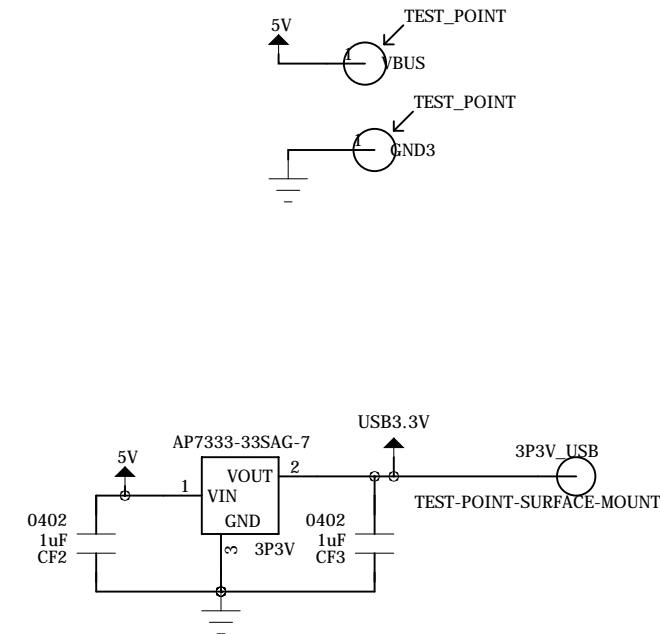
i2c GPIO for general purposes
i2c address = 0111100



institution: University of Hawai'i at Manoa
High Energy Physics Group
Instrumentation Development Lab

title: SCROD
revision: A3
IDLAB design #: IDL_13_037
circuit design: LJR, MZA, GSV, KAN
PCB design: LJR, MZA

sheet #: 6 of 7
sheet description: JTAG, CLOCK, LEDs, PROM
date last modified: 2013-08-27



institution:	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Lab
title:	SCROD
revision:	A3
IDLAB design #:	IDL_13_037
circuit design:	LJR, MZA, GSV, KAN
PCB design:	LJR, MZA
sheet #:	7 of 7
sheet description:	Cypress FX2LP USB
date last modified:	2013-08-27