0.1 Design Goals and Rules

The development of the Common RISC Platform is informed and directed by these goals and rules, in the hopes that something useful will come of it at the end of the day.

- The user must, at all times, retain **full control** over their system

- The platform must make use of established free and open industry standards wherever possible

- The platform must be flexible and extensible to meet the computing needs of the mid-term future

- Anyone should be able to develop and produce a CRP-compliant system

- Any CRP system must be able to boot a fully vanilla Linux kernel with no proprietary firmware blobs either in the kernel or in the platform firmware

- All CRP hardware should be as interoperable and cross-compatible as possible

- The platform should be suitable as a replacement for the PC in all form factors

- The platform design should be as simple as humanly possible

- Software targeted at the platform must be able to run on all systems with no errors (i.e. a common ISA)