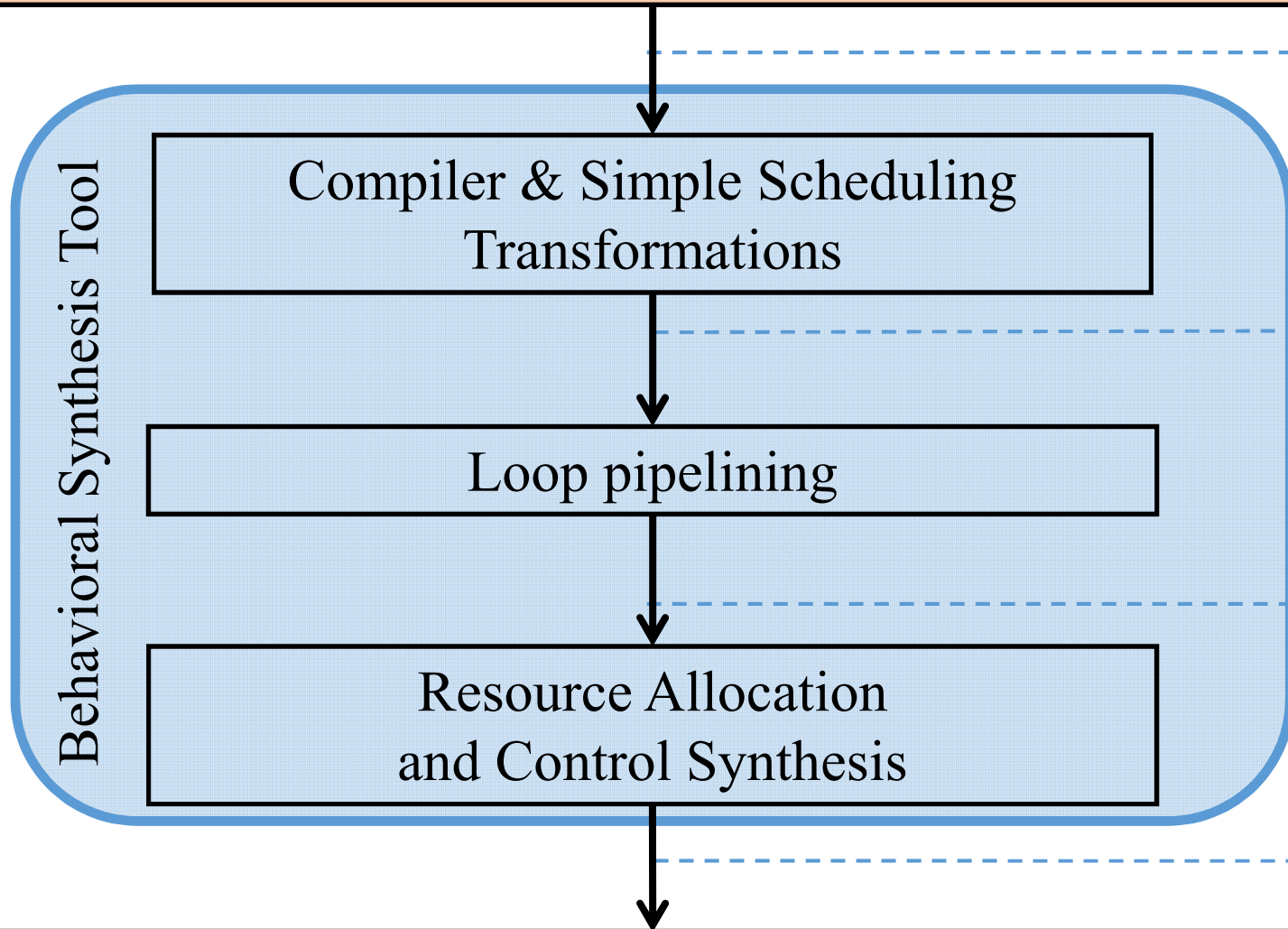


High level specification of hardware design
(in C, C++, System C)



Implementation in hardware description language
(in VHDL, Verilog)

