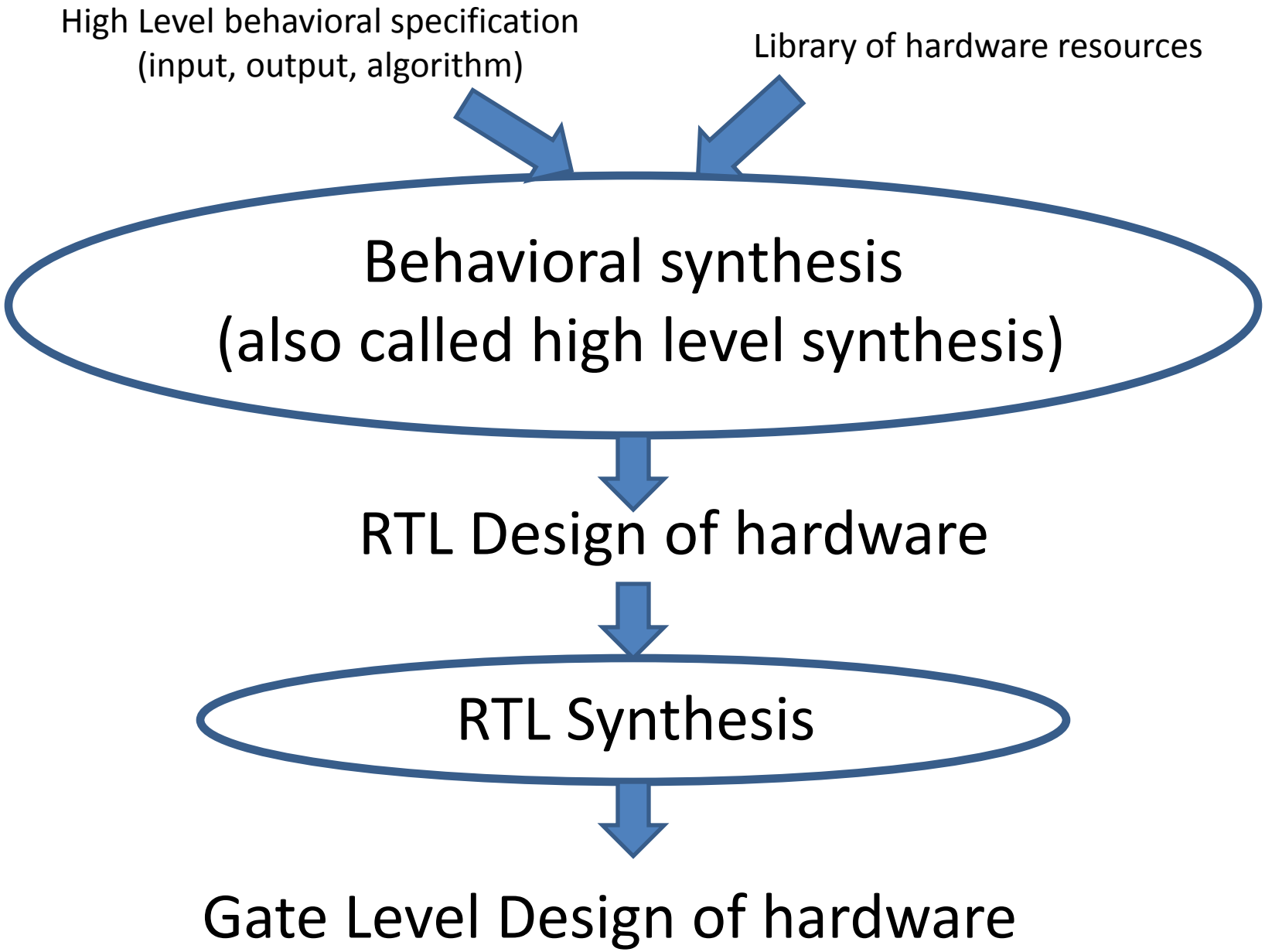
High Level behavioral specification
(input, output, algorithm)
Library of hardware resources
Behavioral synthesis
(also called high level synthesis)
RTL Design of hardware
RTL Synthesis
Gate Level Design of hardware