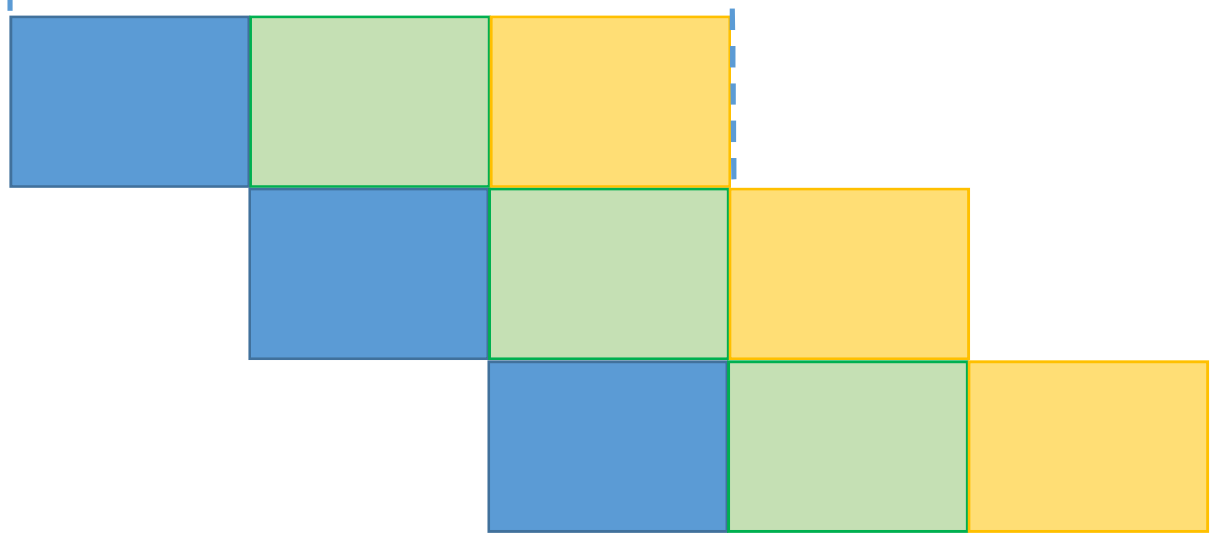


Execution order before pipelining (3 iterations take 9 clock cycles)



Execution order after pipelining (3 iterations take 5 clock cycles)

“ Pipeline interval is 1”