MOHAN RAO DIVATE KODANDARAMA

2110 University Ave, Apt 309,

Madison, WI 53726-2301

Contact: +16087339899

Email : mohanr318@gmail.com / divatekodand@wisc.edu LinkedIn: https://www.linkedin.com/in/mohandivate

Website: divatekodand.github.io

Github : https://github.com/divatekodand

OBJECTIVE

Graduate student in Computer Science looking for a full-time position in the field of **Software Engineering/ Machine Learning/Computer Vision** starting **May 2020**.

EDUCATION

• University of Wisconsin-Madison

M.S. in Computer Science, GPA: 3.93/4.0

Madison, WI May 2020

Coursework: CS838 Computer Vision, CS861 Theoretical ML, CS760 Machine Learning, CS726 Non-linear Optimization, CS761 Mathematical Foundations of ML (Audit), CS744 Big Data Systems, CS537 Operating Systems

National Institute of Technology Karnataka, Surathkal

Surathkal, India

B.Tech in Electronics and Communication Engineering, GPA: 9.11/10

Graduated May 2016

Activities: TA for Engineering Mathematics II, Peer Mentor for MATLAB Programming.

Relevant Coursework:

Computer Programming, Digital Signal Processing, Data Structures and Algorithms, Application of Signal Processing to Image and Video (CT image reconstruction), Digital Processing of Speech and Audio Signals, Linear Algebra and Probability.

WORK EXPERIENCE

PERCEPTION (COMPUTER VISION) INTERN AT ARGO AI

JUNE 2019-AUG 2019

- Developed a Deep Learning based 3D object detection pipeline for Autonomous Vehicle using LiDAR data.
- Developed Graph Convolution based point-cloud features to improve detection of objects far from the AV's.
- Improved the performance of proposed detection algorithm on rare classes and reduced false positive detection rate.
- Proposed an objective function to mitigate the effects of correlated point-cloud sweeps (non-independently and identically distributed data) on detection algorithm.
- Propose a new evaluation metric for the task of object detection in AV's which captures the types of errors made by the detection algorithm.

STUDENT RESEARCHER AT UW-MADISON

SEPT 2018 - PRESENT

Projects worked on -

Automated Analysis of 3D CT Images of Brain using Deep Learning

SEPT 2018 - DEC 2019

- Developed and Implemented Deep Learning Models for detecting Intracranial Hemorrhage and its subtypes using 3D CT images of Brain.
- The Proposed architecture was capable of utilizing any form of CT data (Labeled, Weakly labeled or unlabeled).

SerFer: Serverless Inference

FEB 2019 - MAY 2019

SerFer is a distributed framework for serving Deep Learning queries built on top of a Serverless Computing Platform
(AWS Lambda). It distributes the computation across several Lambda's by splitting both the Deep Learning Model, as
well as the input query.

Robust 3D Object Detection for Autonomous Vehicles using Sensor Fusion

OCT 2019 - DEC 2019

• Explored the viability of augmenting the true point cloud with pseudo point-cloud data generated form monocular images. In particular, we explored if such an augmentation technique can aid 3D Object Detection.

FIRMWARE ENGINEER AT SANDISK, A WESTERN DIGITAL BRAND

JUL 2016 – AUG 2018

 Designed, Developed and Validated following Firmware modules -Garbage Collection/Memory Compaction, Error Correction, Address Translation and Internal File System for USB Flash Drive products.

INTERN AT NATIONAL AEROSPACE LABORATORIES

MAY – JUL 2015

Projects worked on:

- <u>Active Noise Control</u>: Implemented FxLMS Algorithm on a microcontroller (based on TI TMS320C6748) unit which controlled noise control headphones.
- <u>Digital Video Watermarking for metadata embedding:</u> Developed and implemented an algorithm for embedding metadata directly in compressed MPEG-4 Video bit-stream (MATLAB).

SKILLS

- Programming Languages: C/C++, Python, MATLAB
- Tools / Platform: Tensorflow , Pytorch, GIT, Accurev, OpenCV, scikit-learn
- Other Skills: Object Oriented Design, Machine Learning (CNN's, RNN's, GAN's, and VAE), Big Data Systems (Hadoop, Spark, Spark Streaming, Spark SQL, GraphX), AWS, Docker, SQL

^{*}Code and Report for the projects available on the website: divatekodand.github.io

Reconfigurable Architecture for face Detection

AUG 15 - APR 16

- Developed a custom Face Detection Model suitable for Hardware Implementation using Viola-Jones Face Detection Framework. This model was trained and validated on MATLAB.
- Designed and Developed Hardware accelerator for Face Detection using Xilinx Zedboard.
- Languages and Tools Used Xilinx Vivado Design Suite, C++, VHDL, and OpenCV.