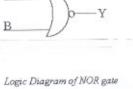
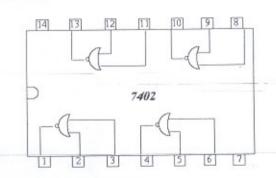
Activity-5. Implementation of Logic NOR Gate using 10 /402





Pin-out Diagram of IC 7402

Observations