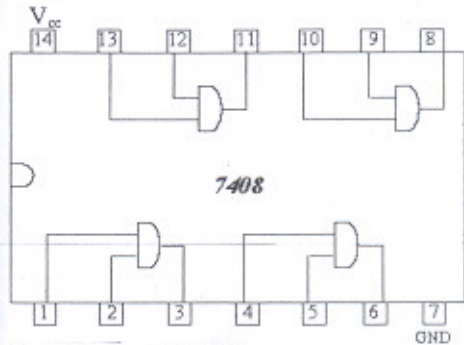


*Activity-1.* Implementation of Logic AND Gate using IC 7408



*Logic Diagram of AND gate*



*Pin-out Diagram of IC 7408*

*Observations*