

CPEN 400Q Lecture 19

Intro to quantum error correction

Wednesday 19 March 2025

Announcements

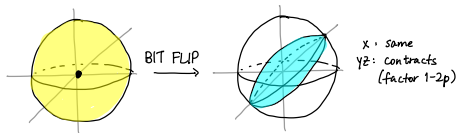
- Quiz 9 Monday
- Sign up for project presentations, and final oral interview (Canvas calendar)
- A3 due Tuesday 25 March 23:59

Last time

We expressed a noisy processes as quantum channels.

Bit flip channel

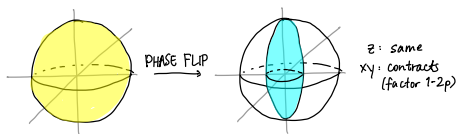
$$\Phi(\rho) = (1-p)\rho + pX\rho X$$



$$|0\rangle \leftrightarrow |1\rangle$$

Phase flip channel

$$\Phi(\rho) = (1-p)\rho + pZ\rho Z$$

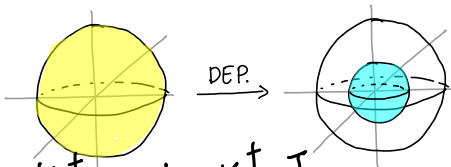


$$|+\rangle \leftrightarrow |-\rangle$$

Last time

Depolarizing channel

$$\Phi(\rho) = (1-p)\rho + \frac{p}{3}X\rho X + \frac{p}{3}Y\rho Y + \frac{p}{3}Z\rho Z$$



$$\Phi(\rho) = \sum_i K_i \rho K_i^\dagger \quad \sum_i K_i K_i^\dagger = I$$

Amplitude damping channel

$$K_0 = \begin{pmatrix} 1 & 0 \\ 0 & \sqrt{1-p} \end{pmatrix}$$

$$K_1 = \begin{pmatrix} 0 & \sqrt{p} \\ 0 & 0 \end{pmatrix}$$

$\hookrightarrow \sqrt{p} |0\rangle\langle 1|$

Last time

We compared density matrices using two distance measures.

Trace distance

$$T(\rho, \sigma) = \frac{1}{2} \|\rho - \sigma\|_1 = \frac{1}{2} \text{Tr} \sqrt{(\rho - \sigma)^\dagger (\rho - \sigma)}$$

Fidelity

$$F(\rho, \sigma) = \left[\text{Tr} \sqrt{\sqrt{\rho} \sigma \sqrt{\rho}} \right]^2$$

We simulated these processes and computed distance measures using PennyLane's ‘‘default.mixed’’ device.

Learning outcomes

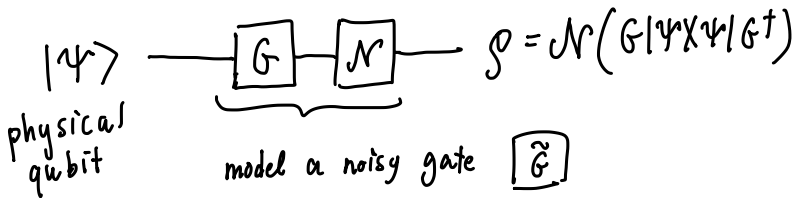
Module 5 (NEW!): Introduction to quantum error correction

Today:

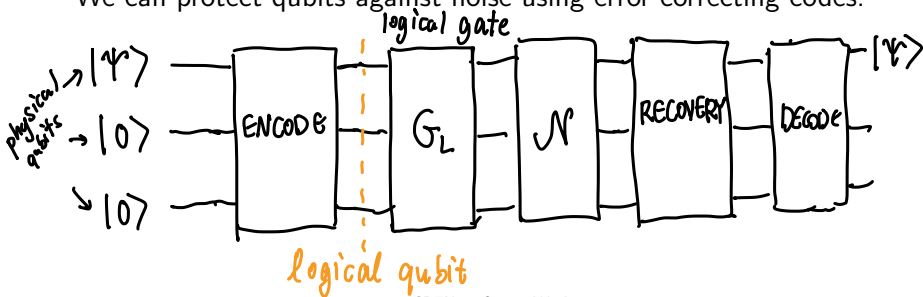
- ➊ Design circuits to correct bit flips using a simple quantum error correcting code (repetition code)
- ➋ Apply logical operations to encoded qubits
- ➌ Correct bit and phase flip errors with the 9-qubit code

Quantum error correction

Our current picture of noise:



We can protect qubits against noise using error correcting codes.



Repetition codes

Imagine sending a bit string through a classical channel that flips each bit (individually) with probability p .

$$\phi \left(\begin{matrix} 0 & 0 & 0 & 1 & 1 \end{matrix} \right) \rightarrow \begin{matrix} 0 & 0 & 1 & 1 & 1 \end{matrix}$$

Idea: add redundant information to enable *detection* and *correction* of bit flips.

Define *encoding* and *decoding* operations, \mathcal{E} and \mathcal{D} ,

$$\begin{aligned} \mathcal{E}(0) &\rightarrow 000 && \text{"logical 0"} \\ \mathcal{E}(1) &\rightarrow 111 && \text{"logical 1"} \end{aligned}$$

$$\mathcal{D}(000) \rightarrow 0$$

$$\mathcal{D}(111) \rightarrow 1$$

$$\begin{matrix} 0 & 0 & 0 & 1 & 1 & \rightarrow & \underbrace{000} & 000 & 000 & 111 & 111 \\ & & & & & & 001 & & & \downarrow & \\ & & & & & & & & & 101 & \end{matrix}$$

Repetition codes

Devise a procedure, \mathcal{R} , to recover from an error: majority voting.

$$\mathcal{R}(b_0, b_1, b_2) = \text{MAJ}(b_0, b_1, b_2)$$

Operation	Outcome
	0 0 1 0 1
\mathcal{E}	000 000 111 000 111
ϕ	<u>010</u> 000 <u>110</u> 000 111
\mathcal{R}	000 000 111 000 111
\mathcal{D}	0 0 1 0 1

Is this better? When will this fail? \rightarrow 2 bit flips!

$$p_{\text{error}} = 3 p^2 (1-p) + p^3 \rightarrow p_e < p_{\text{original}}$$

(two flips) (all three flipped)

000
↓
101
↓
111
↓
1

!!

Quantum repetition code (bit flip code)

Idea: $\mathcal{E}(|\psi\rangle) \Rightarrow |\psi\rangle^{\otimes 3}$

Why won't this work?

- No CLONING!!!
- Measuring to compare them destroys them
- More than one type of errors can happen

Alternative: quantum-specific methods!

$$\mathcal{E}(|0\rangle) = |000\rangle = |0\rangle_L$$

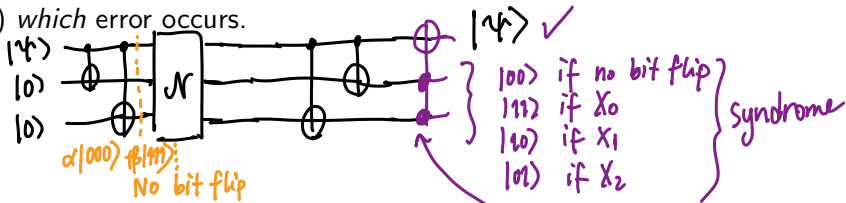
$$\mathcal{E}(|1\rangle) = |111\rangle = |1\rangle_L$$

(b)

$$(\alpha|0\rangle + \beta|1\rangle) |00\rangle = \alpha|000\rangle + \beta|100\rangle$$

Bit flip code: recovery operation

Let's design a circuit to detect (a) *whether* an error occurs, and (b) *which* error occurs.



$\alpha|000\rangle + \beta|111\rangle$
No bit flip
or
one bit flip

$$I: \alpha|000\rangle + \beta|111\rangle$$

$$X_0: \alpha|100\rangle + \beta|011\rangle$$

$$X_1: \alpha|010\rangle + \beta|101\rangle$$

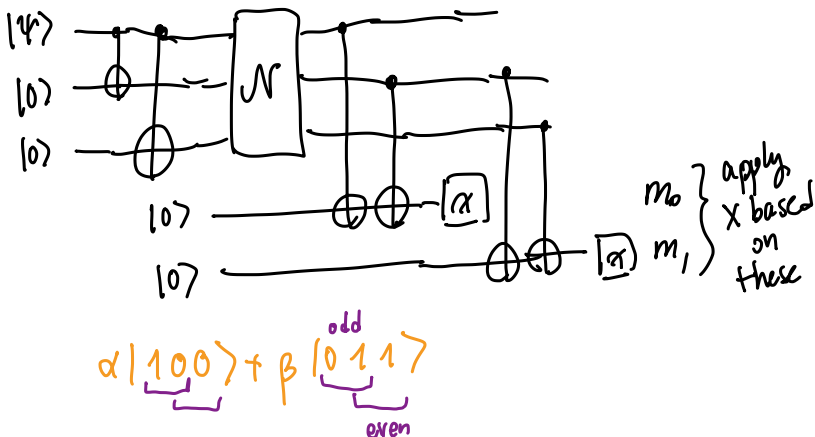
$$X_2: \alpha|001\rangle + \beta|110\rangle$$

detect

$$\begin{aligned} \alpha|000\rangle + \beta|100\rangle &= (\alpha|0\rangle + \beta|1\rangle)|00\rangle \checkmark \\ \alpha|111\rangle + \beta|011\rangle &= (\alpha|1\rangle + \beta|0\rangle)|11\rangle \times \text{Apply } X \\ \alpha|010\rangle + \beta|110\rangle &= (\alpha|0\rangle + \beta|1\rangle)|10\rangle \checkmark \\ \alpha|001\rangle + \beta|101\rangle &= (\alpha|0\rangle + \beta|1\rangle)|01\rangle \checkmark \end{aligned}$$

Bit flip code: recovery operation

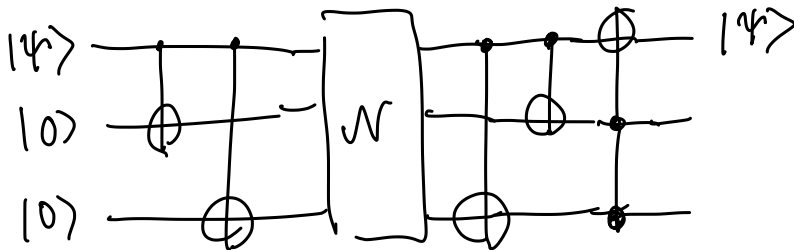
Let's design a circuit to detect (a) *whether* an error occurs, and (b) *which* error occurs.



Bit flip code: recovery operation

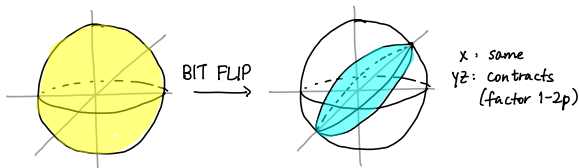
Design a circuit to *recover* the state of the first qubit

Bit flip code: full circuit for bit flip code



Bit flip code: analysis

Imagine any possible state that can go through the channel:



Exercise (from last time): Determine $F(|\psi\rangle, \Phi(|\psi\rangle \langle\psi|))$ where Φ is the *bit flip channel* with parameter p .

$$\begin{aligned}
 F(|\psi\rangle, \rho) &= \langle\psi|\rho|\psi\rangle \\
 &= \langle\psi|((1-p)|\psi\rangle\langle\psi| + p|X\psi\rangle\langle X\psi|)|\psi\rangle \\
 &= (1-p)\langle\psi|\psi\rangle + p\underbrace{\langle\psi|X|\psi\rangle^2}_{\langle x \rangle^2} \\
 &= 1 - p + p\langle x \rangle^2 \geq 1 - p
 \end{aligned}$$

Bit flip code: analysis

Compare overall fidelity of the state *after the error*, vs. fidelity *after recovery*.

After the error

$$F \geq 1 - p$$

After recovery, have the state

Mixed state :

$$(1-p)^3 \overset{\substack{\uparrow \\ \text{no error}}}{| \psi \rangle \langle \psi |} + 3p(1-p)^2 | \psi \rangle \langle \psi | + \text{other stuff}$$

$$F(|\psi\rangle, \downarrow) \geq (1-p)^3 + 3p(1-p)^2$$

$p < \frac{1}{2} \Rightarrow$ better to do correction

Designing logical operations

Logical operations are specific to codes.

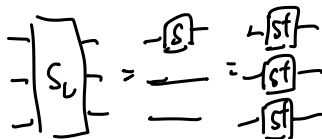
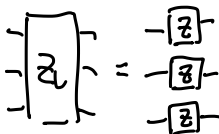
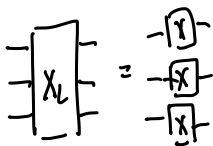
They should act on the logical states $|0\rangle_L$, $|1\rangle_L$ the same way the physical operations act.

Exercise: design circuits for logical X , Z , H , S , and $CNOT$.

$$X: \alpha|0\rangle + \beta|1\rangle \rightarrow \alpha|1\rangle + \beta|0\rangle$$

$$X_L: \alpha|0\rangle_L + \beta|1\rangle_L \rightarrow \alpha|1\rangle_L + \beta|0\rangle_L$$

$$S^3 = S^\dagger$$



Phase flip errors

With our encoding

★ Start here on
Monday.

and appropriate circuitry, we can correct *bit flip errors* but not phase flip errors.

Phase flip code: encoding circuit

Main idea: make phase flip errors “look like” bit flip errors.

Shor code

To correct a combination of one bit flip and/or phase flip error, we can *concatenate* codes: use logical qubit of a phase flip code as the “physical” qubits in a bit flip code.

Next time

Next class:

- Properties of errors and error correcting codes
- Stabilizers and stabilizer codes

Action items:

- ➊ A3 (due 25 March 23:59)
- ➋ Work on project

Recommended reading:

- From this class: Codebook EC; N&C 10.1-10.2
- For next class: Codebook EC; 10.3, 10.5