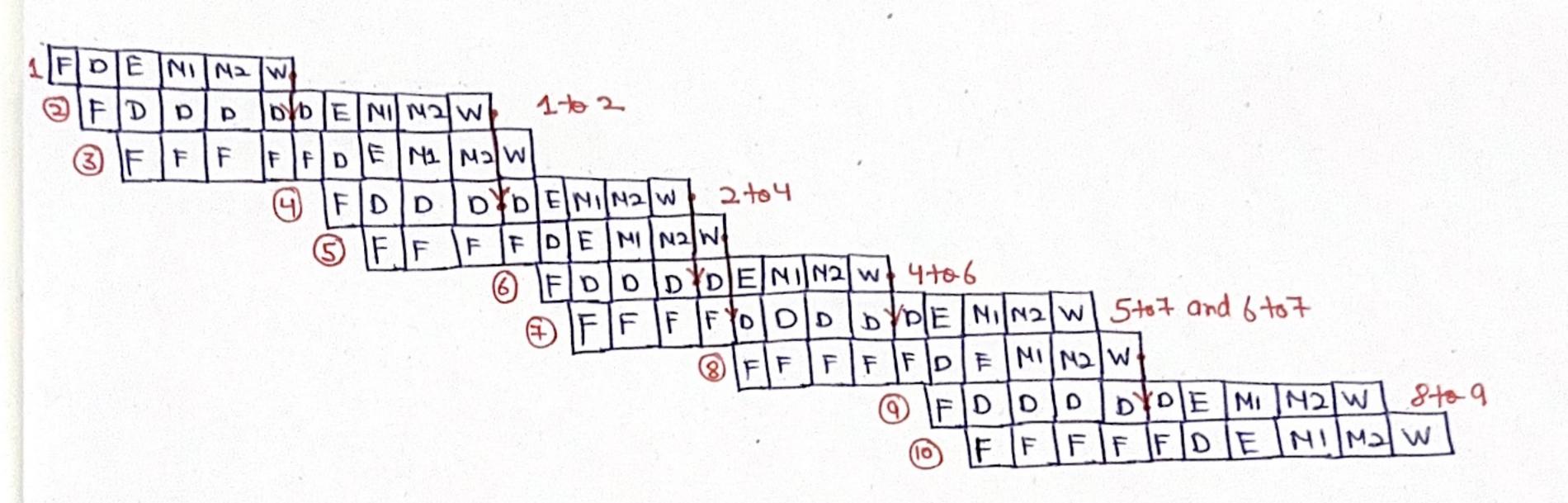
ASSIGNMENT 2 - DIVYANK SHARMA

Checking Dependencies:					
1	lw	\$912	,	0 (\$45)	
2	Subi	\$ × 4	,	Sie 2	, 1
3	lw	144	,	Q(\$×6)	
4	add	\$ 913	,	\$210	3 \$ 91 Y
5	idus	£ н 2		\$46	, 1
6	Subi	\$44	1		, 5
7	add	\$43	7	\$42	× 944
8	lw	\$42	>	(fx2)0	
٩	04	424	9	1 \$912	142,
10	subi	F12	,	\$912 \$913	, 9

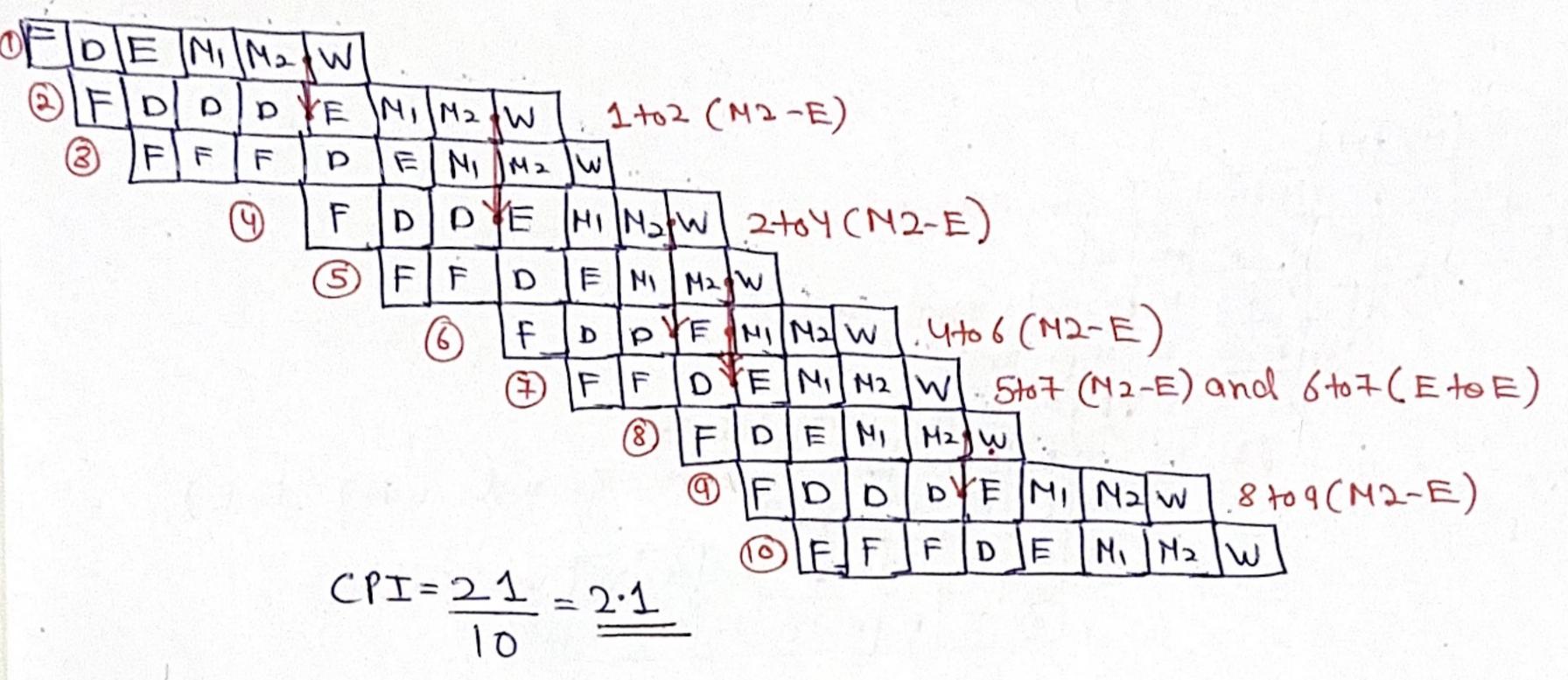
Auchitecture 2 - Only stalling, no forwarding pipelines =



$$CPI = No of cycles = \frac{33}{10} = \frac{3.3}{10}$$
Total Installations

- @ Ferom 1 to2
- 2) Ferom 2 to 4
- 3 Ferom 4 to 6
- 9 Forom 5to7 and 6to7
- (5) ‡ sion 8 to 9

ASIZ. M2-E, E-E, W-D, ALU can fetch at E, M2, W Lu/swaan fetch at M2, W



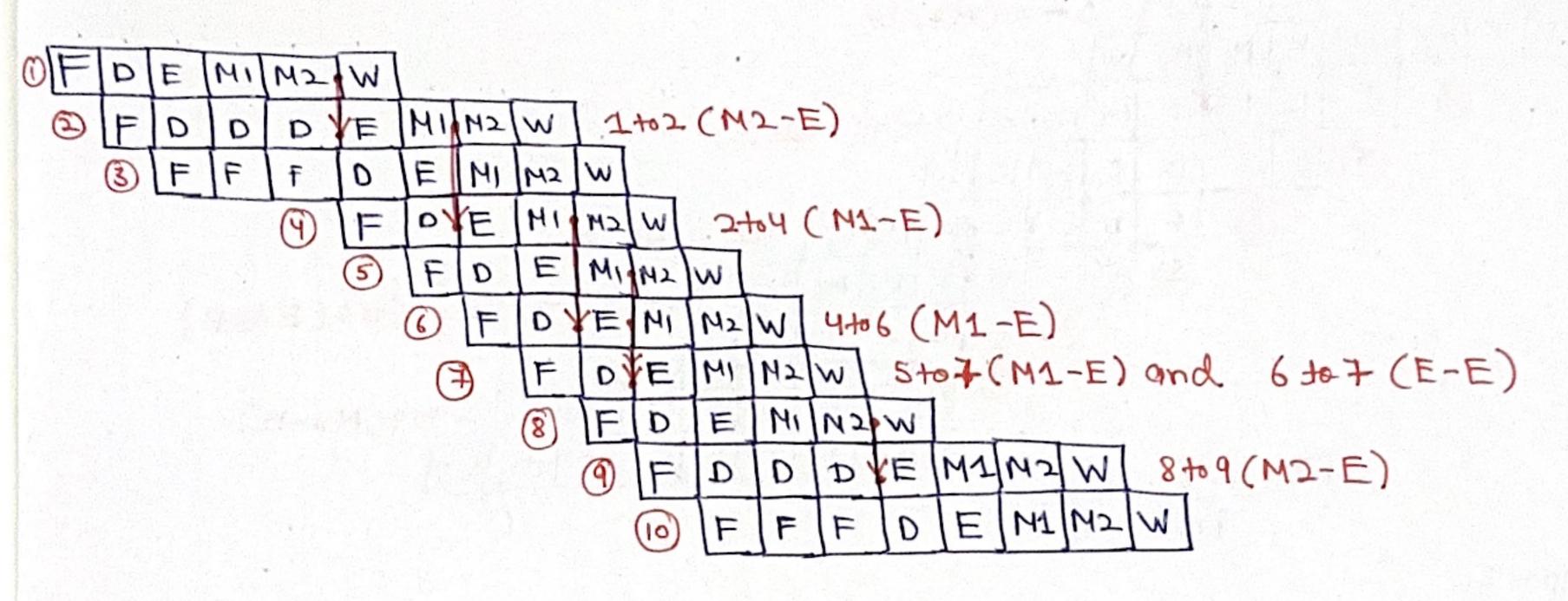
Steps :

- 1.] FOR 2nd instruction it has dependency on 1st. and 1st is (lw). So the output will be ready to read after M_2 . So we used M_2-E .
- 2.] For 4th, depends on 2nd and 2nd is an ALU instruction, steady to be stead after E, N1, N2, W but can only use E-E, N2-E, W-D. So we used M2-E.
- 3.] 6th, depends on 4th (an ALU instruction) can be read at E, M2, W as per rules of this architecture. So we con't use Eto E so we used M2-E.
- 4.] Ith, depends on 5th and 6th, both one ALU can be seed at E, M2, W.
 But we need to seed them at some cycle. For 6th, E to E and 5th, M2-E.
- 5.] 9th, depends on 8th and 3rd. But 3rd abready Binished so no dependency For 8th its IU so we can read at M2 OH W. so M2-E.
- 6] 10th, depends on 7th (ALU). We can fetch at W but as we can see Wis finished and them D stants so the values will be seedy to be shead already by 10th instruction. (W-D)

Asychitecture 4

ALUcan fetch at E, M1, M2, W LW/SW can betch at M2, W

ETE, MITE, MITE, WID



$$CPI = 19 = 1.9$$

Steps >

- 1.] FOR 2nd it depends on 1st ie... Iw so we can stead at M2 or W. Herewe used M2-E.
- 2.] For 4th, depends on 2nd (ALU) we can seed at MI, MI, E, W. So based is MI-E.
- 3.] For 6th, depends on 4th (ALU) We can read at M1-E.
- 4.] For 7th, depends on 5th and 6th both ALU. We can seed at E,N1,N2,W
 forom 5th we can seed at M1-E and for 6th E-E.
- 5th] For 9th, depends on 8th and 3rd. as 3rd already finished so only on 8th (lw). So for lu the instructions can be read at M2, W. So we read at M2-E.
- 6th] For 10th, depends on 7th. (ALU) as 7th ends and D startsfor 10th so the values can be fetched as they will be sready before D. (W-D)