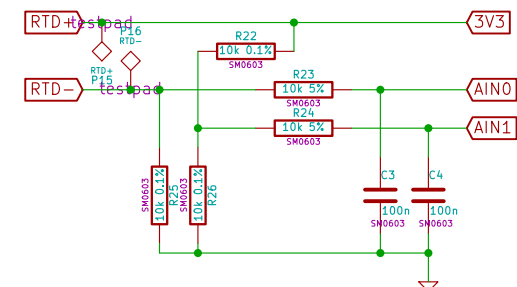
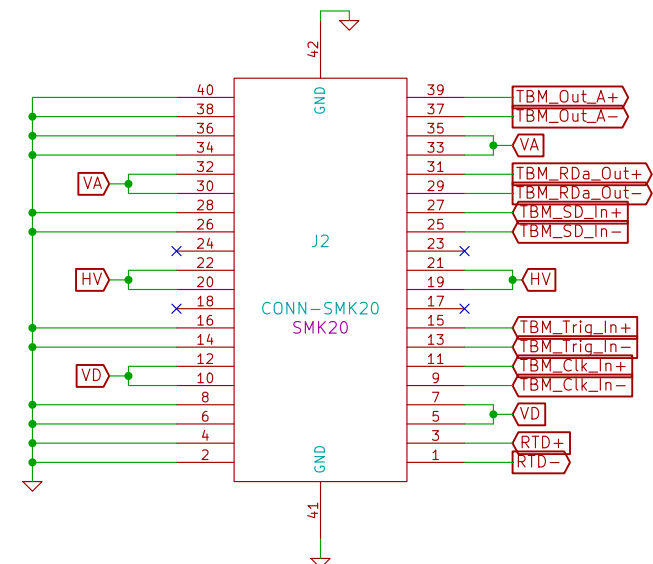
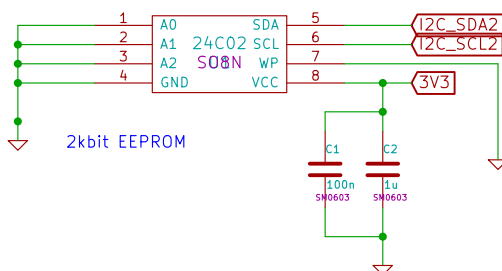
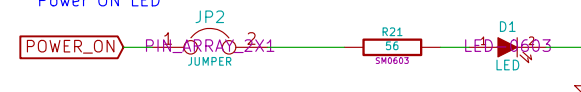


The R with values of 10G are placeholders and will not be put on the board. This allows to have the solder points available for future needs to allow for impedance matching to cables with higher impedance.

#### RTD network



#### Power ON LED



F. Meier		
University of Nebraska, Lincoln		
File: FPix2DTB_90deg_up.sch		
Sheet: /		
Title: Adaptercard DTB to FPix module		
Size: A4	Date: 1 may 2014	Rev: 2.0
KiCad E.D.A.		Id: 1/1