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A

A

B

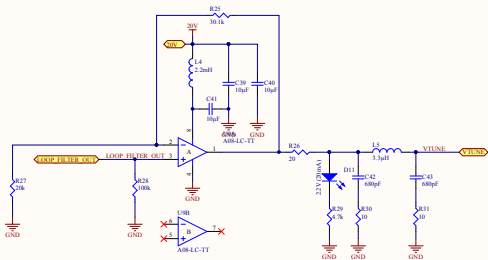
B

C

C

D

D



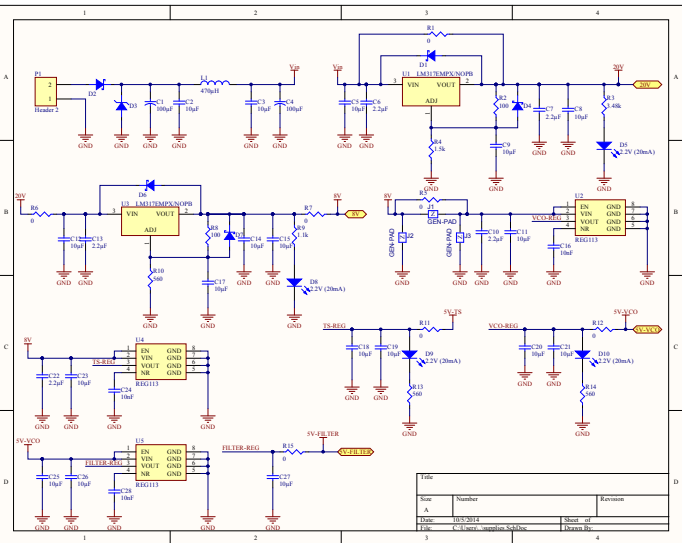
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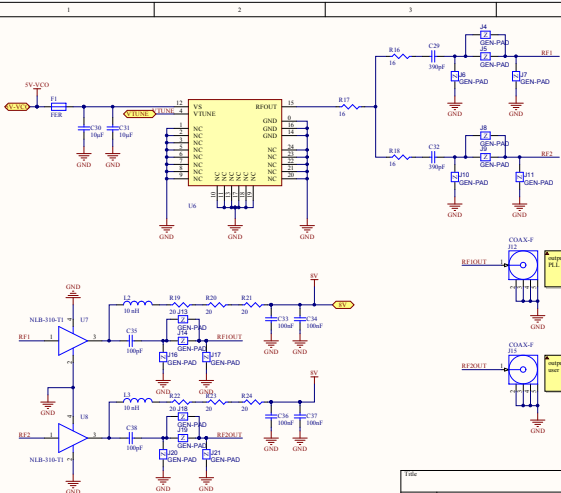
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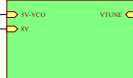


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Voltage Regulators
analog SchDwg



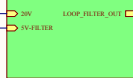
VCO and Buffer
analog SchDwg



OpAmp
analog SchDwg



Active/Passive PLL Filters
analog SchDwg



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