





Audio

The diagram illustrates the electrical connections for an audio module centered around the TLV320AIC3107 integrated circuit. The IC is shown with its pinout, including control pins (RESETN, SCL, SDA), clock pins (MCLK, BCLK), data pins (DIN, DOUT), and power pins (AVDD, DVDD, AVSS, DVSS). The circuit includes a +3V3 supply connected to the IC's AVDD and DVDD pins, and a +5V supply connected to the L2 inductor. A speaker (LS1) is connected to the output pins (HPL, HPR) through a network of resistors (R11, R12, R13, R14) and capacitors (C18, C19). The I2C interface is connected to the SCL and SDA pins. Various passive components, including resistors (R8, R9, R10, R11, R12, R13, R14, R15, R16, R17) and capacitors (C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15, C16, C17, C18, C19), are used for filtering and impedance matching. The ground connections are labeled AGND and DGND.

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