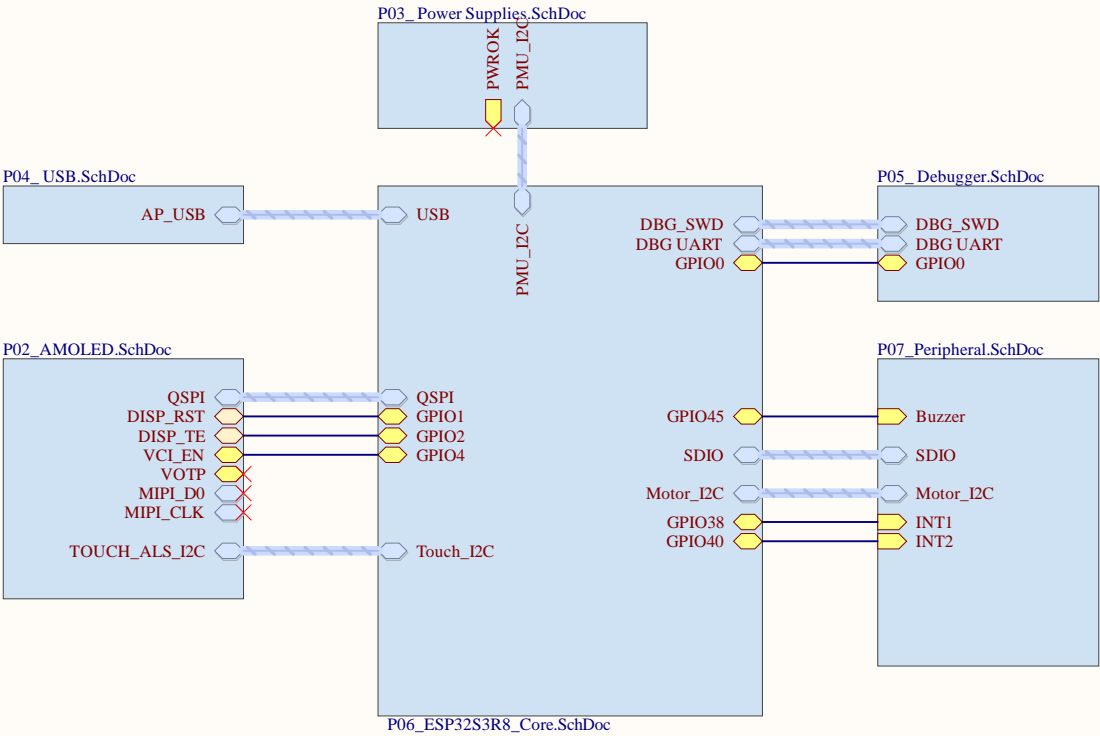



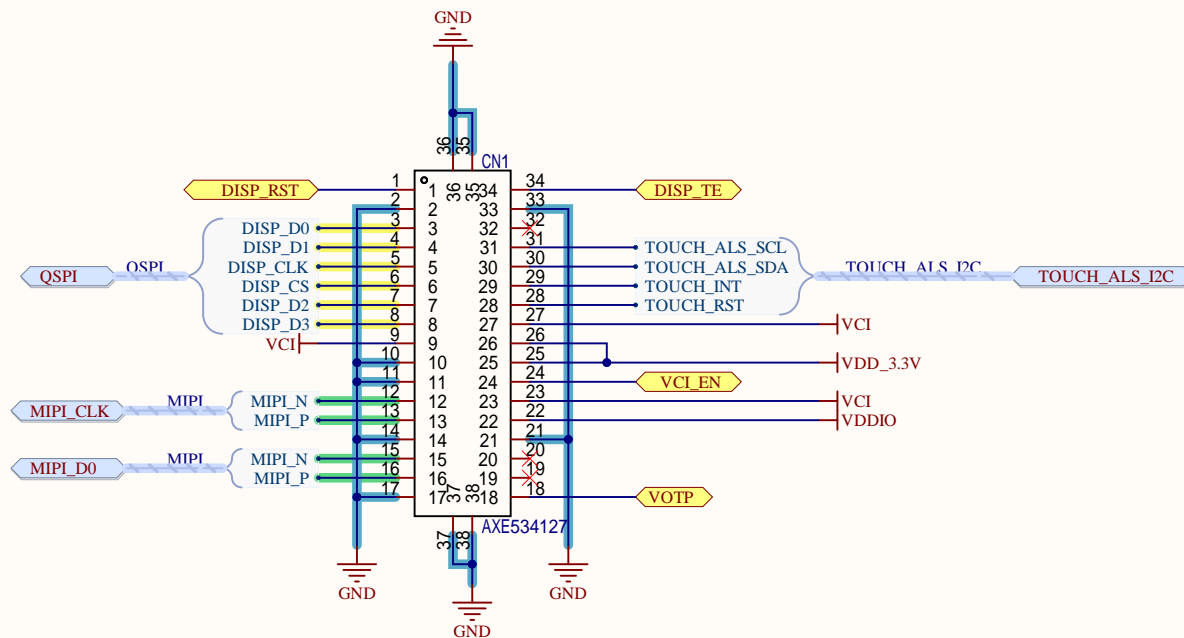
ESP32S3_Watch Diagram





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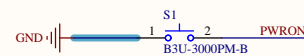
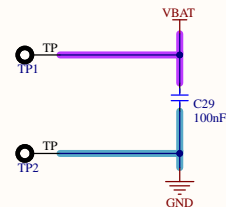
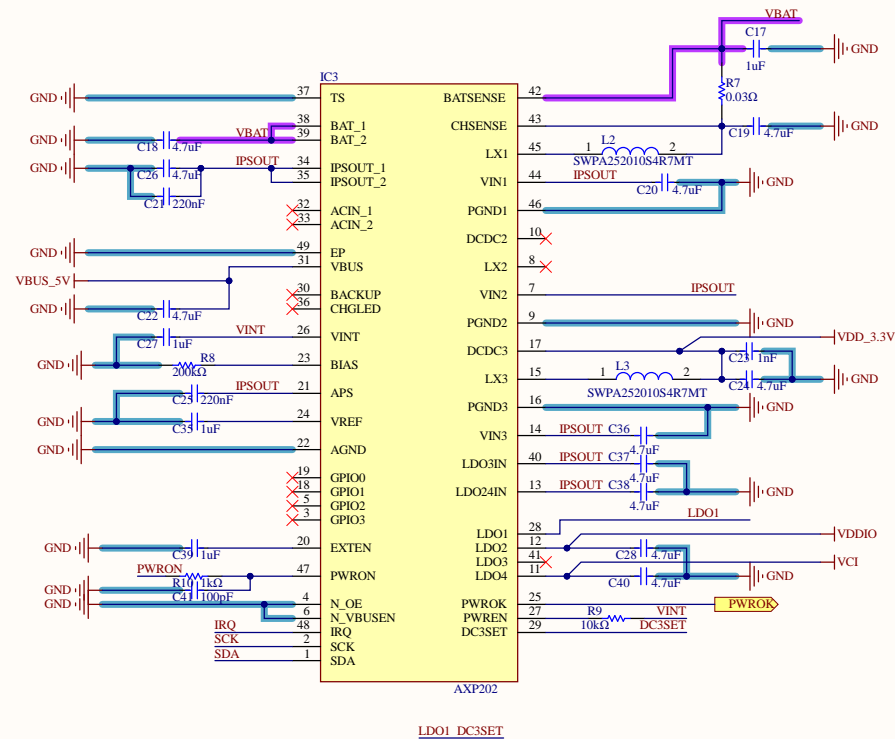
Title		AMA4B_Adapter_Board		Revision	1.0
Sheet Name		P01_Top Level		Sheet 1 of 7	
Date	2024/6/19	Drawn By	Tomas	2024/6/19	
Number	Apollo_4B	Aprvd By	----	2024/7/1	




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Title		Revision	
AMA4B_Adapter_Board		1.0	
Sheet Name		Sheet 2 of 7	
P02_AMOLED			
Date	2024/6/14	Drawn By	Tomas
Number	Apollo_4B	Aprvd By	----
		2024/6/14	
		2024/7/1	

PMU

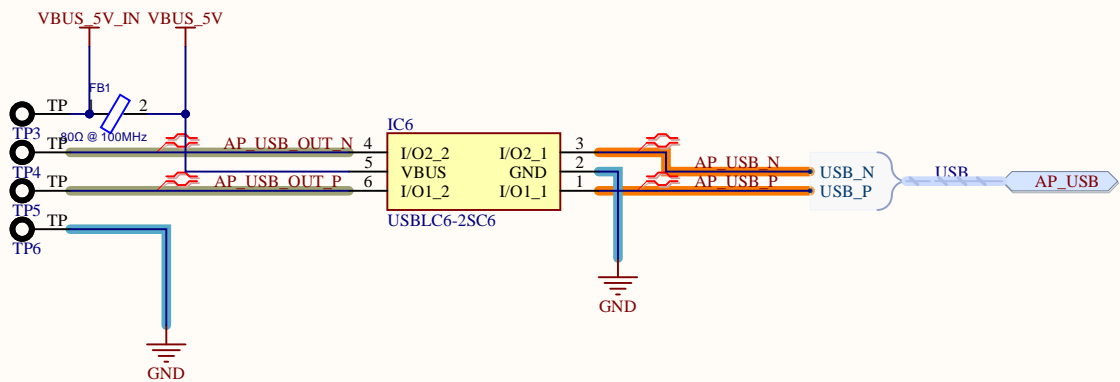





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Title: AMA4B_Adapter_Board		Revision: 1.0
Sheet Name: P03_Power Supplies		Sheet 3 of 7
Date: 2024/6/26	Drawn By: Tormas	2024/6/26
Number: Apollo_4B	Aprvd By: ----	2024/7/1

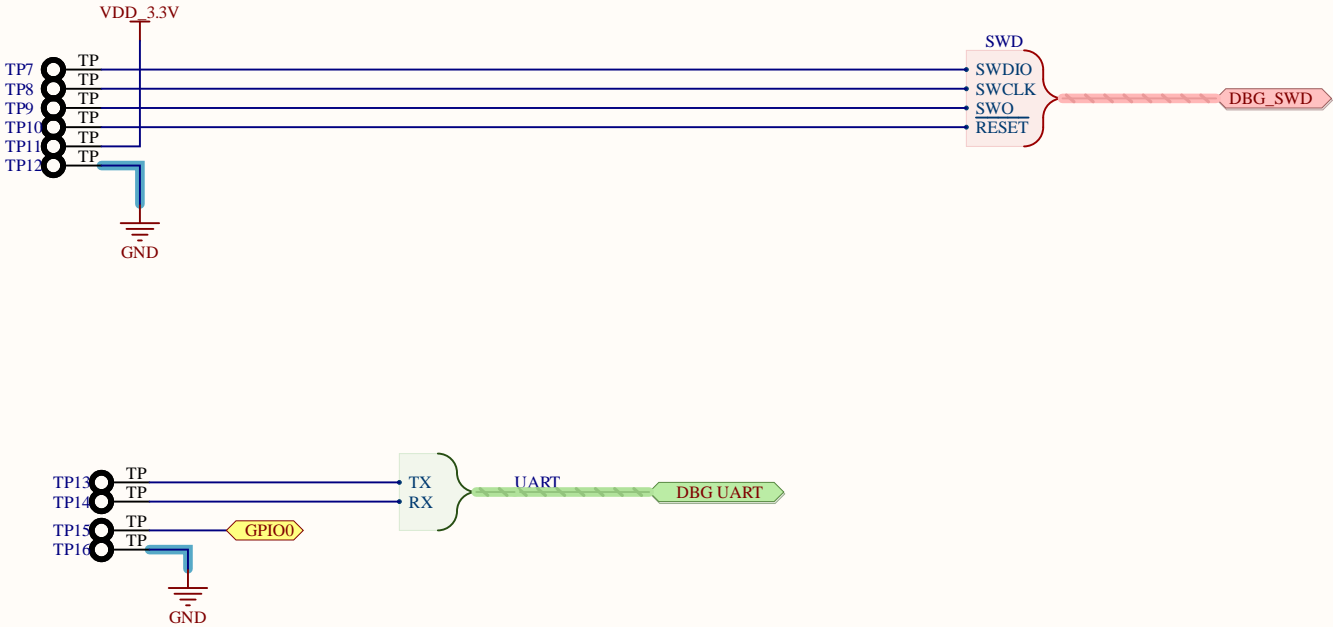
USB Connector




晟合微电

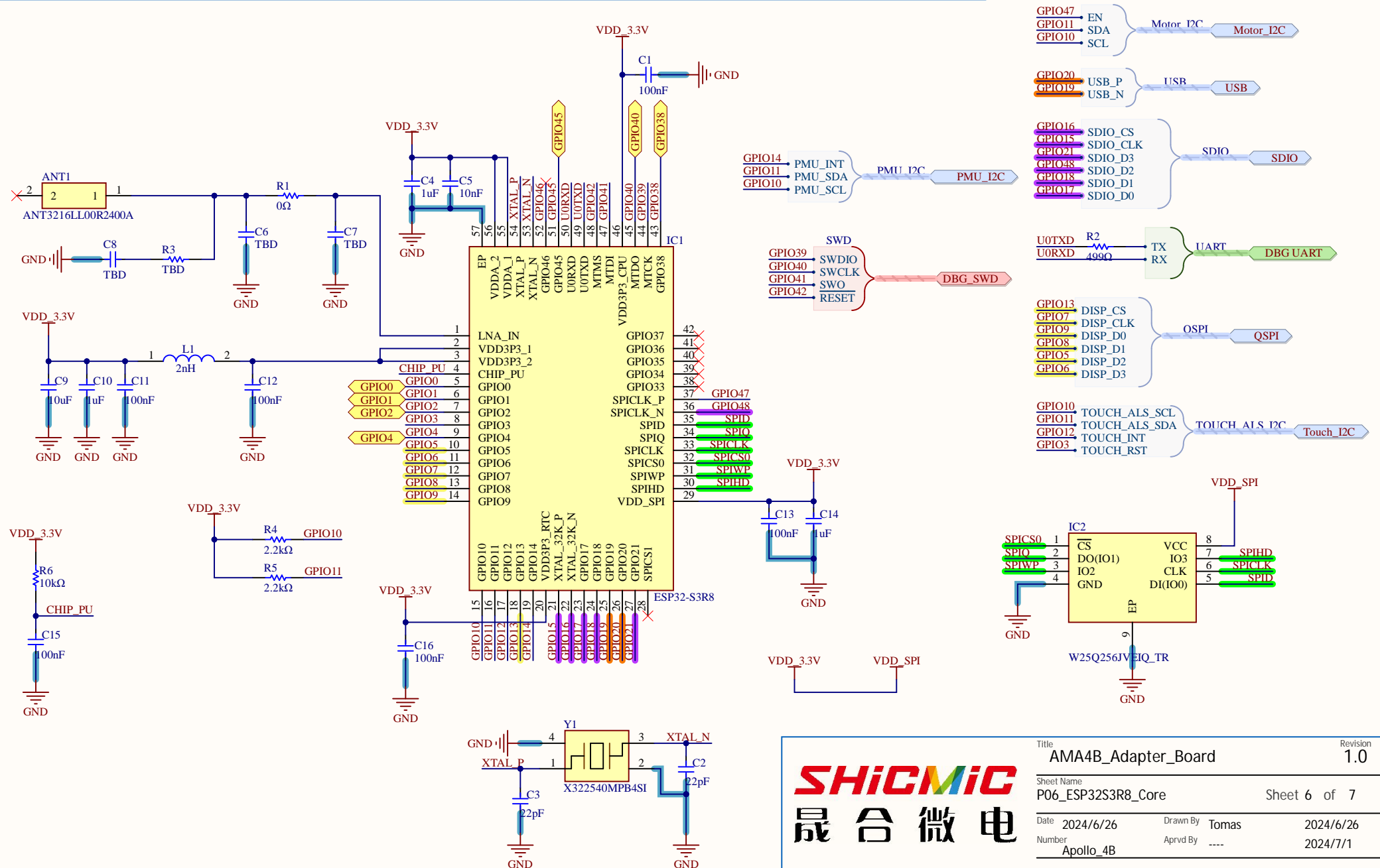
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Sheet Name		P04_ USB		Sheet 4 of 7	
Date	2024/6/19	Drawn By	Tomas	2024/6/19	
Number	Apollo_4B	Aprvd By	----	2024/7/1	

Debug-In Header



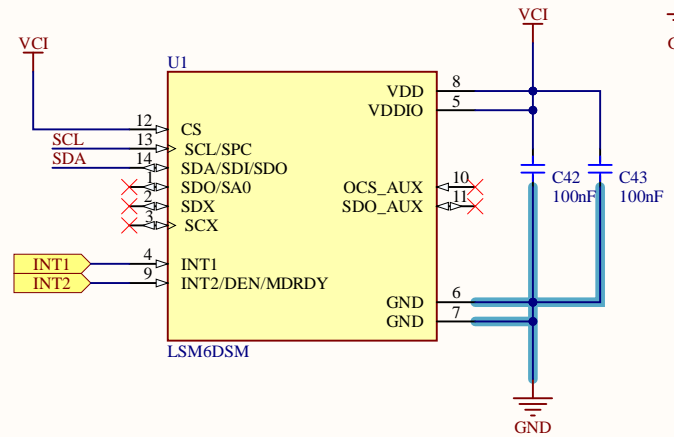
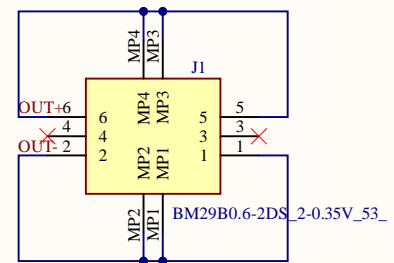
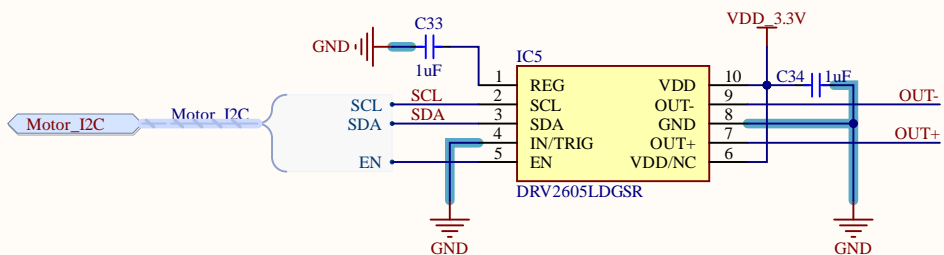
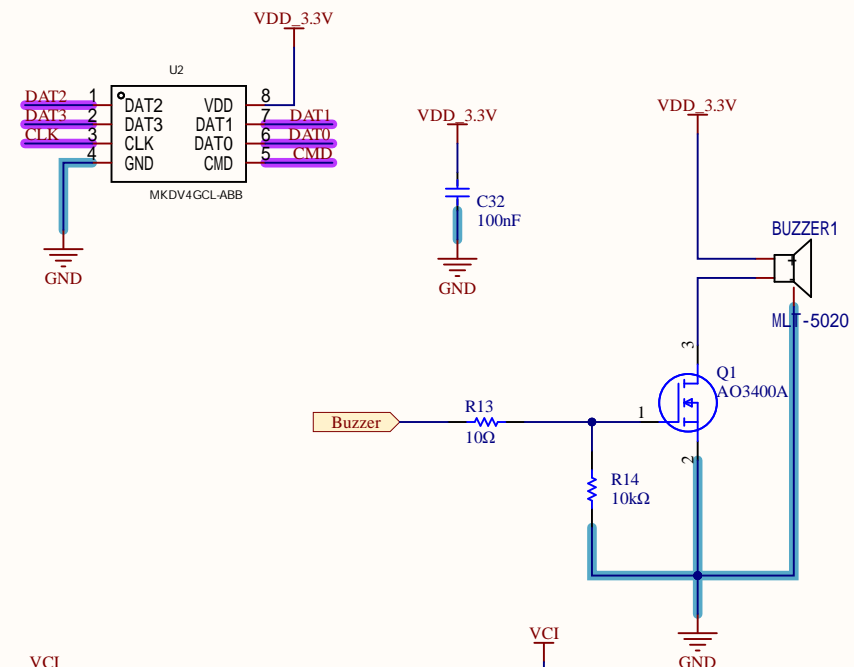
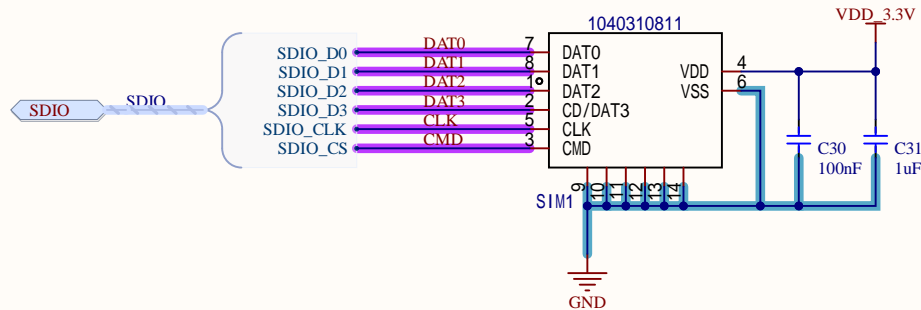
Title		AMA4B_Adapter_Board		Revision
				1.0
Sheet Name		P05_ Debugger		Sheet 5 of 7
Date	2024/6/14	Drawn By	Tomas	2024/6/14
Number	Apollo_4B	Aprvd By	----	2024/7/1


ESP32S3 Core



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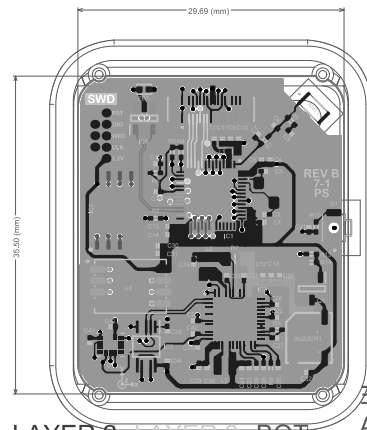
Title AMA4B_Adapter_Board		Revision 1.0
Sheet Name P06_ESP32S3R8_Core		Sheet 6 of 7
Date 2024/6/26	Drawn By Tomas	2024/6/26
Number Apollo_4B	Aprvd By ----	2024/7/1



 晟合微电		Title AMA4B_Adapter_Board		Revision 1.0
		Sheet Name P07_Peripheral		Sheet 7 of 7
Date 2024/7/1	Drawn By Tomas	2024/7/1		
Number Apollo_4B	Aprvd By ----	2024/7/1		

NOTES:

REVISIONS			
REV	DESCRIPTION	DATE	APPROVAL
1.0	Initial Release	2024/7/1	



TOP LAYER 2 LAYER 3 BOT
SILKTOP
ASSEMBLY TOP SIDE
ASSEMBLY BOTTOM SIDE
SILKBOT

REV. 1.0		Apollo_4B-DWG	
SHEET 5 OF 5		Apollo_4B-DWG	
DRAWN BY: A		AMA4B_Adapter_Board	
DRAWN NO.		ASSEMBLY DRAWING,PCB,	
DATE		2024/7/1	
SHEET 5 OF 5		2024/7/1	

				APPROVAL		DATE		SHM	
		UNLESS OTHERWISE SPECIFIED		DRAWN		2024/7/1		Tomas	
		DIMENSIONS ARE IN INCHES		CHECKED					
		TOLERANCES ARE 1/2-1/16 DEG		RELEASED					
		3 PL DIMENSIONS 1/2-1/16							
		REMOVE ALL BURRS AND SHARP EDGES							
		DO NOT REPLY TO THIS DRAWING TO DIM							
		DIMENSIONAL LIMITS APPLY AFTER PROCESSING							