

# Dae Jung Kim

[djnara@gmail.com](mailto:djnara@gmail.com)

+82-10-6822-5335

## EDUCATION:

### **Daejin University,. M.S.E.E**

Master course of Computer Engineering

Thesis title : “An enhanced PKI security management on smartphone”

Advisor : Yun-Seok Chang

Date of Graduation : February '11

### **Daejin University,. B.S.E.E**

Bachelor of Computer Engineering

Area of major concentration : Device Security and Operating Systems

Date of Graduation : February '09

## ACADEMIC EXPERIENCE:

*March '06 ~ December '11* **Daejin University**

Researched for Cryptography algorithms

Researched for Linux kernel

Researched for Macintosh system

Researched for android system and development of applications

Researched for weak of security on web servers

## INDUSTRY EXPERIENCE:

*May'19 ~ Present* **ODuck CEO**

Project title : Humanoid

Main job : Research and Development for all parts

1. Sensor/ Motor Control
2. Firmware(STM32F0 / STM32F1)
3. PC Program(Qt5)
4. 3D Design(FreeCAD, Fusion 360)
5. FGPA(VHDL, Verilog)
6. Artwork Design
7. 3D Printer
8. Tensorflow(TTS, STT, Noise Filtering)

*November '17 ~ March '19* **上海奉石科技有限公司 Partner**

Project title : Bio-signal detection device with Robot design

Main job : Development of signal processing analysis system

9. Robot design
10. System build-up and set-up for design of IC
11. Development of android application to control and test for IC
12. Development of android application to control signal processing and display
13. Development of firmware to signal processing(STM, PIC, Ti)
  - A. Interface : Bluetooth, BLE(TI, Nordic), USB, I<sup>2</sup>C, SPI, USART, UART, ADC, TIM, RTC, DMA
14. Artwork design
15. Controls Optical Transmission Equipment, Joining Optical Cable, Maintenance WAN, LAN, PC, Equipment

16. Design and Build for Access control system and environmental disaster

*June '11 ~ September '17* **uMedix Senior Research Engineer**

Project title : VHDL Design, Analog Design and Layout

Main job : Development of signal processing analysis system

1. Development of android application to control and test for IC
2. Development of android application to control signal processing and display
3. Development of firmware to signal processing(STM, PIC, Ti)
  - A. Interface : Zigbee, Bluetooth, USB, I<sup>2</sup>C, SPI, CAN, USART, UART, ADC, DAC, TIM, RTC, DMA
4. Research and development for pattern applications program
5. Artwork design
6. Signal processing
7. Server management and development for server program
8. Development PC program

*March '11 ~ December '11* **Daejin University Part-time instructor**

Subject : Multimedia, Micro-Computer

*August '08 ~ August '09* **ICS Research Engineer**

Project title : Bio-signal processing

Main job : Signal processing analysis system

1. Realization of signal processing H/W, include 3 channel sensor
2. Supporting main programmer

*March '06 ~ March '07* **Daejin University**

Home-network and Homecare service's security Researcher

Designed security model.

*September '04 ~ February '05* **Comtec Technical Engineer**

Main job : Fixed System

1. Recover Server in banks
2. Fixed computer devices

## **CONFERENCE PUBLICATIONS:**

“A Wireless ExG Interface for Patch-Type ECG Holter and EMG-Controlled Robot Hand”

Sensors 2017, 17(8), 1888;doi:10.3390/s17081888, Aug 16<sup>th</sup>, 2017

“A Multi-Sensor Mobile Interface for Industrial Environment and Healthcare Monitoring”

IEEE Transactions on Industrial Electronics, Vol. 64, Issue 3, pp.2344-2352, Mar, 2017

“Tissue-Informative Mechanism for Wearable Non-invasive Continuous Blood Pressure Monitoring”

Scientific Reports, 5, 6618; DOI:10.1038/srep06618, Oct 21<sup>th</sup>, 2014

“Multi-Purposes u-Healthcare Service System for Smart Mobile Devices”

CUTE1182-12-02

“An enhanced PKI security management on smartphone”

KIPS 2011. 11

“Design and Implementation of Zigbee-based BAN Measurement System for WBAN Service”

KSII 2011.6, 11-12

## **LICENSE:**

*July '08 CISSP, Oct '03 Linux Master Level 2, Jan '02 Network Master, June '02 Network+,  
Nov '01 PC Master*

## **SKILLS:**

Tool : Cadence, Calibre, Synopsis, Synthesis, VIVADO, OrCAD, Allegro, CAM350, Eclipse, MPLAB, Keil, xide, Source Insight, Matlab, Wireshark, Photoshop, LightRoom, MS word, MS Office, MS excel, MS Project, MS Visio, Keynote, Page, HWP, Rational Rose, Premiere Pro, FinalCut Pro, After Effects, Putty, UltraEdit, Fusion 360, FreeCAD, Tensorflow, Docker

Languages : C, C#, Java, VHDL, Verilog, ASP.Net, SQL, PHP, Objective-C, Script, JavaScript, HTML, CSS, Python

Operating System : Windows, Linux, Macintosh, DOS

DataBase : MySQL, ORACLE

Design : UML