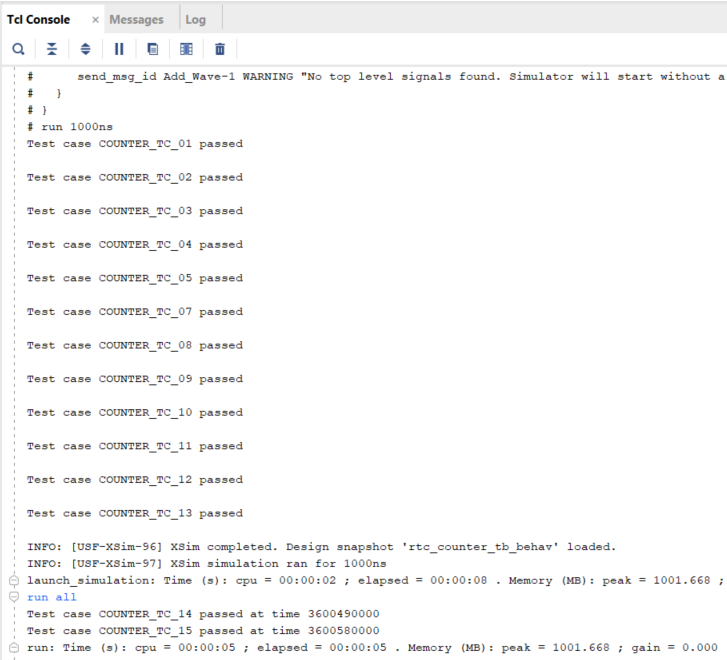
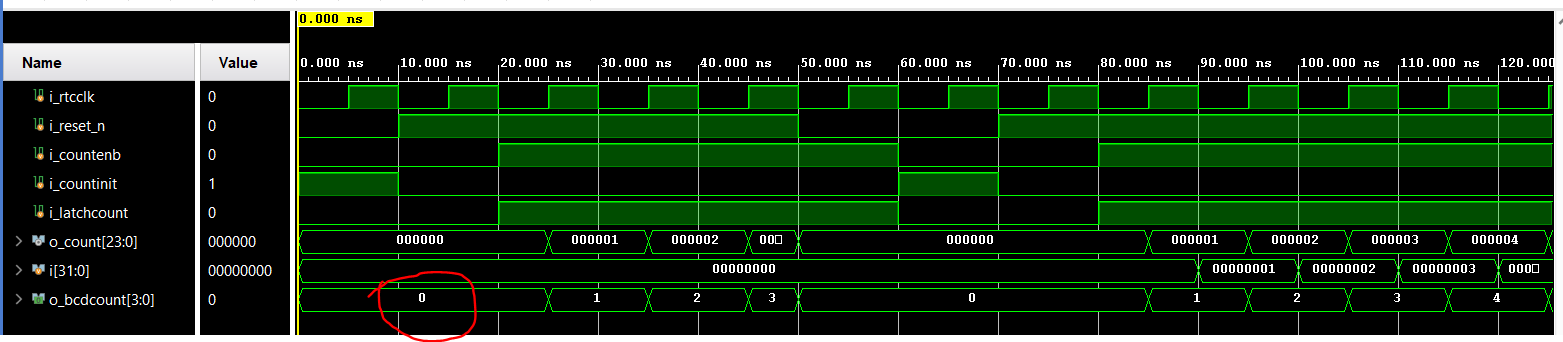
**Module: Counter**

**Tested by: Waseem Orphali**

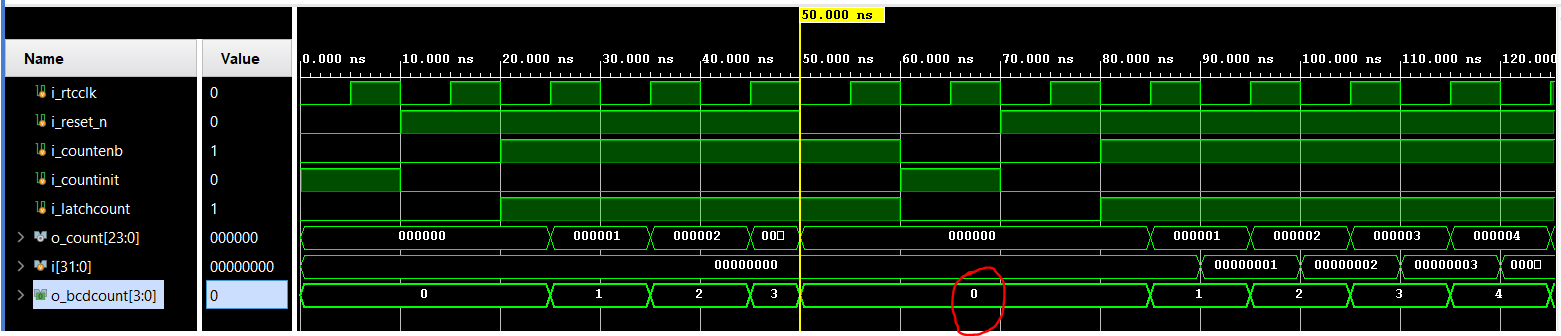
All tests passed with no errors.



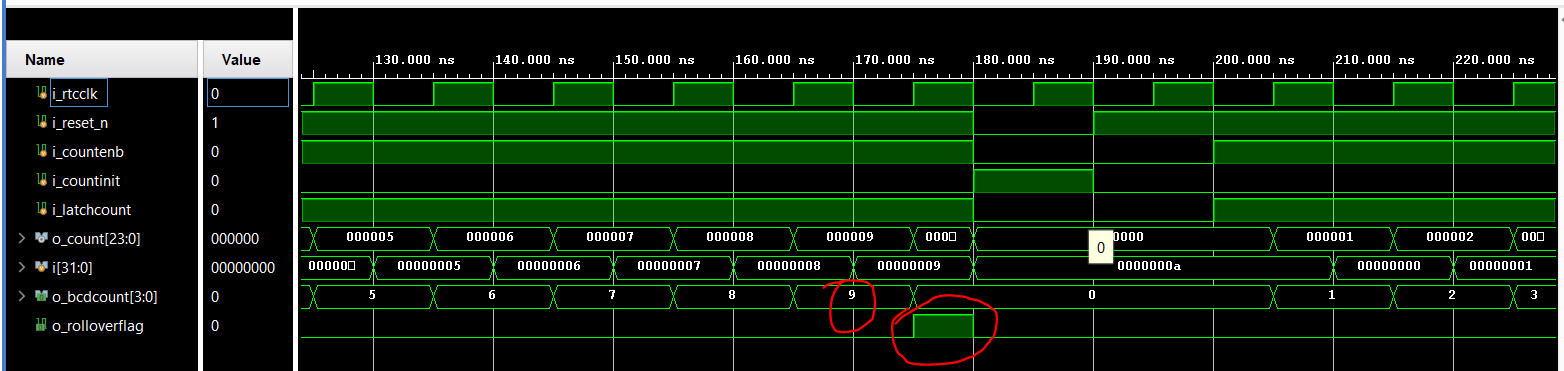
* COUNTER\_TC\_01:
  + Requirement tested: COUNTER\_INC\_01
  + Passed.
  + Method of testing: Waveform observation.



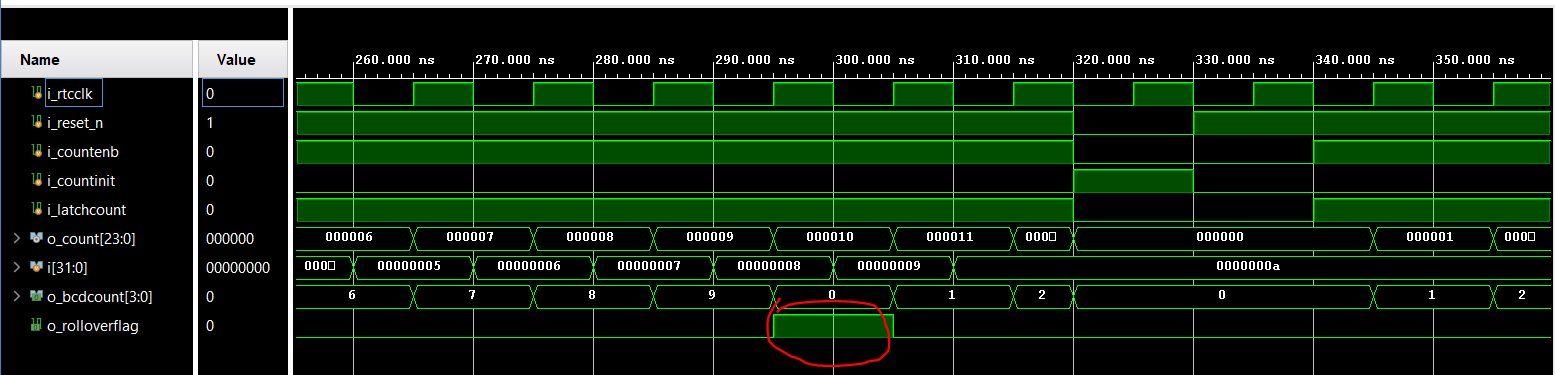
* COUNTER\_TC\_02:
  + Requirement tested: COUNTER\_RESET\_01
  + Passed.
  + Method of testing: Waveform observation.



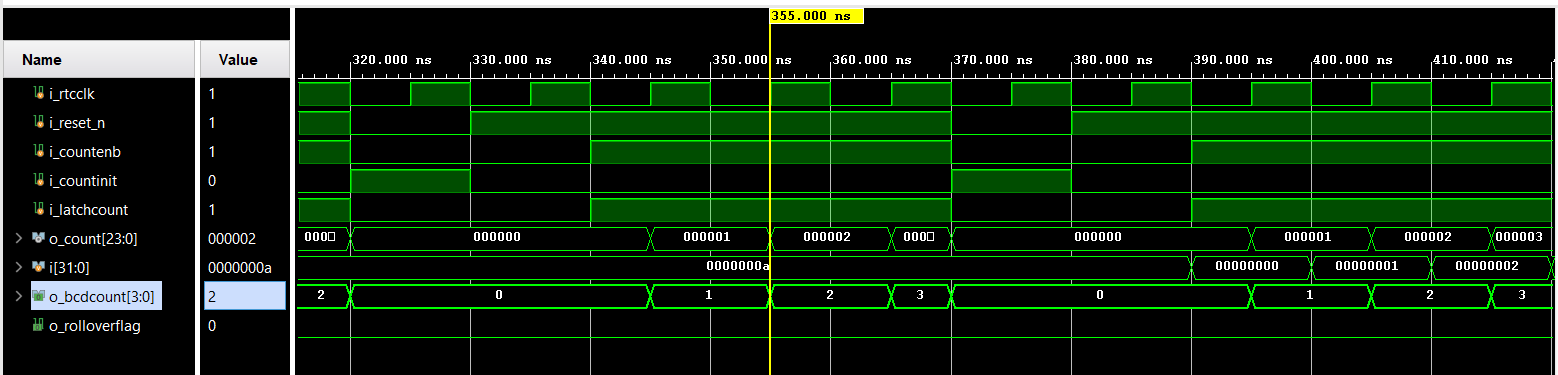
* COUNTER\_TC\_03:
  + Requirement tested: COUNTER\_ROLLOVER\_01
  + Passed.
  + Method of testing: Waveform observation.



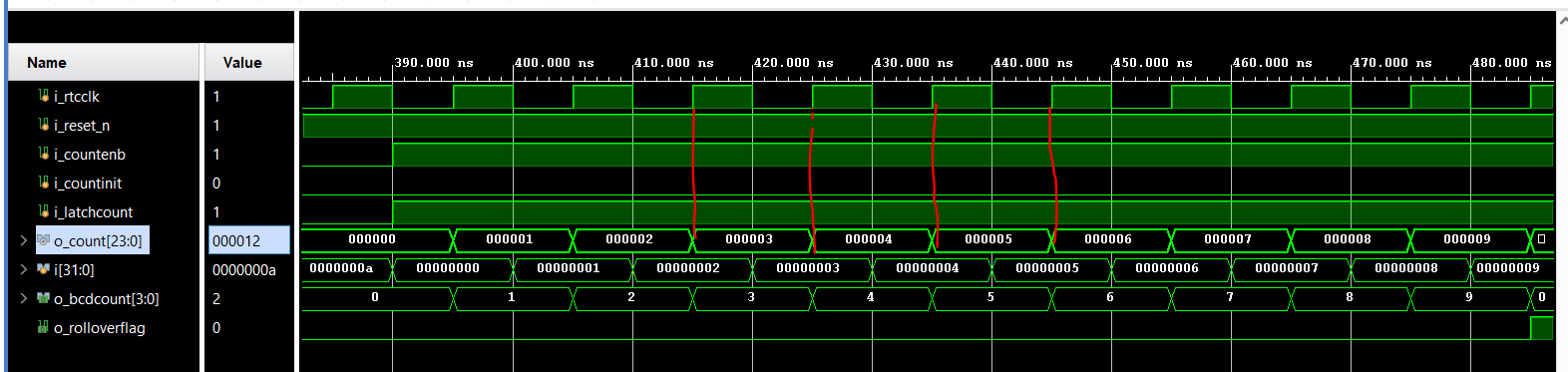
* COUNTER\_TC\_04:
  + Requirement tested: COUNTER\_ROLLOVER\_02
  + Passed.
  + Method of testing: Waveform observation.



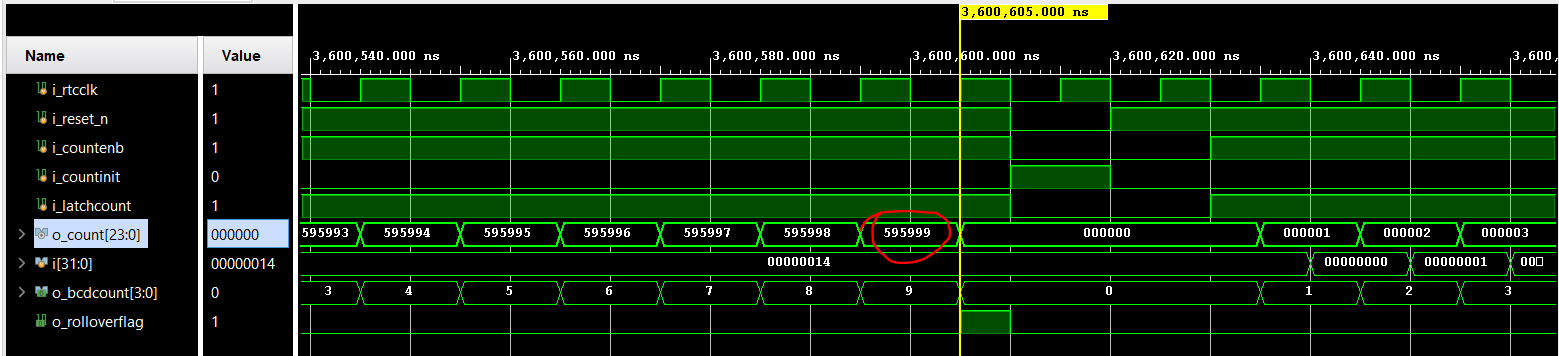
* COUNTER\_TC\_05:
  + Requirement tested: COUNTER\_INC\_02
  + Passed.
  + Method of testing: Waveform observation.

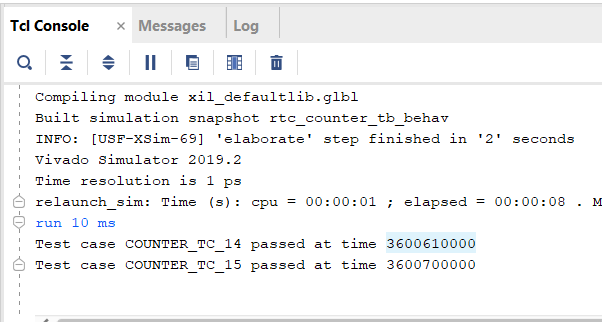


* COUNTER\_TC\_06:
  + Requirement tested: COUNTER\_TOP\_01
  + Passed.
  + Method of testing: port map observation.
* COUNTER\_TC\_07:
  + Requirement tested: COUNTER\_TOP\_02
  + Passed.
  + Method of testing: port map observation.
* COUNTER\_TC\_08:
  + Requirement tested: COUNTER\_TOP\_03
  + Passed.
  + Method of testing: port map observation.
* COUNTER\_TC\_09:
  + Requirement tested: COUNTER\_TOP\_04
  + Passed.
  + Method of testing: port map observation.
* COUNTER\_TC\_10:
  + Requirement tested: COUNTER\_TOP\_05
  + Passed.
  + Method of testing: port map observation.
* COUNTER\_TC\_11:
  + Requirement tested: COUNTER\_TOP\_06
  + Passed.
  + Method of testing: port map observation.
* COUNTER\_TC\_12:
  + Requirement tested: COUNTER\_TOP\_07
  + Passed.
  + Method of testing: port map observation.
* COUNTER\_TC\_13:
  + Requirement tested: COUNTER\_TOP\_08
  + Passed.
  + Method of testing: Waveform observation.



* COUNTER\_TC\_14:
  + Requirement tested: COUNTER\_TOP\_09
  + Passed.
  + Method of testing: Waveform observation and test bench assertion.





* COUNTER\_TC\_15:
  + Requirement tested: COUNTER\_TOP\_10
  + Passed.
  + Method of testing: Waveform observation and test bench assertion.

