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1  /*ENEL 387*/
2  /*Authors: Daniel Takyi & Dwijen Kapadia*/
3  /*Filename: pwm.c*/
4
5
6
7  #include "stm32f10x.h"
8  #include "pwm.h"
9
10
11 void pwmInit(void)
12 {
13     RCC->APB2ENR |= RCC_APB2ENR_TIM1EN | RCC_APB2ENR_IOPAEN | RCC_APB2ENR_AFIOEN; //ENABLE clocks for
Timer 1, Port A, and alternate function I/O
14
15
16     GPIOA->CRH &= 0xFFFFF00;
17     GPIOA->CRH |= 0x000000BB; //Alternate function output Push-pull, max speed 50 MHz on PA8 and PA9
18
19
20
21     TIM1->CR1 |= TIM_CR1_CEN; // Enable Timer1
22     TIM1->CR2 |= TIM_CR2_OIS2 | TIM_CR2_OIS1; // Output Idle State for Channel 1 & 2 OC1=1 when MOE=0
23     TIM1->EGR |= TIM_EGR_UG; // Reinitialize the counter
24     TIM1->CCMR1 |= TIM_CCMR1_OC1M_2 | TIM_CCMR1_OC1M_1 | TIM_CCMR1_OC1PE | TIM_CCMR1_OC1FE; //PWM mode 1,
Preload Enable, Fast Enable on CH1
25     TIM1->CCMR1 |= TIM_CCMR1_OC2M_2 | TIM_CCMR1_OC2M_1 | TIM_CCMR1_OC2PE | TIM_CCMR1_OC2FE; //PWM mode 1,
Preload Enable, Fast Enable on CH1
26     TIM1->CCER |= TIM_CCER_CC1E | TIM_CCER_CC2E; //Enable CH1 output on PA8 & CH2 output on PA9
27     TIM1->PSC = 0x095F; //Divide 24 MHz by 2400 , PSC_CLK = 10000 Hz, 1 count = 0.1 ms
28     TIM1->ARR = 200; // period = 20 ms <== is required for the motor controller(from datasheet)
29     TIM1->CCR1 = 0; // 10 counts = 1 ms
30     TIM1->CCR2 = 0; // 10 counts = 1 ms
31     TIM1->BDTR |= TIM_BDTR_MOE | TIM_BDTR_OSSI; //Main Output Enable, Force Idle Level First
32     TIM1->CR1 |= TIM_CR1_ARPE | TIM_CR1_CEN; // Enable Timer1
33 }
34
35
36 /*****
37 **From the motorcontroller datasheet- the motor controller uses **
38 **pulsewidth to determine speed and direction of the motors' **
39 **rotation. **
40 **Pulsewidth > 1520us ==> Forward revolution **
41 **Pulsewidth < 1480us ==> Backward revolution **
42 **1480us > Pulsewidth > 1520us ==> No revolution **
43 *****/
44
45 void PWMForward(void)
46 {
47     TIM1->CCR1 = 17;
48     TIM1->CCR2 = 17;
49 }
50 void PWMLeft(void)
51 {
52     TIM1->CCR1 = 16;
53     TIM1->CCR2 = 15;
54 }
55 void PWMRight(void)
56 {
57     TIM1->CCR1 = 15;
58     TIM1->CCR2 = 16;
59 }
60 void PWMSharpLeft(void)
61 {
62     TIM1->CCR1 = 16;
63     TIM1->CCR2 = 14;
64 }
65 void PWMSharpRight(void)
66 {
67     TIM1->CCR1 = 14;
68     TIM1->CCR2 = 16;
69 }
70 void PWMStop(void)

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71  {
72      TIM1->CCR1 = 15;
73      TIM1->CCR2 = 15;
74  }
75  void PWMBack(void)
76  {
77      TIM1->CCR1 = 14;
78      TIM1->CCR2 = 14;
79  }
80
81
82
83
84
```