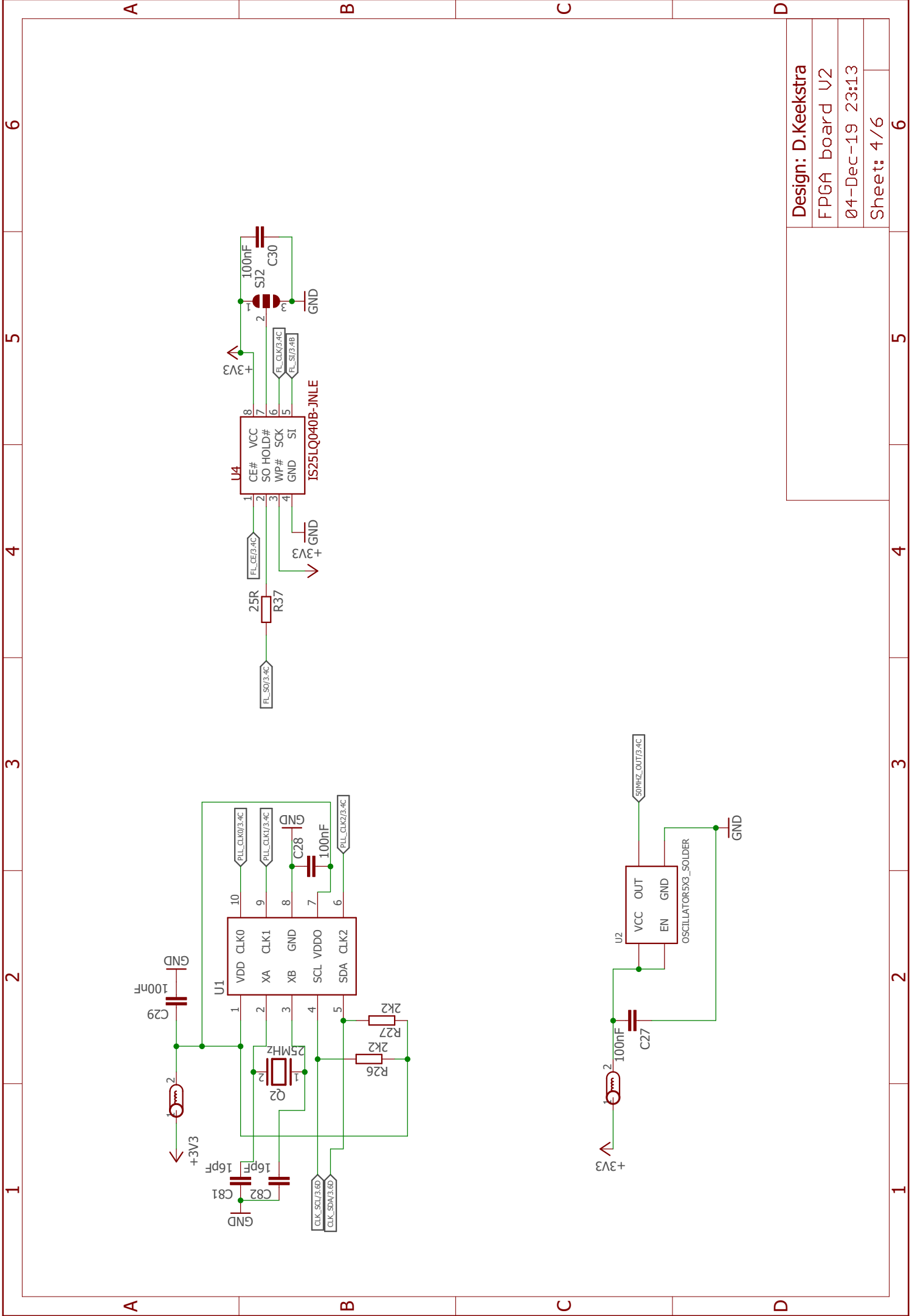


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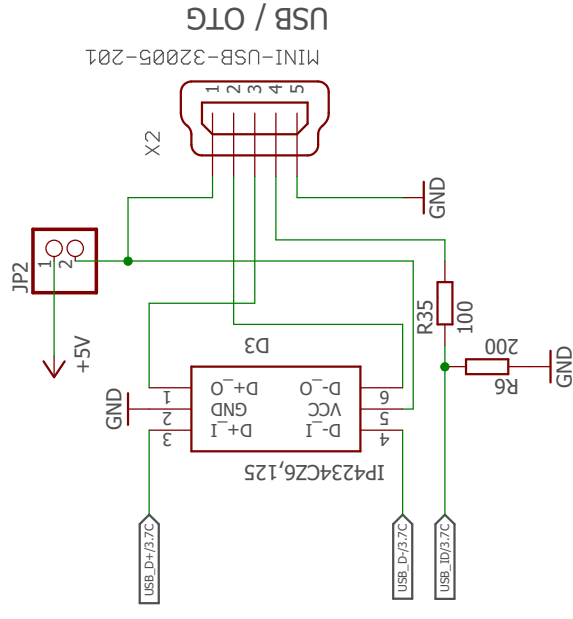
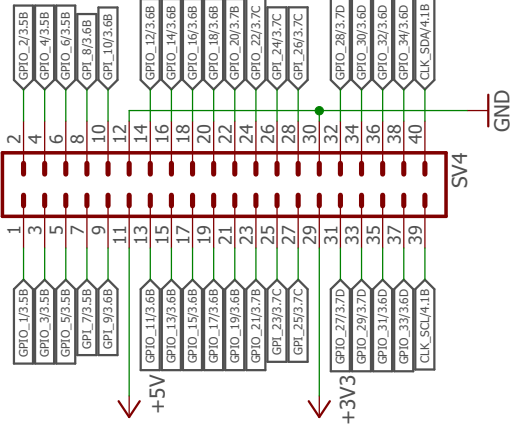
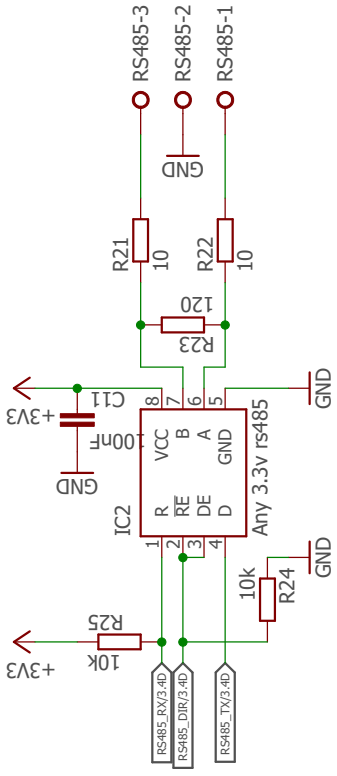
6

A

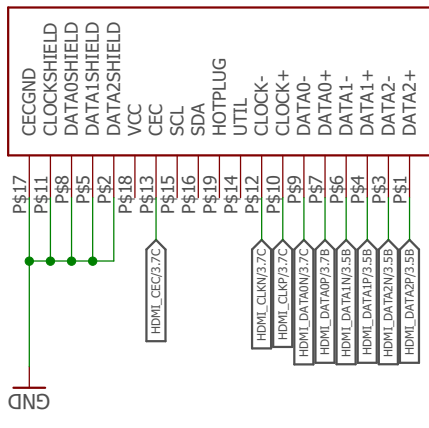
B

C

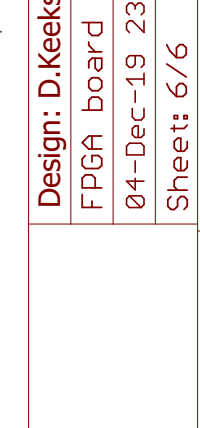
D



HDMI



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