



	Latency	Relative Throughput	LUT	Total Reg
Non-Pipelined	7.1	1	154	64
2-stage Pipeline	9.2	1.54	156	89
3-stage Pipeline	10	2.13	158	118
4-stage Pipeline	11.8	2.41	161	124

$$\text{Latency} = N \times \text{Clock Period} + (N-1) \times \text{Reg Delay} \quad (N = \# \text{ of Pipeline stages}), \quad \text{Relative Throughput} = N \times \frac{\text{Latency of Non-pipelined Multiplier}}{\text{Latency of pipelined Multiplier}}$$