

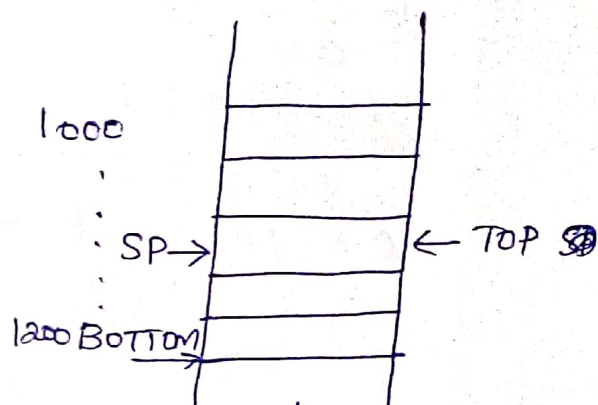
June 10, 2020

COA
Series 1

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2.

~~Assume~~ Let the diagram shown represent the the bottom be the initial address of the stack. The stack fills in such a way that it decrease in address value.



STACK PUSH

MOVE RO, -(SP)

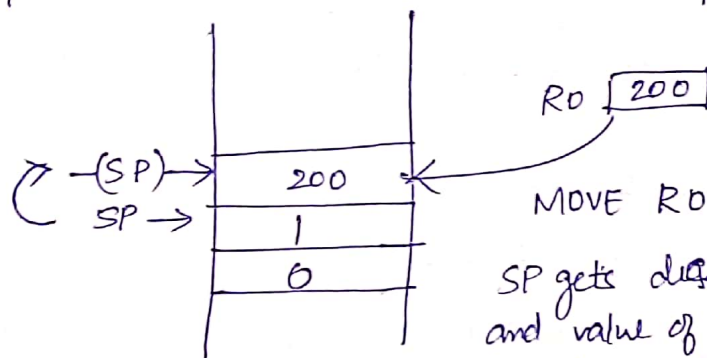
Let RO contain value to be pushed and SP represent the initial pointed location.

STACK POP

MOVE (SP)+, RO

Alternate Method PUSH
Sub #4 SP
MOV RO (SP)

For example if value 200 is in RO when pushed

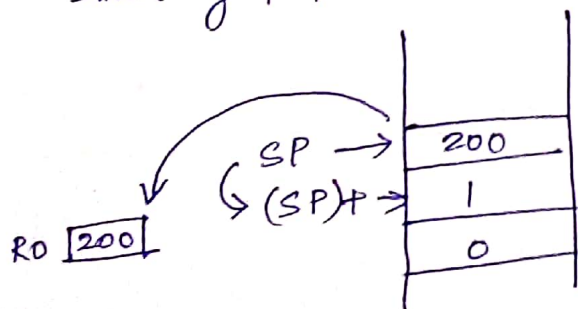


MOVE RO, -(SP)

SP gets decreased by 1 (4 bytes) and value of RO gets stored to address pointed by SP

3.

Similarly POP



First SP moves value to the RO and SP gets updated by increasing the mem location

$$(31)_{10} = (01111)_2$$

L Shift #3 R0

After Lshift #3 RO

~~14300~~

~~1400~~ 96 bit

$(111000)_2$ $(11111000)_2$

After Rotate L #3, RO

$(000111)_2$ 8 bit
 \downarrow $(000.11111)_2$
 carry

In Rotate L through carry assumed zero move byte by byte from LSB to MSB through carry and shift the old MSB to the ~~left~~ right. We are actually doing the circular shift operation.

4. My phone number ends in 9.

∴ option 9 20 (R1, R3)

This is a mode of accessing memory. Indexed mode with offset.
ie, $[R1 + R3 + \underline{20}]$ - as the memory location
 $\quad\quad\quad \downarrow$
 $\quad\quad\quad$ Offset

Instruction sequence

1. PCout MARin Read Select 4 Add Zin
2. Zout Yin PCin WMFC
3. MDRout IRin
4. R1out Yin
5. R3out, Select Y, Add, Zin
6. Zout Yin
7. Offset of IRout Select Y Add Zin // Adds offset 20
8. ~~MARin, Zout, MARin, Add WMFC~~ END
8. Zout, R1 end

1. Last 4 digits of phone number 3609

$$(3609)_{10} = (111000011001)_2$$

In 32 bit format,

- ① Little endian. — means that the ending value will be least significant bit i.e., Index 0 will have ~~the~~ LSB of the digit.

31	...	11	10	9	8	7	6	5	4	3	2	1	0	
0	...	0	0	1	1	0	0	0	0	1	1	0	0	1

- ② Big Endian — means that the ending value will be the MSB bit of the binary. i.e., Index 0 have the MSB of the digit.

31	...	0	...	0								
1	0	0	1	1	0	0	0	0	1	1	...	0

~~to single access we can say that~~