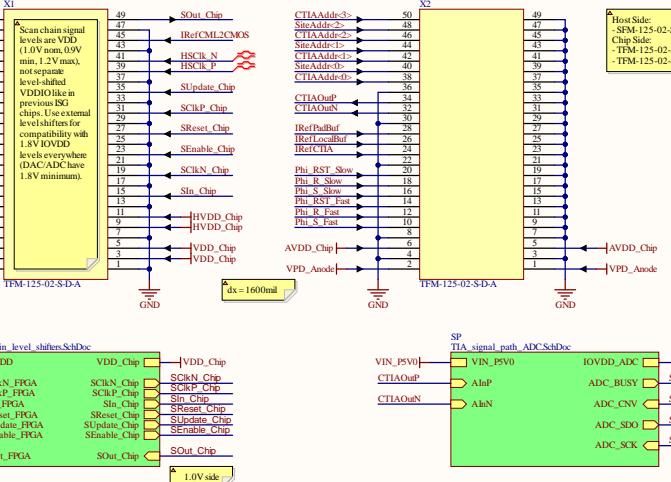
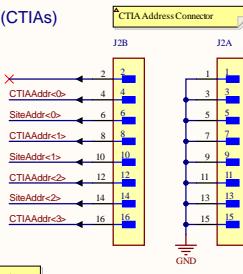


Use DACs to set voltages and bias currents.
Do not use any pots on this board.
DAC1_VDD, HVDD, AVDD, VPD
DAC2_IRefPadBuf, IRefLocalBuf, IRefCTIA, IRefCML2CMOS

Wirebond Board Connectors (Mirror Image)

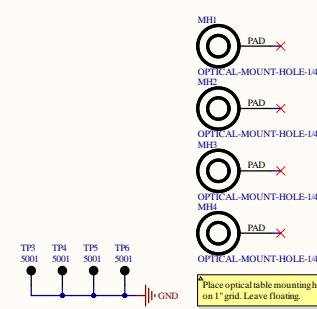
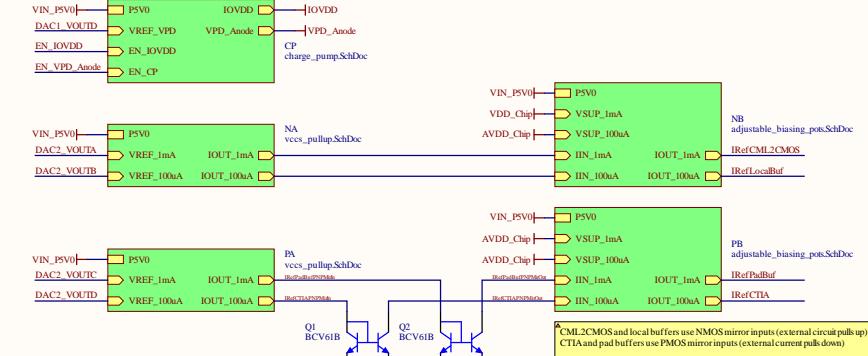
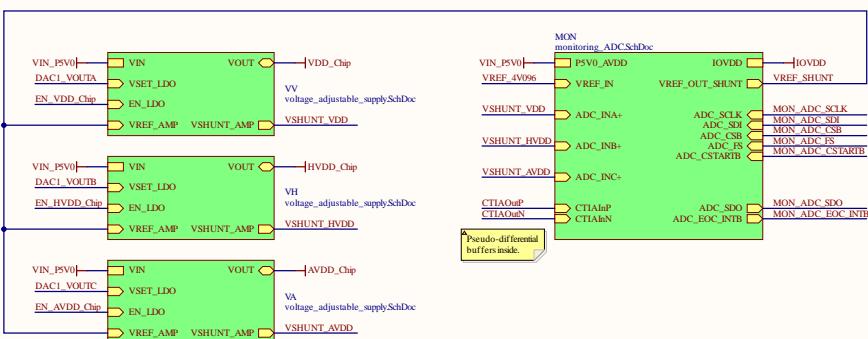
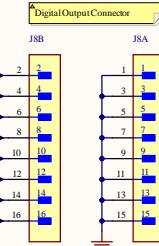
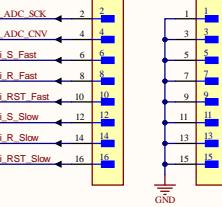


Mixed-Signal Side (CTIAs)

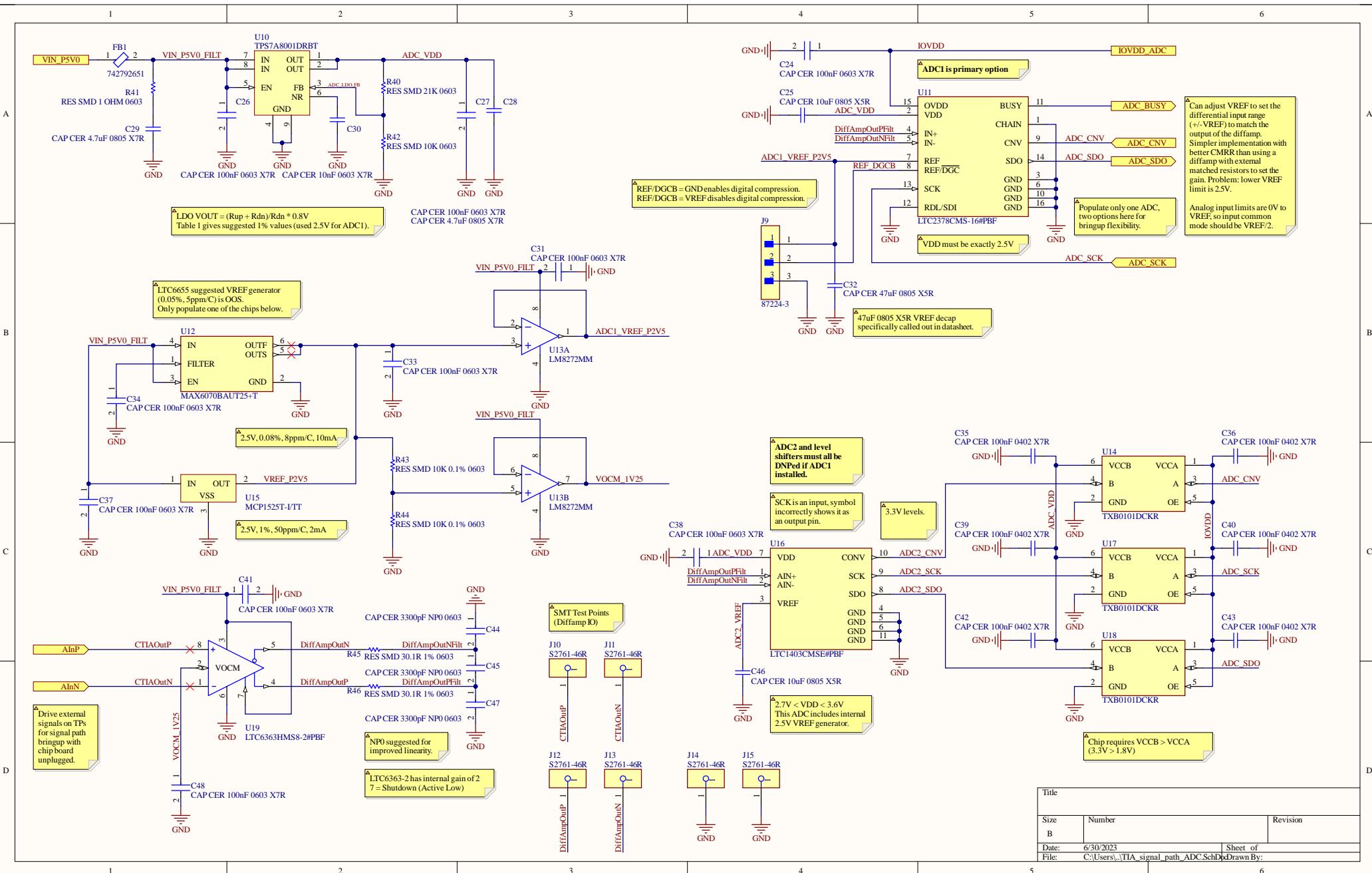


Right side connectors are rotated 180-deg so south side of schematic symbol corresponds to north side of layout.

1.8V(AVDD) signals



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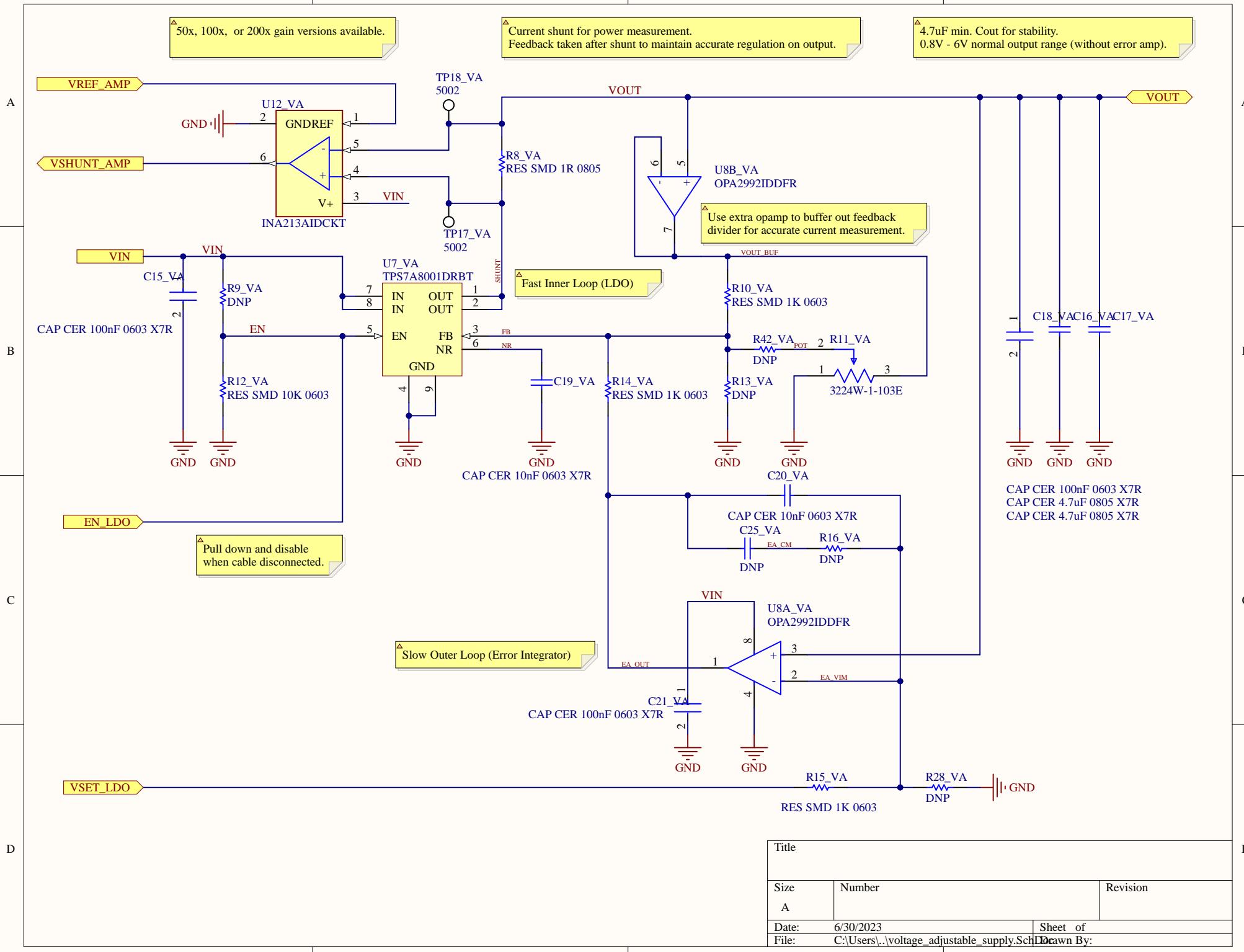


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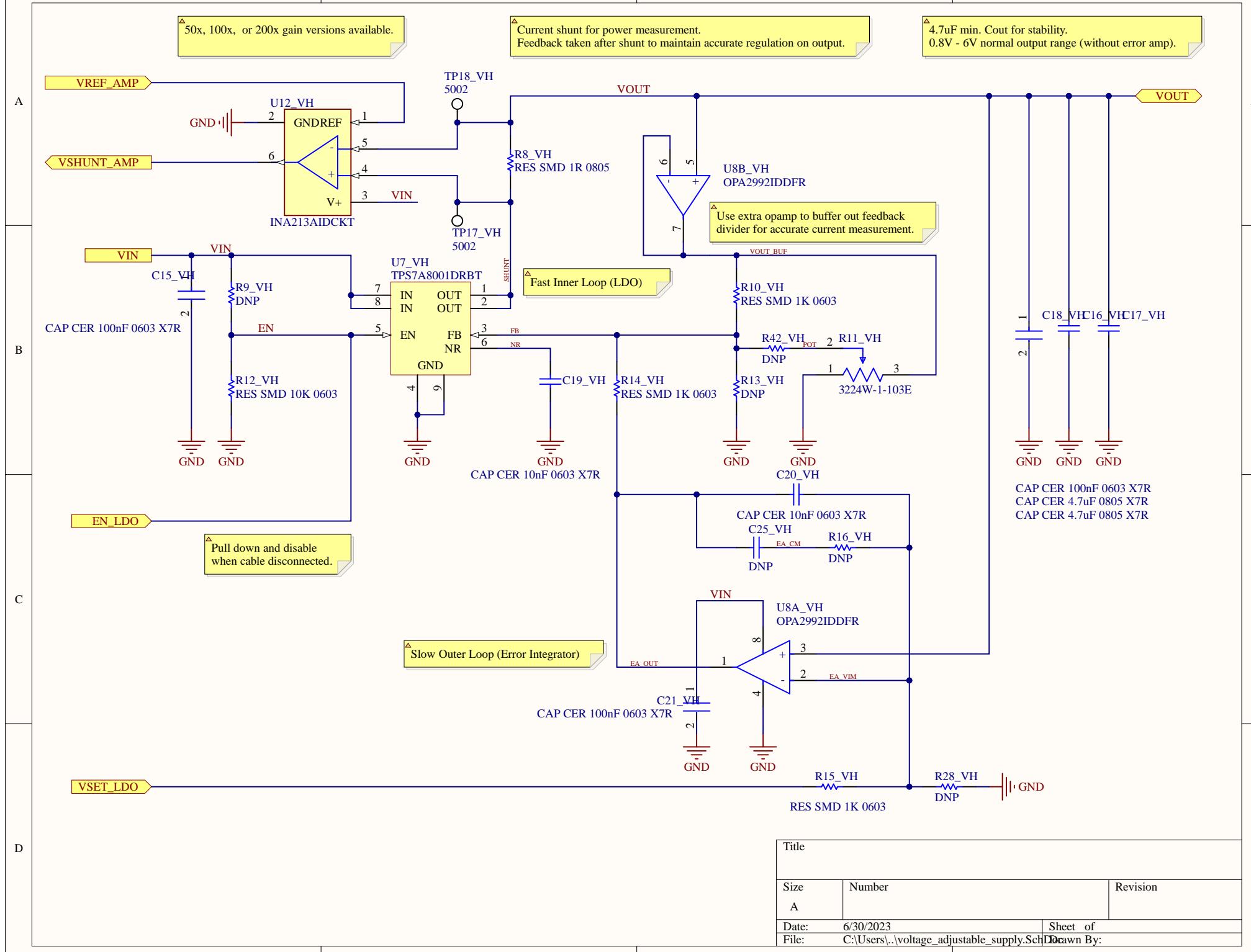
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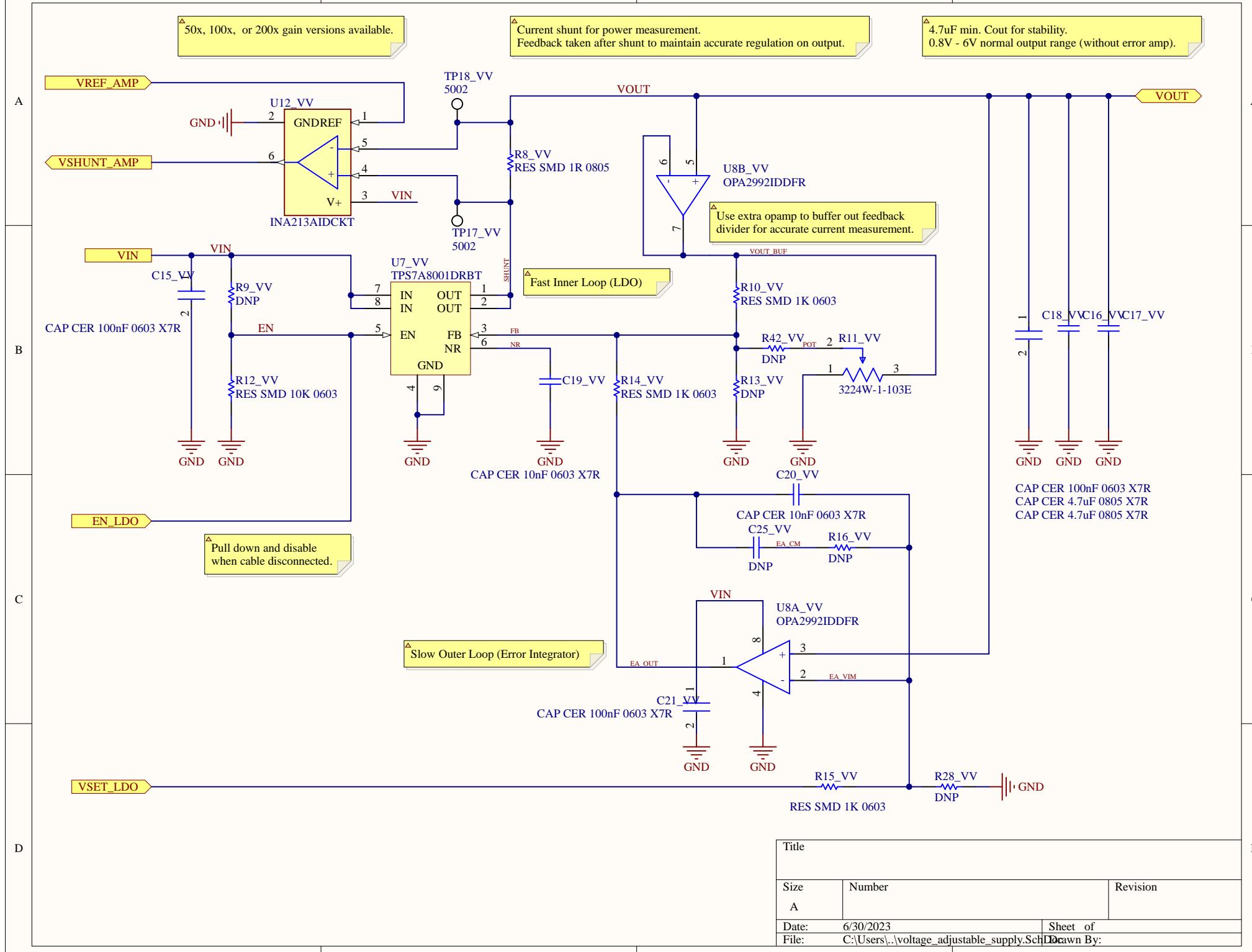
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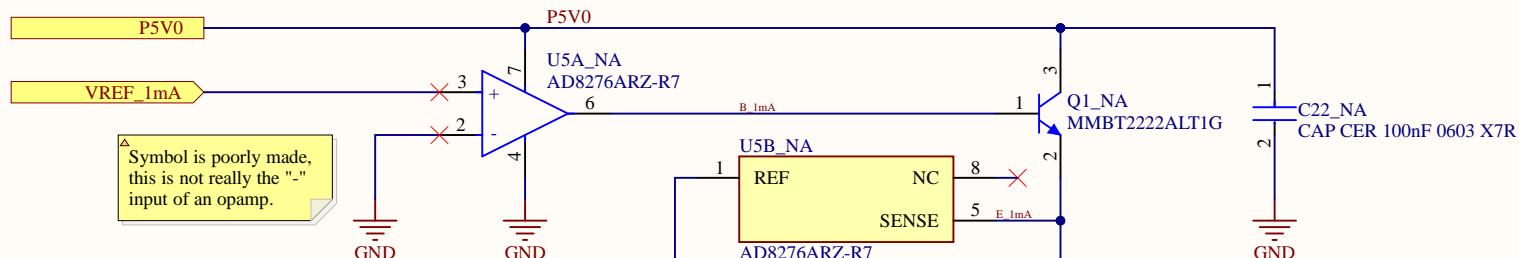
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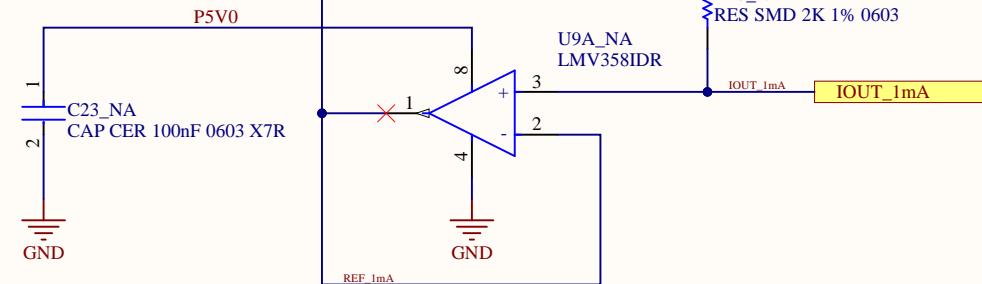
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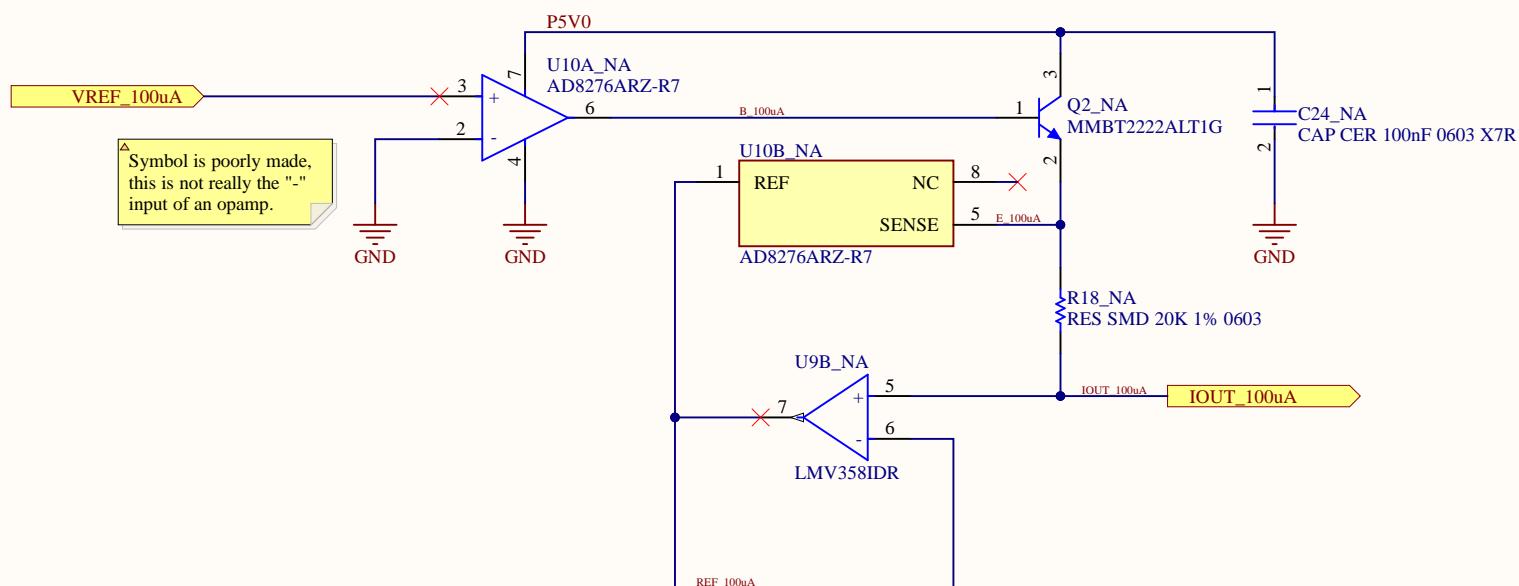


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D

INA597 is a similar pin-compatible replacement from TI that is in stock, use it instead.

<https://www.analog.com/en/analog-dialogue/articles/diff-amp-heart-of-precision-current-source.html>

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Revision

Date:

6/30/2023

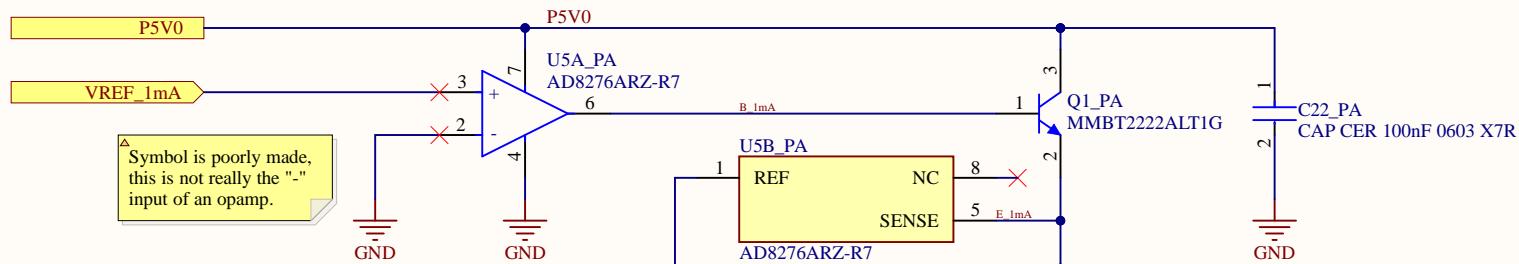
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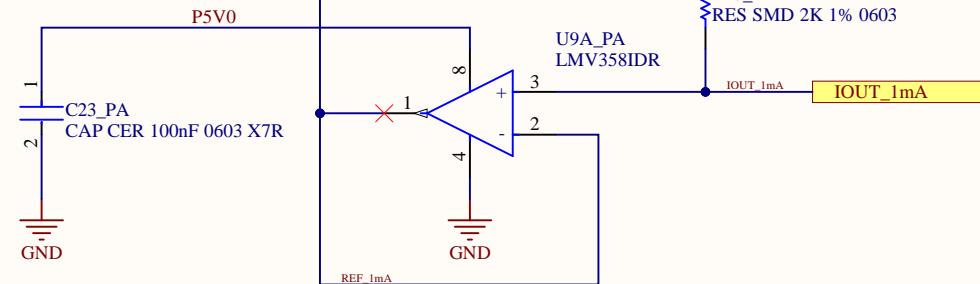
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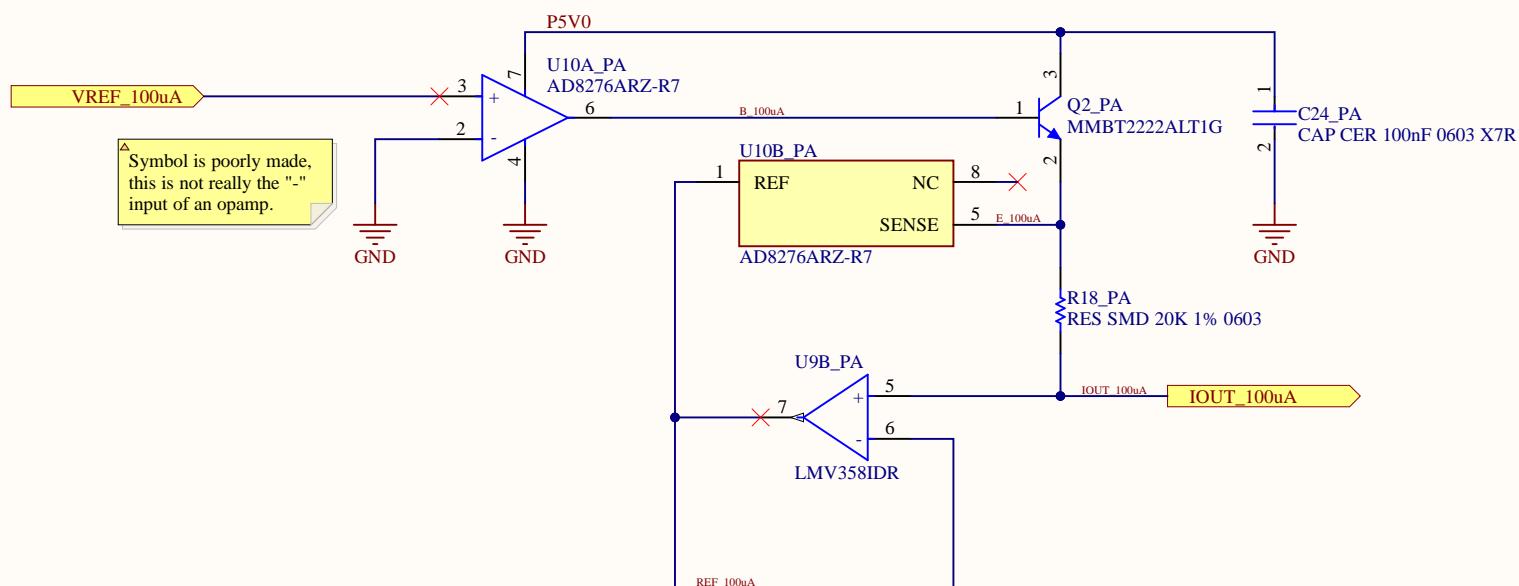


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C

D

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<https://www.analog.com/en/analog-dialogue/articles/diff-amp-heart-of-precision-current-source.html>

Title

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Date:

6/30/2023

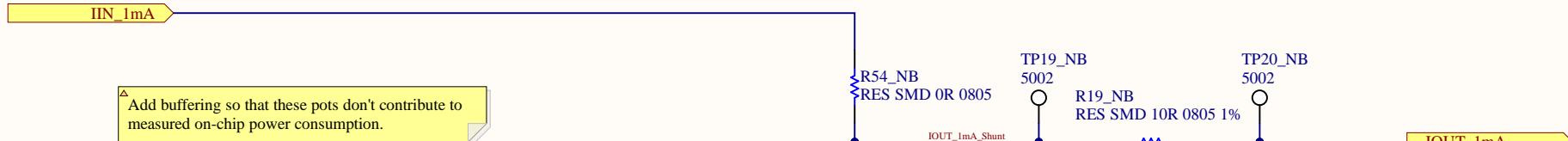
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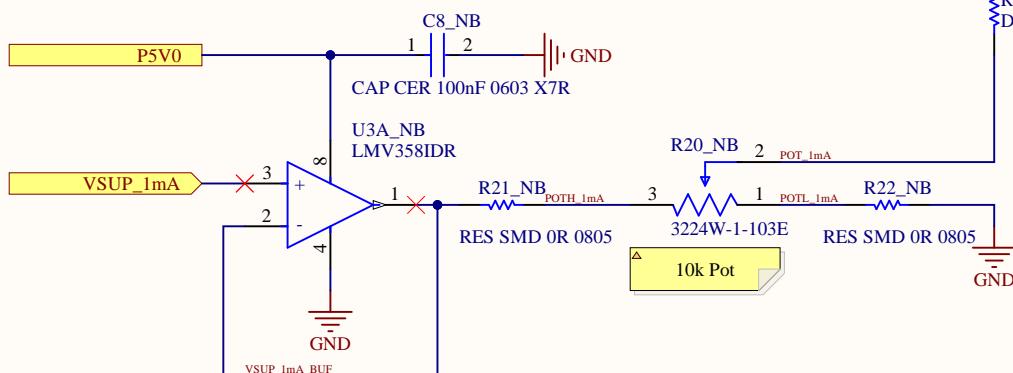
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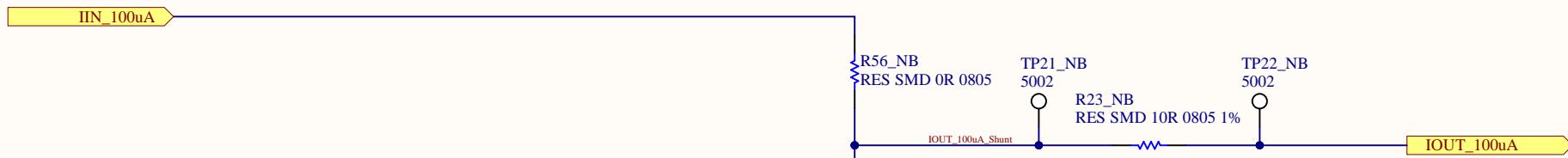
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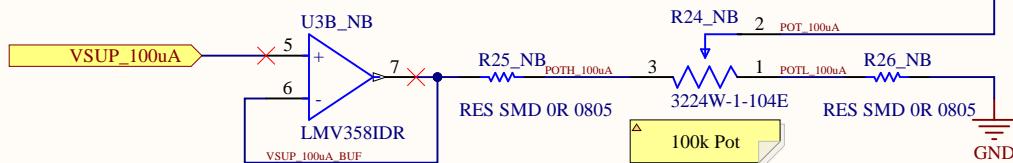
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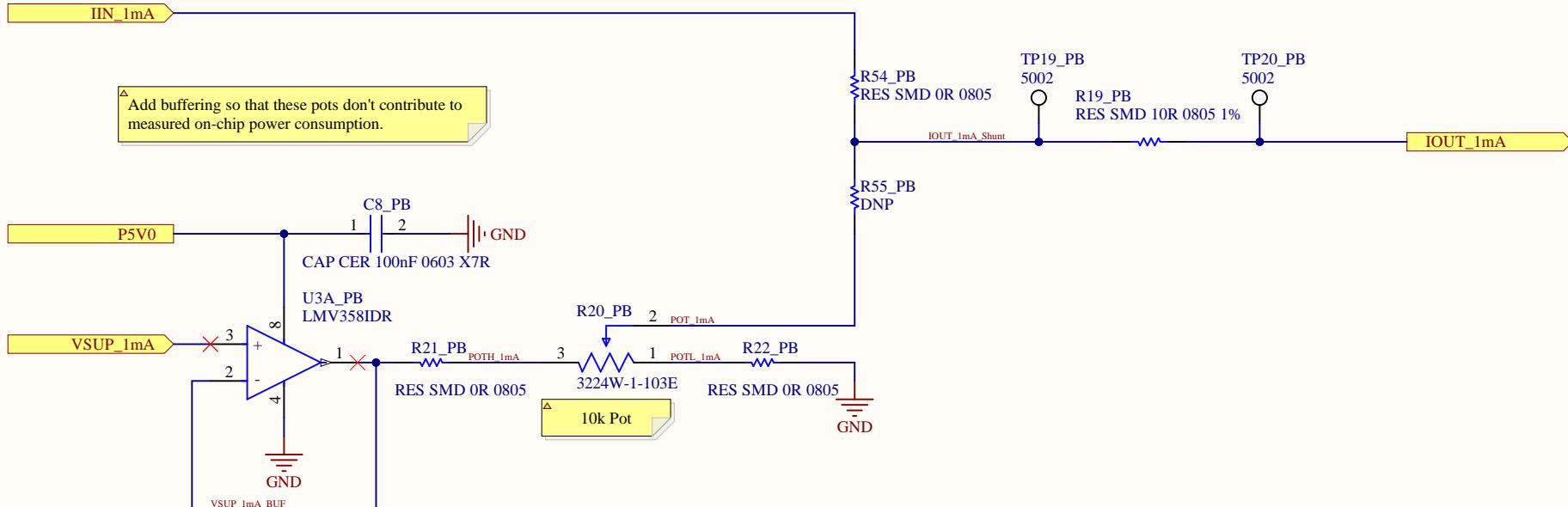
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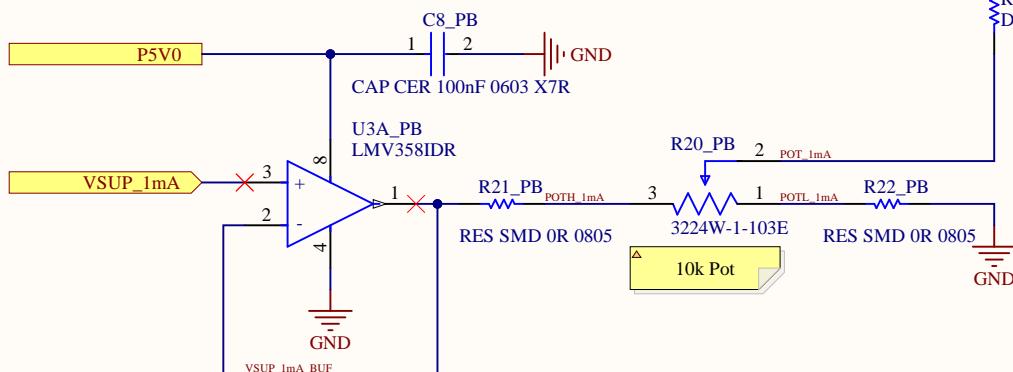
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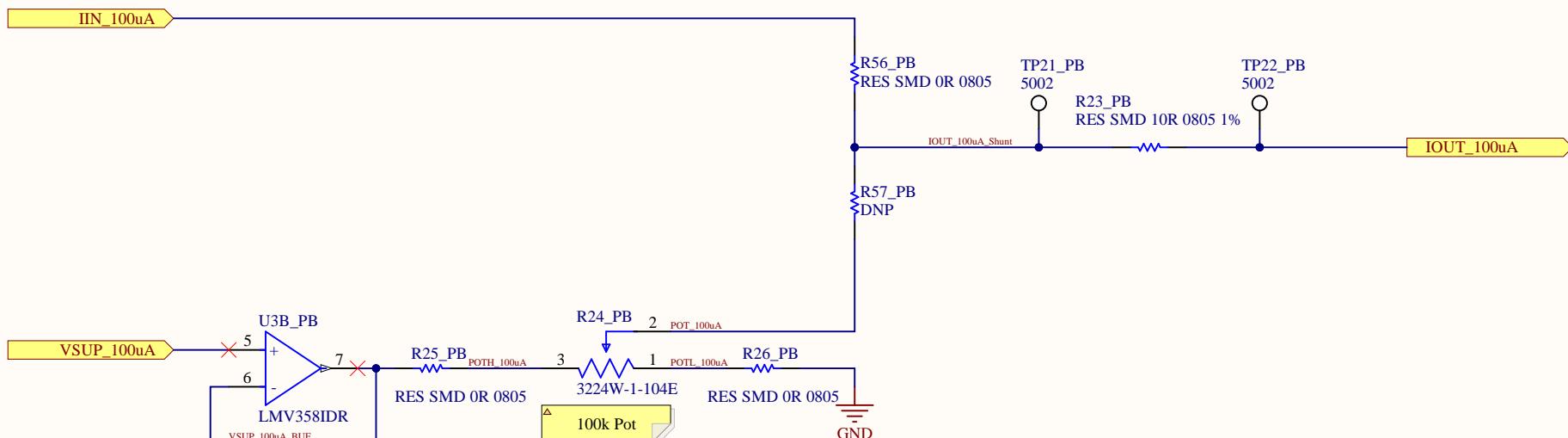
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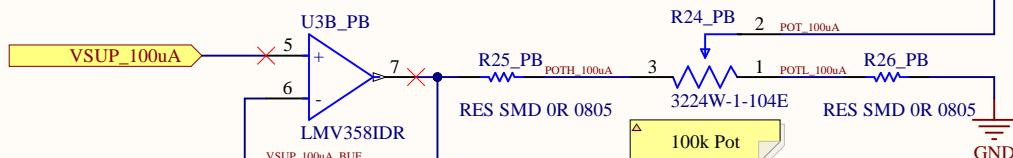
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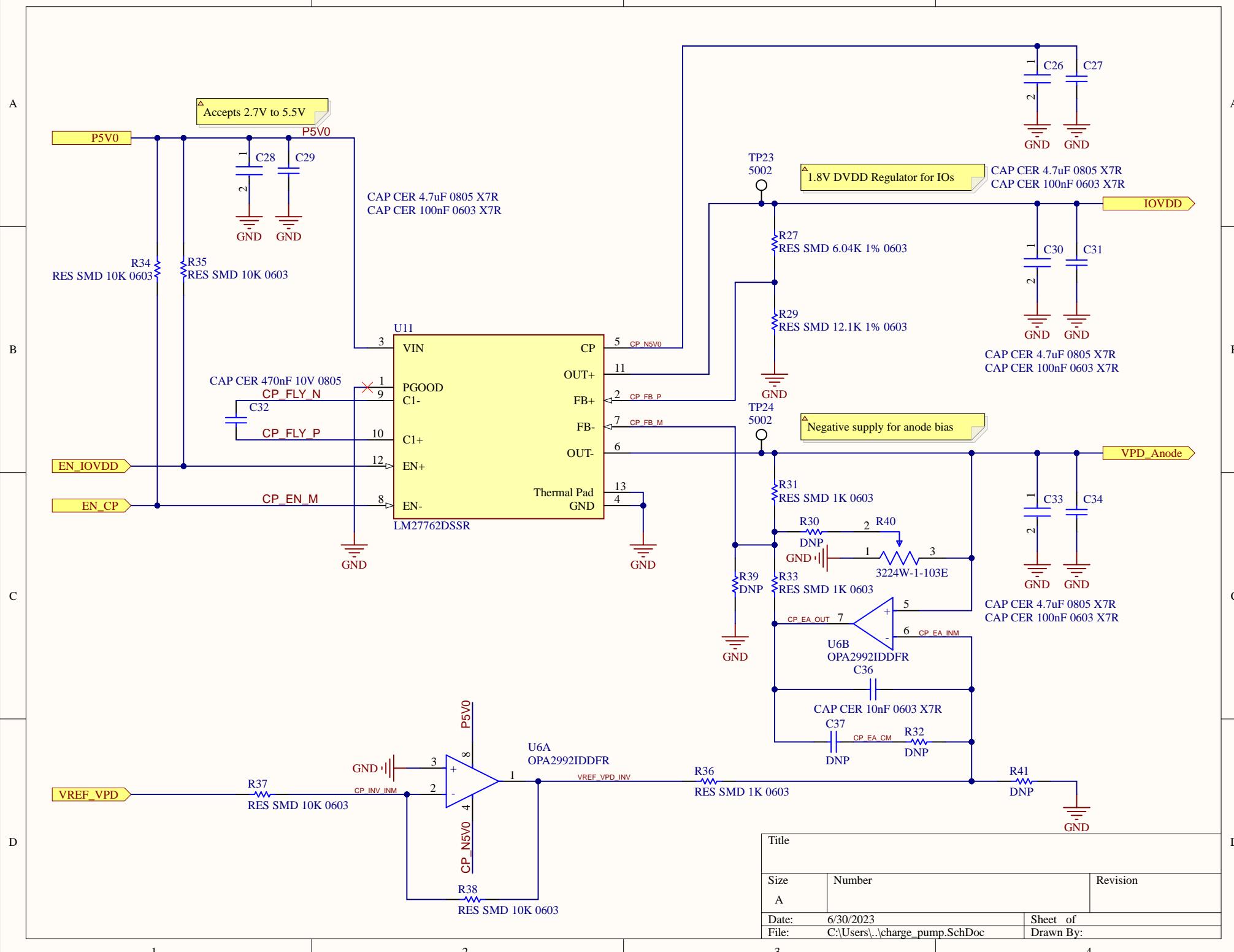
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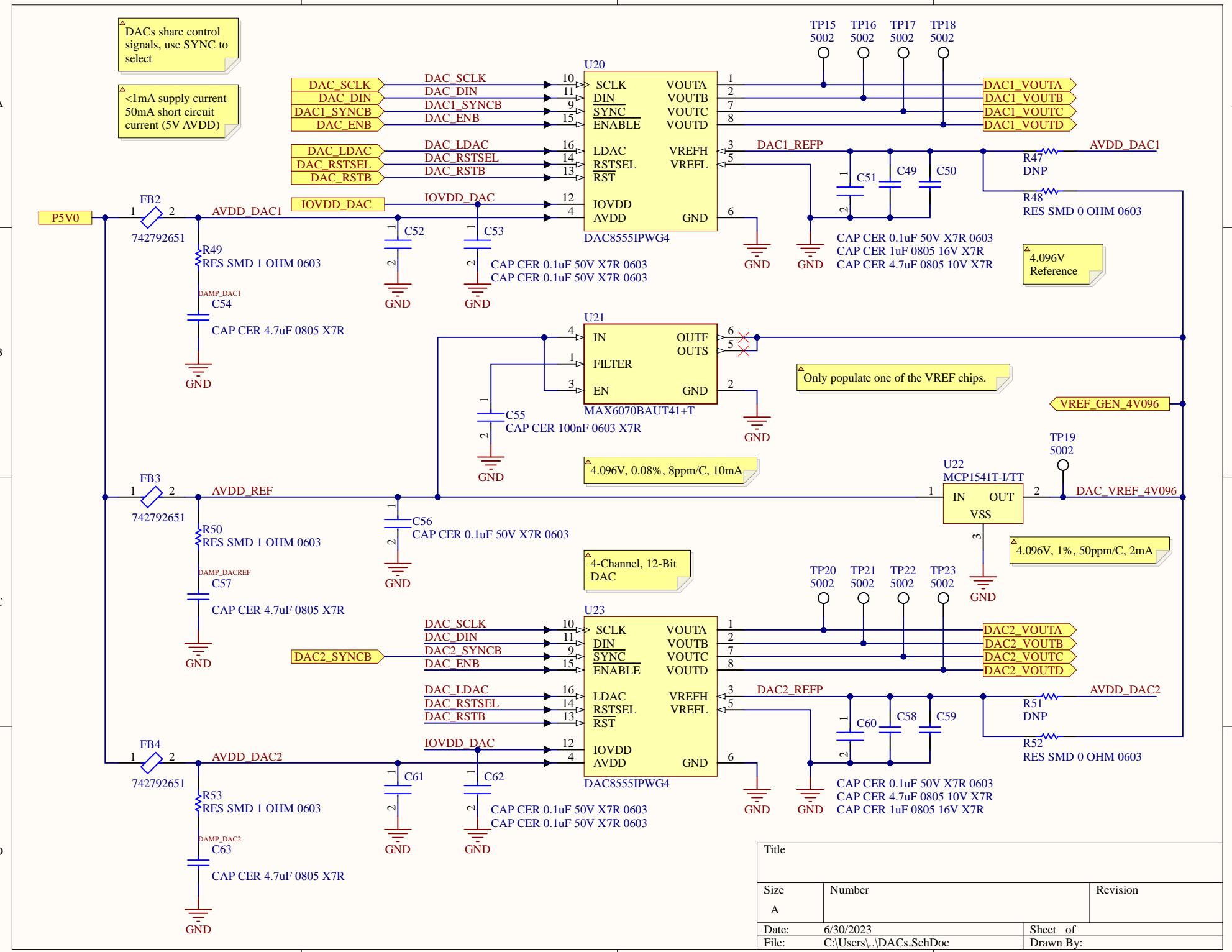
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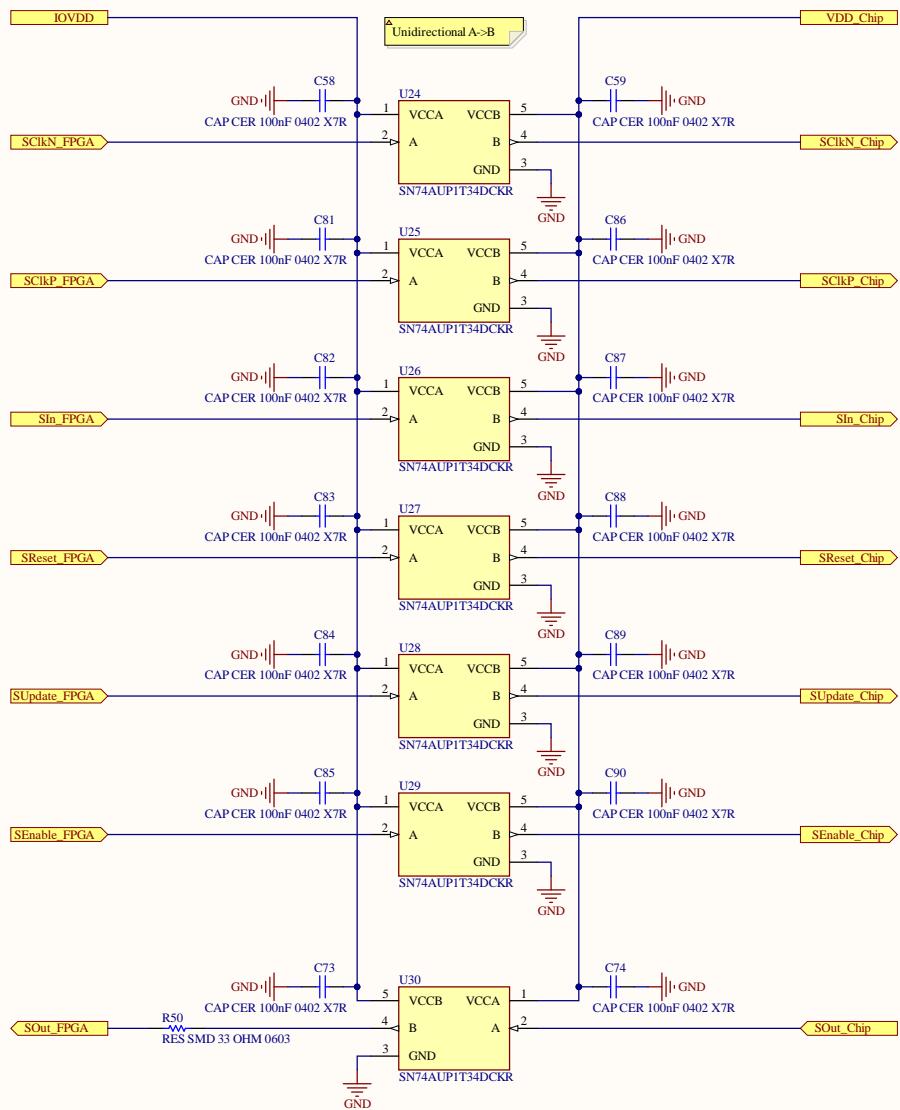
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todo: decaps (do we really need 1 per?)



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