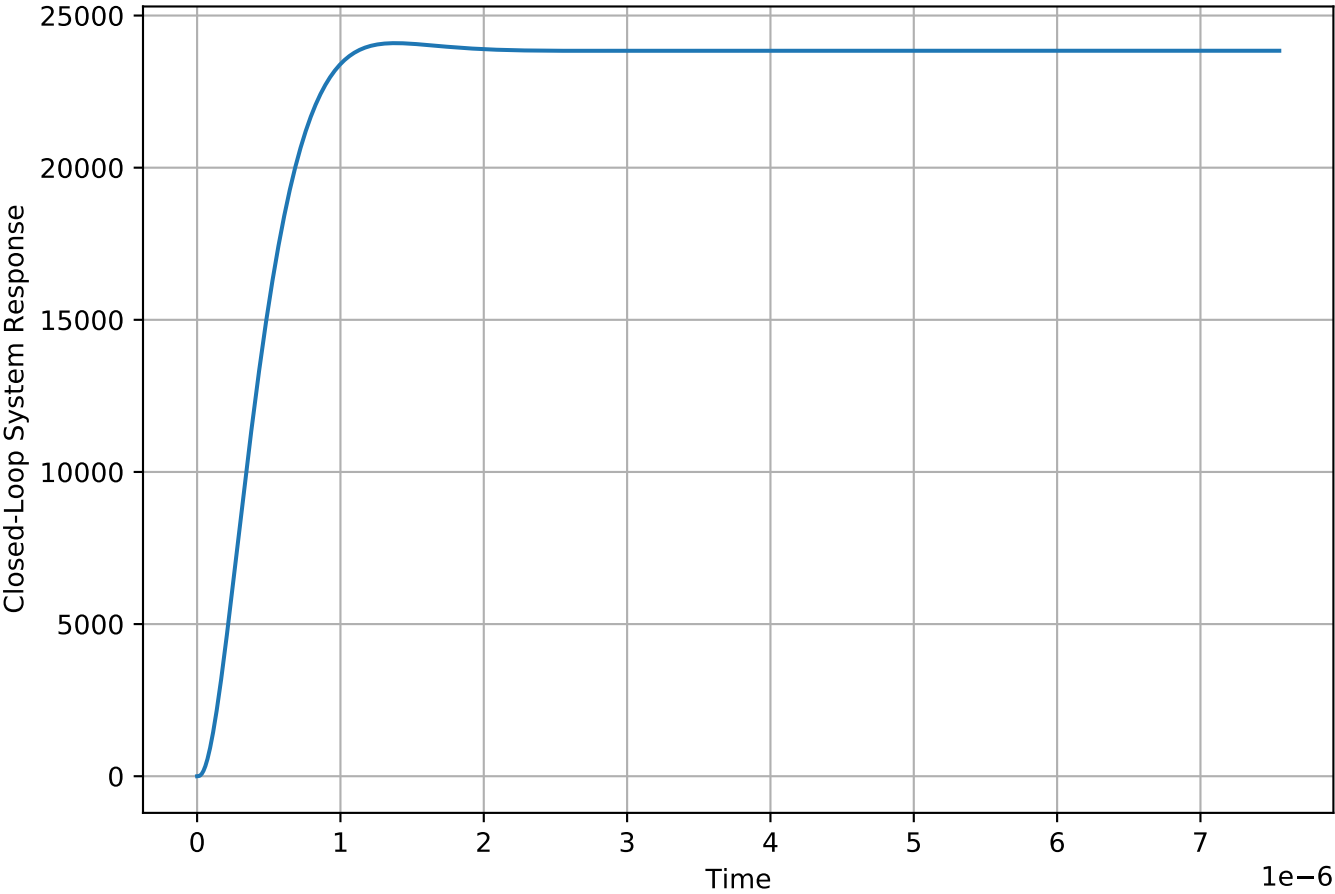


Spice Simulation for System with Phase-Margin=90 for Step Signal as Input



Spice Simulation for System with Phase-Margin=90 for Sinusoidal Signal as Input

