

```
L0: mov r1, addr
    ldr r2, [r1, #0]
    cmp r2, r3
    beq L2
```

Basic Block 0

```
L1: add r2, r2, r2
    sub r2, r2, r3
    add r2, r2, #1
    add r3, r3, #1
    jmp L3
```

Basic Block 1

```
L2: add r2, r2, r2
    add r2, r2, #1
    ldr r2, [r2, #0]
    mov r3, r2
```

Basic Block 2

```
L3: add r1, addr
    add r1, r1, r2
    add r1, r1, r3
    ldr r3, [r1, #0]
```

Basic Block 3