CPE301D - SPRING 2023

Design Assignment - KICAD

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Primary Github address: https://github.com/dlenzin15/submissions

Directory: submissions/tree/main/CPE_301_KiCAD_Final

Submit the following for all Labs:

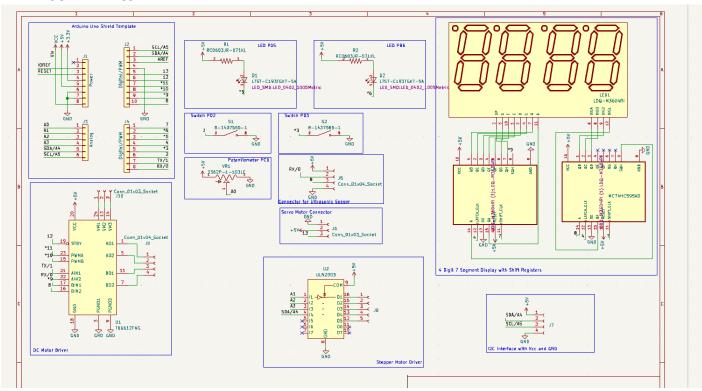
1. In the document, for each task submit the modified or included code (only) with highlights and justifications of the modifications. Also, include the comments.

- 2. Use the previously create a Github repository with a random name (no CPE/301, Lastname, Firstname). Place all labs under the root folder ESD301/DA, sub-folder named LABXX, with one document and one video link file for each lab, place modified asm/c files named as LabXX-TYY.asm/c.
- 3. If multiple asm/c files or other libraries are used, create a folder LabXX-TYY and place these files inside the folder.
- 4. The folder should have a) Word document (see template), b) source code file(s) and other include files, c) text file with youtube video links (see template).

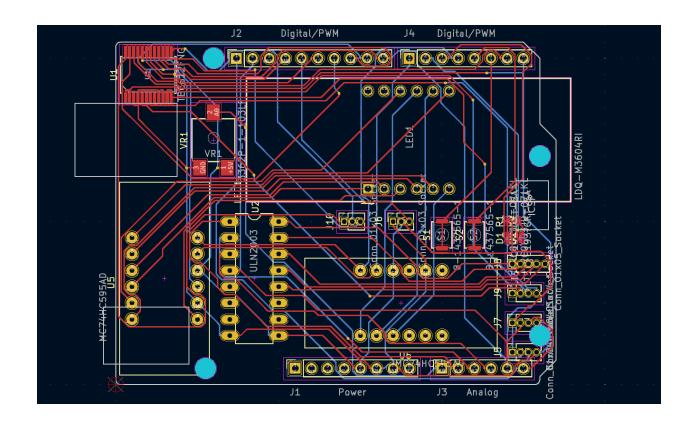
1. COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS

No hardware used. All design was done in KiCAD.

2. SCHEMATICS



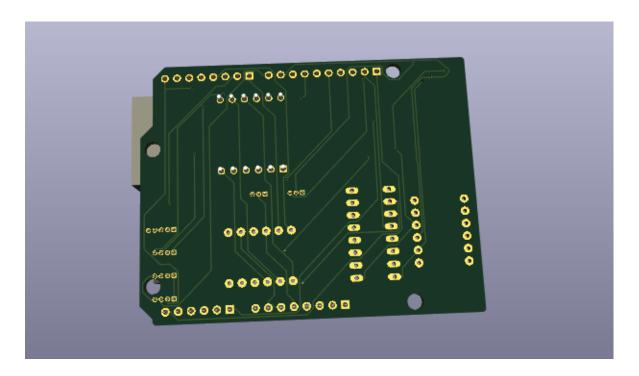
3. LAYOUT



4. 3D VIEW (Front & Back)



Back:



5. BILL OF MATERIALS

	Α	В	С	D	E	F	G	Н		J	K	L	M	
1 #		Reference	Qty	Value	Footprint	DNP								
2	1	D1, D2	2	LTST-C193	LED_SMD:	LED_0402_	1005Metr	ic						
3	2	J1	1	Power	Connector	r_PinSocket	_2.54mm:	PinSocket_	1x08_P2.54	mm_Verti	cal			
4	3	J2	1	Digital/PW	Connector	r_PinSocket	_2.54mm:	PinSocket_	1x10_P2.54	mm_Verti	cal			
5	4	J3	1	Analog	Connector	r_PinSocket	_2.54mm:	PinSocket_	1x06_P2.54	mm_Verti	cal			
6	5	J4	1	Digital/PW	Connector	r_PinSocket	_2.54mm:	PinSocket_	1x08_P2.54	mm_Verti	cal			
7	6	J5, J7, J9	3	Conn_01x	Connector	r_PinSocket	_1.27mm:	PinSocket_	1x04_P1.27	mm_Verti	cal			
8	7	J6, J10	2	Conn_01x	Connector	r_PinSocket	_1.27mm:	PinSocket_	1x03_P1.27	mm_Verti	cal			
9	8	J8	1	Conn_01x	Connector	r_PinSocket	_1.27mm:	PinSocket_	1x05_P1.27	mm_Verti	cal			
10	9	LED1	1	LDQ-M360	Display_79	Segment:CA	456-12CGK	WA						
11	10	R1, R2	2	RC0603JR-	Resistor_S	MD:R_020	1_0603Me	tric						
12	11	S1, S2	2	8-1437565	Button_Sv	vitch_SMD:	SW_Push_	SPST_NO_/	Alps_SKRK					
13	12	U1	1	TB6612FN	Package_S	O:SSOP-24	_5.3x8.2m	m_P0.65m	m					
14	13	U2	1	ULN2003	ULN2003/	A:DIL16								
15	14	U5, U6	2	MC74HC5	LDQ-M360	04RI (1):LD0	Q-M3604R	I						
16	15	VR1	1	3362P-1-1	Potention	eter_SMD:	Potentiom	eter_ACP_	CA6-VSMD_	Vertical				
17 18														
18														
19														

6. GITHUB LINK OF THIS DA

https://github.com/dlenzin15/submissions/tree/main/CPE 301 KiCAD Final

Student Academic Misconduct Policy

http://studentconduct.unlv.edu/misconduct/policy.html

"This assignment submission is my own, original work".

David Lenzin