CPE301D – SPRING 2023

Design Assignment - KICAD

Student Name: David Lenzin

Student #: 2001654470

Student Email: lenzin@unlv.nevada.edu

Primary Github address: <https://github.com/dlenzin15/submissions>

Directory: submissions/tree/main/CPE\_301\_KiCAD\_Final

Submit the following for all Labs:

1. In the document, for each task submit the modified or included code (only) with highlights and justifications of the modifications. Also, include the comments.
2. Use the previously create a Github repository with a random name (no CPE/301, Lastname, Firstname). Place all labs under the root folder ESD301/DA, sub-folder named LABXX, with one document and one video link file for each lab, place modified asm/c files named as LabXX-TYY.asm/c.
3. If multiple asm/c files or other libraries are used, create a folder LabXX-TYY and place these files inside the folder.
4. The folder should have a) Word document (see template), b) source code file(s) and other include files, c) text file with youtube video links (see template).

1. **COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS**

No hardware used. All design was done in KiCAD.

1. **SCHEMATICS**

A screenshot of a computer

Description automatically generated with low confidence

1. **LAYOUT**

A picture containing text, screenshot, circuit

Description automatically generated

1. **3D VIEW (Front & Back)**

Front:

A digital clock on a circuit board

Description automatically generated with medium confidence

Back:

A picture containing text, circuit

Description automatically generated

1. **BILL OF MATERIALs**

**A screenshot of a computer

Description automatically generated with medium confidence**

1. **GITHUB LINK OF THIS DA**

<https://github.com/dlenzin15/submissions/tree/main/CPE_301_KiCAD_Final>

**Student Academic Misconduct Policy**

<http://studentconduct.unlv.edu/misconduct/policy.html>

“This assignment submission is my own, original work”.

David Lenzin