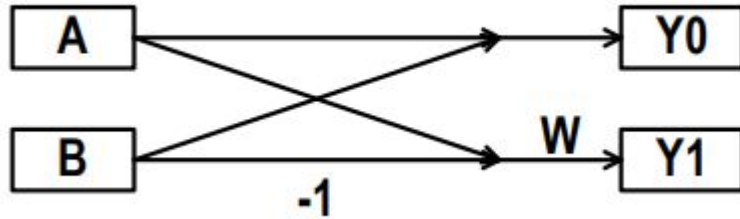
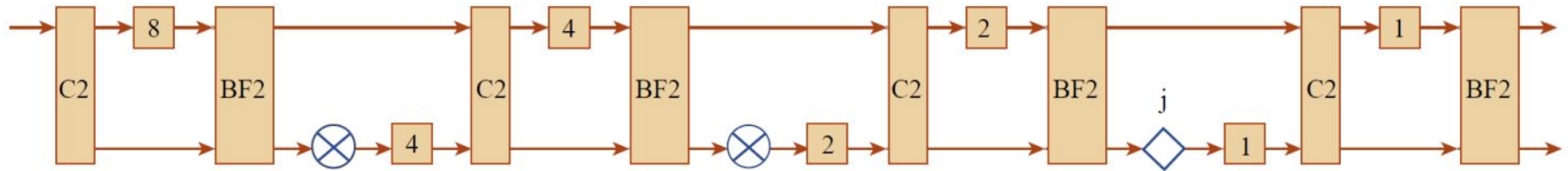


## Inverse FFT - Decimation in Time

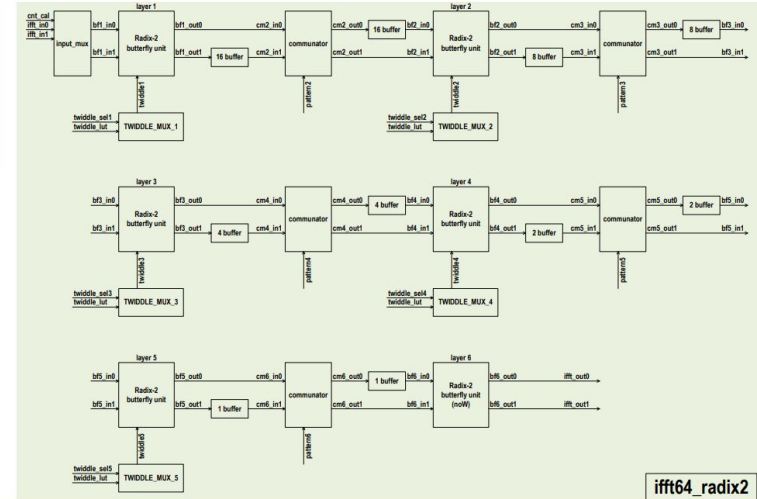
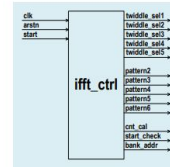
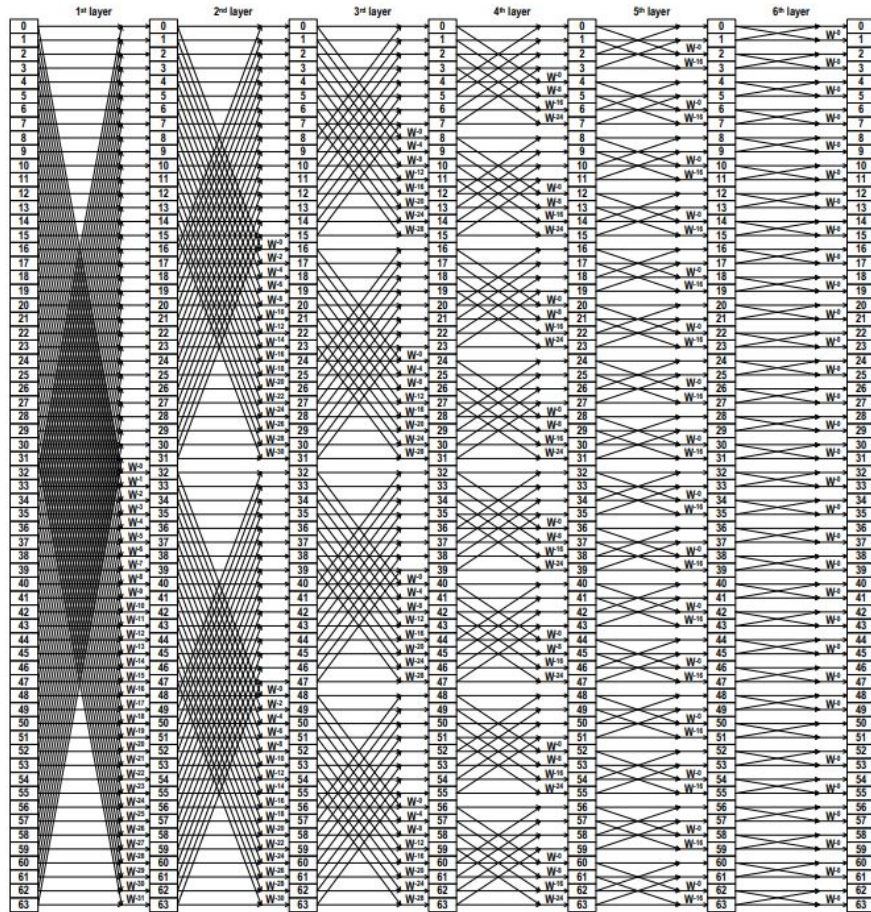


Basic building block is the Radix-2 Butterfly Unit



We will use multi-path delay commutator architecture

# Target - Build R2MDC for 64-bit input



ifft64\_radix2