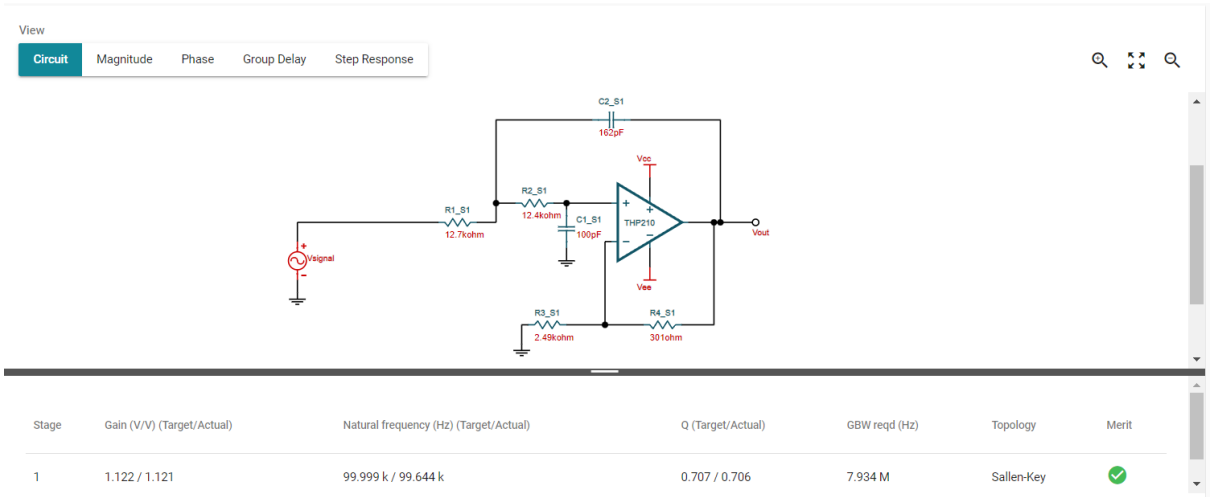
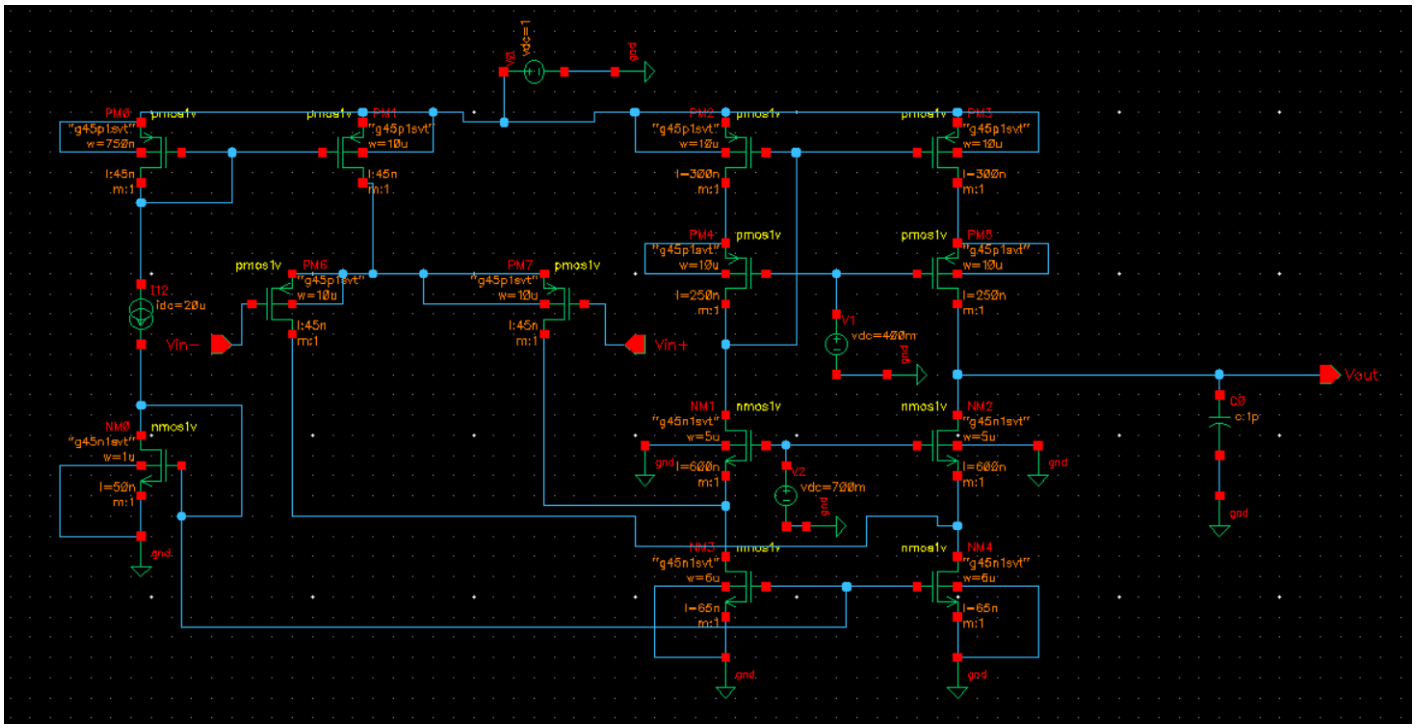
# 1. webench Circuit Diagram

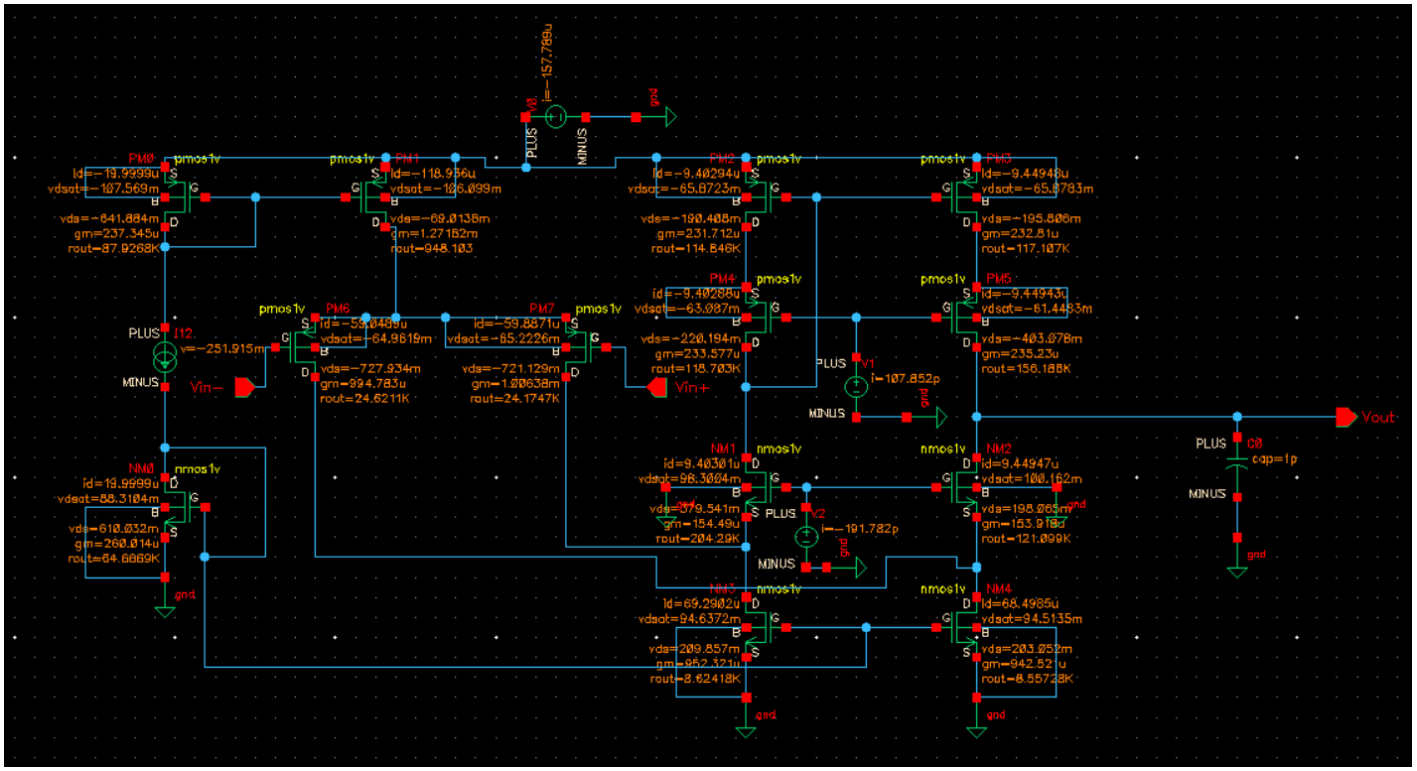



| Stage | Gain (V/V) (Target/Actual) | Natural frequency (Hz) (Target/Actual) | Q (Target/Actual) | GBW reqd (Hz) | Topology | Merit |
|---|---|---|---|---|---|---|
| 1 | 1.122 / 1.121 | 99.999 k / 99.644 k | 0.707 / 0.706 | 7.934 M | Sallen-Key | ✓ |

# 2. Transistor Sizings

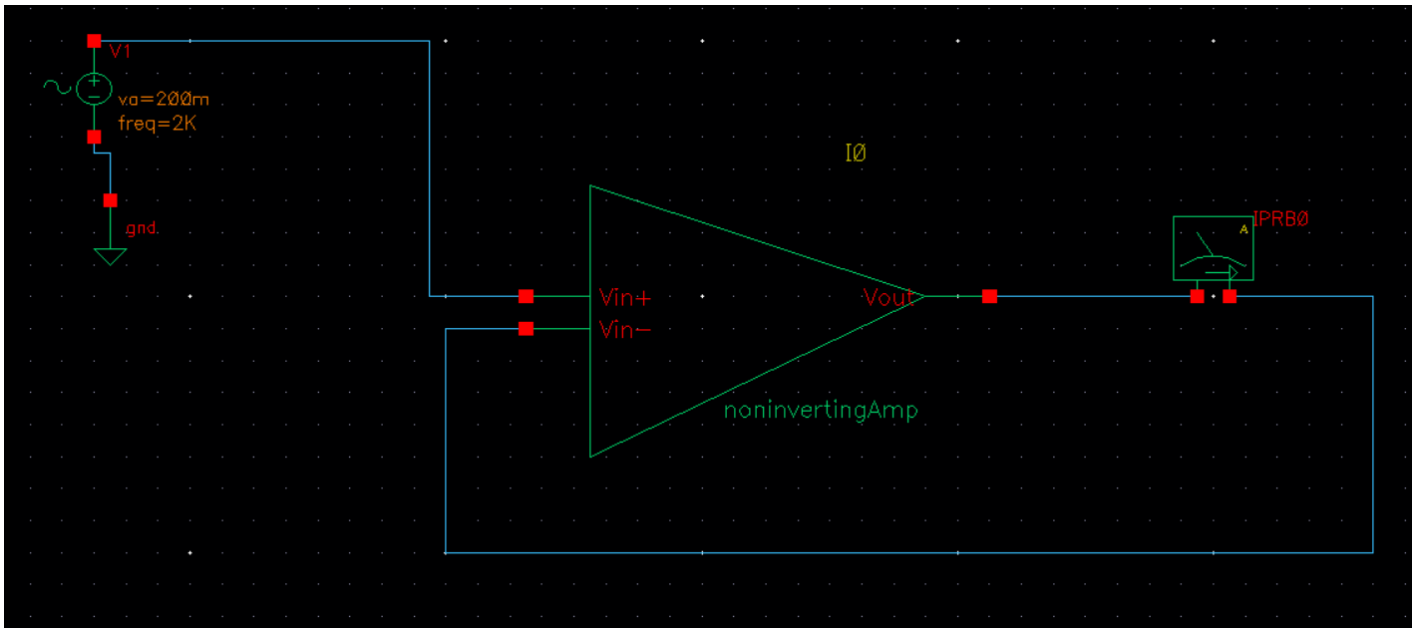


## 3. DC Operating Points



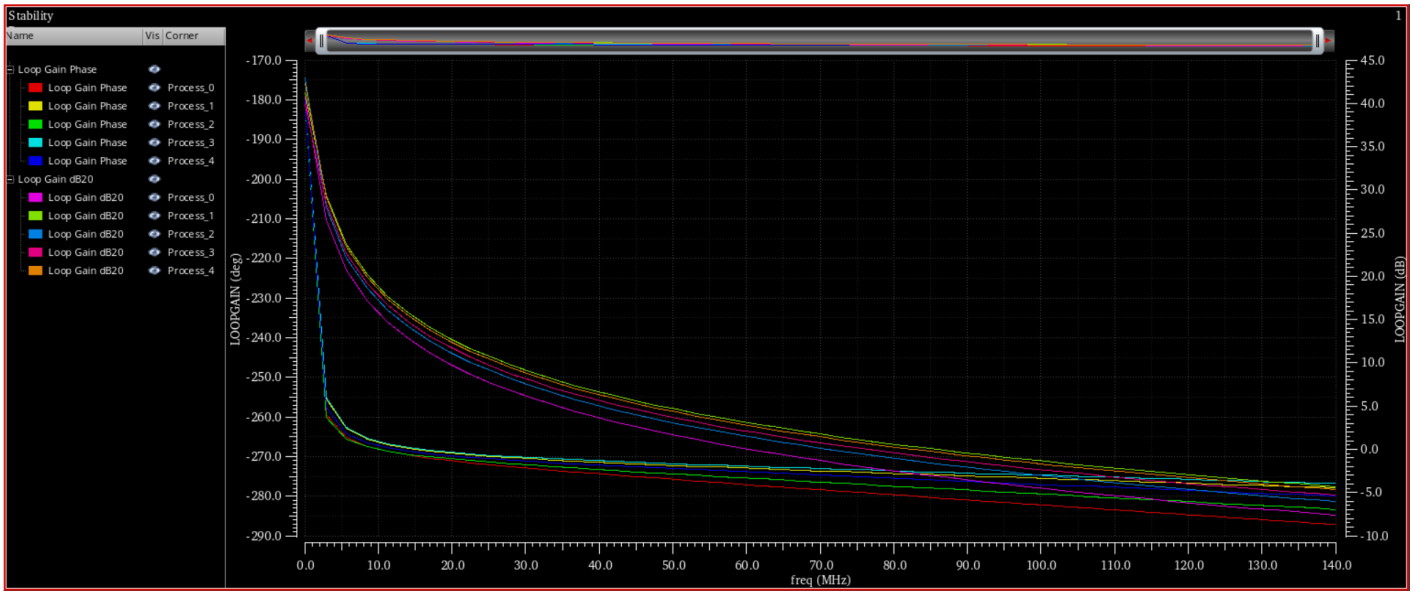

## 4. Op-amp STB analysis

### 4.1 - Setting up Unity Gain configuration

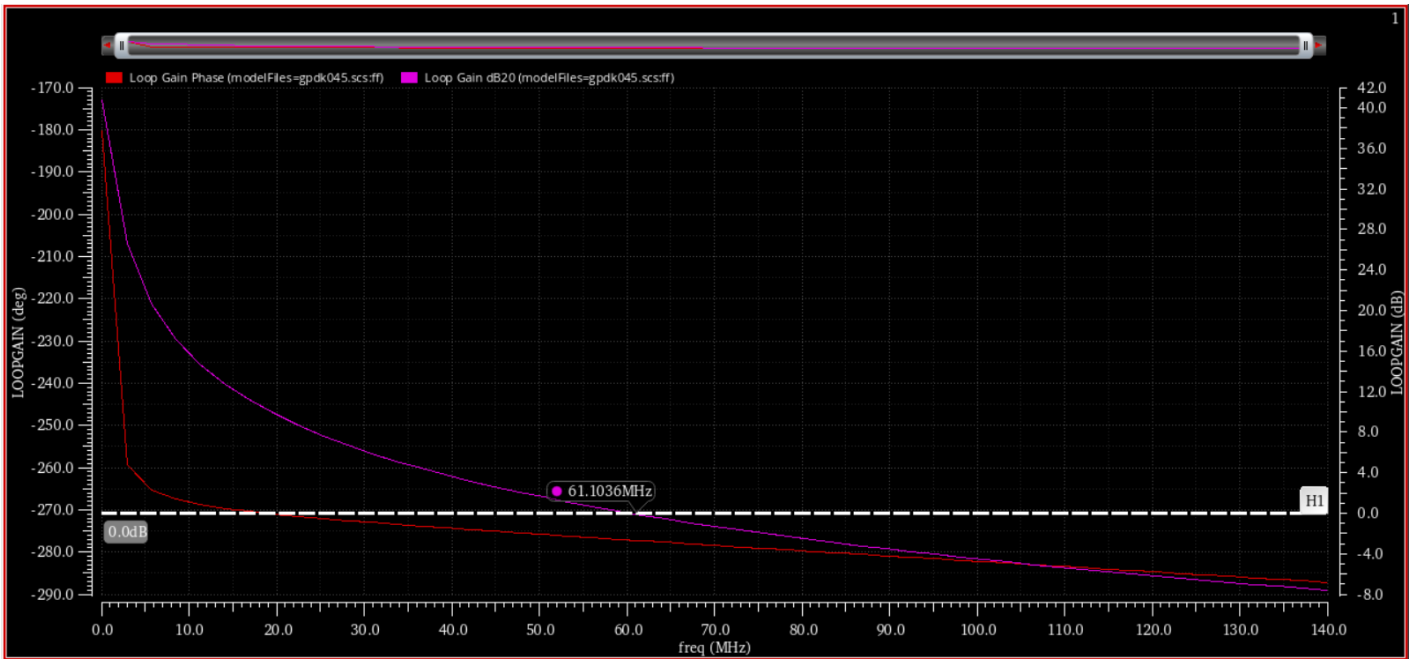


## 4.2 - Process Corner results (Summary)

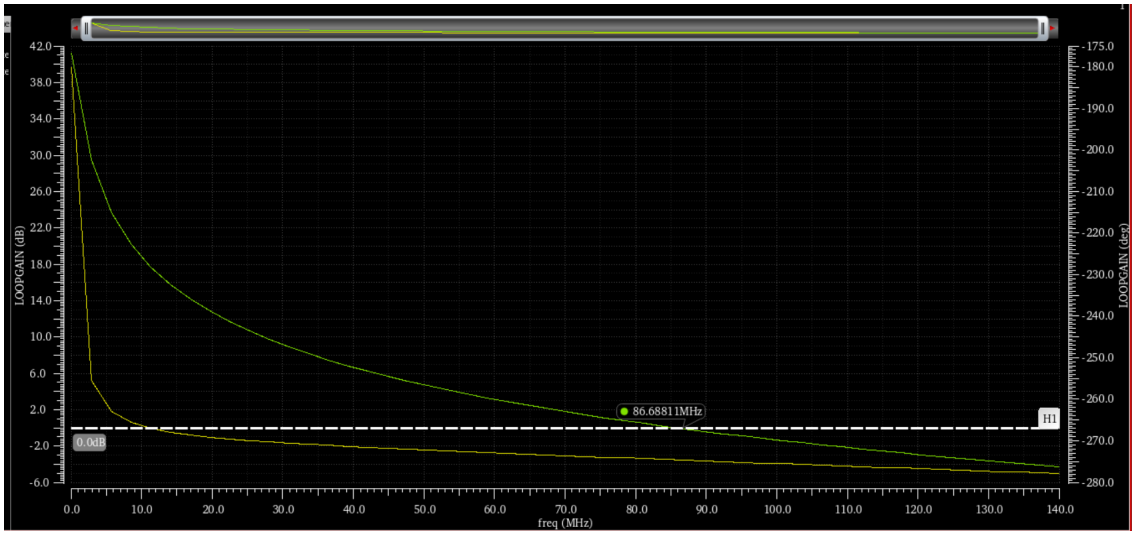
| Parameter | Process_0 | Process_1 | Process_2 | Process_3 | Process_4 |
|---|---|---|---|---|---|
| gpdk045.scs | ff, | fs, | sf, | ss, | tt |



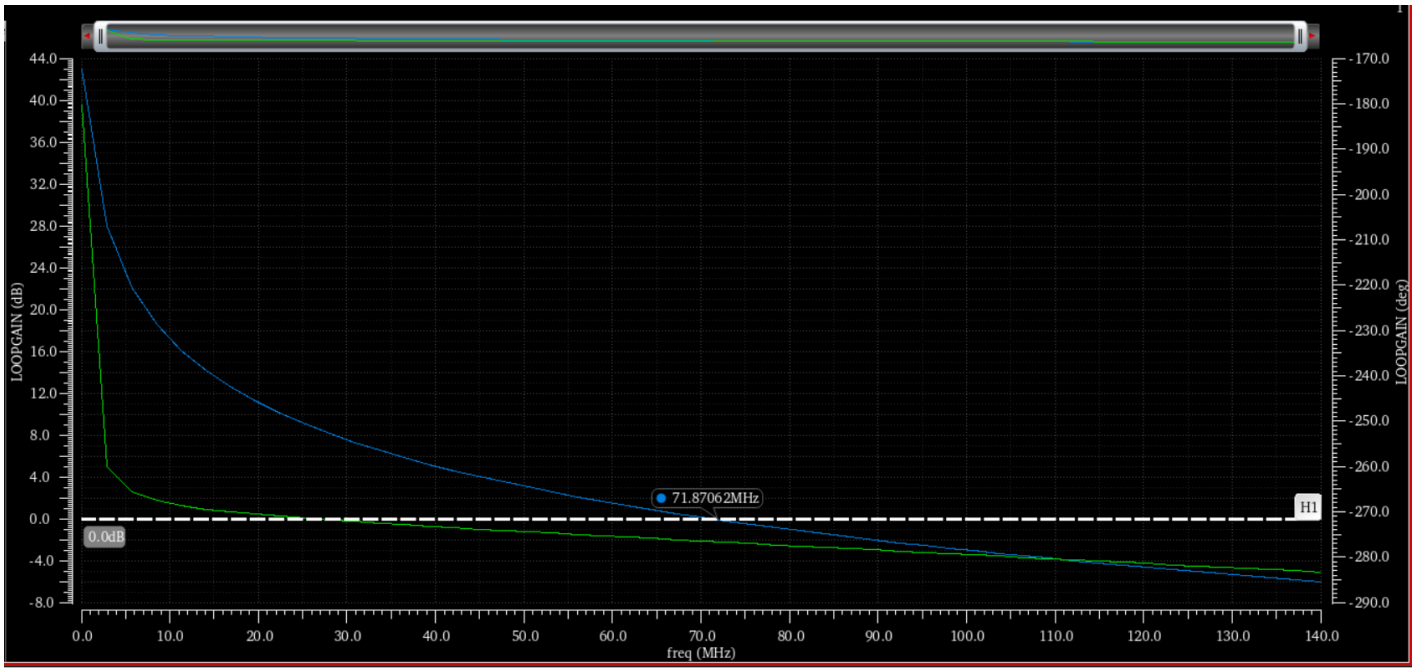

## 4.3 - ff corner



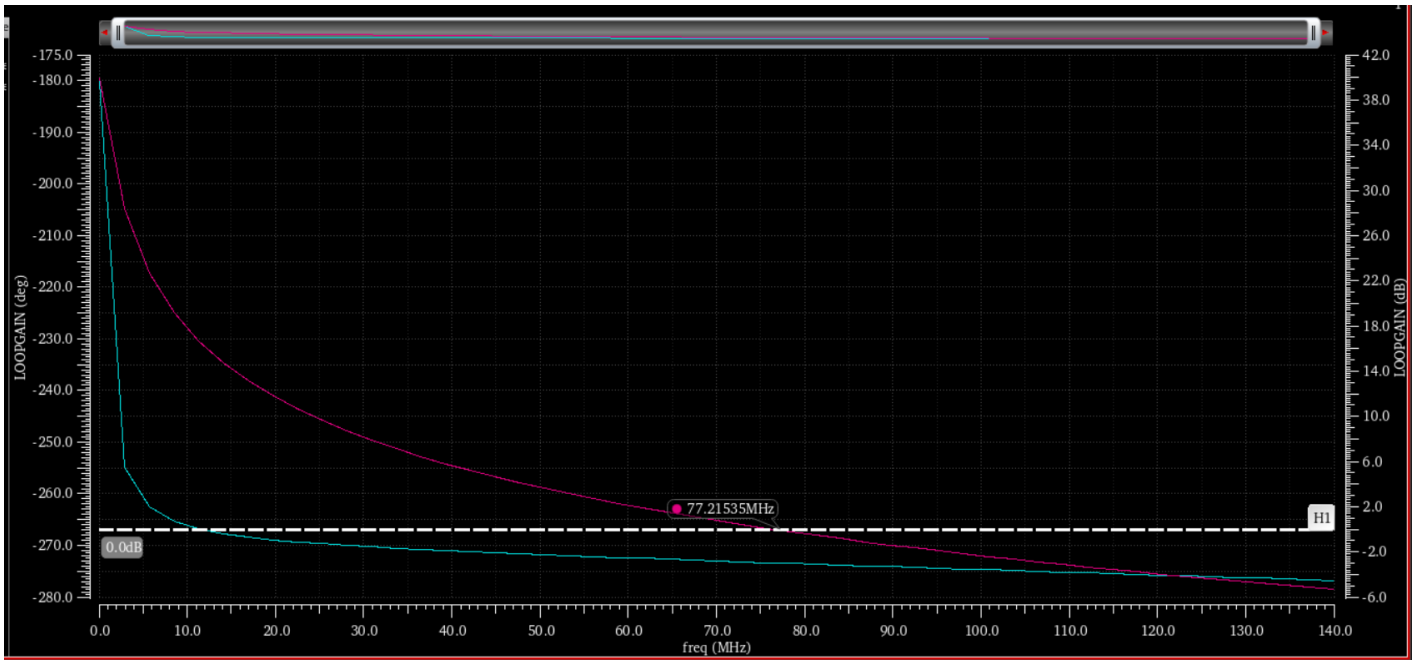

## 4.4 - fs corner



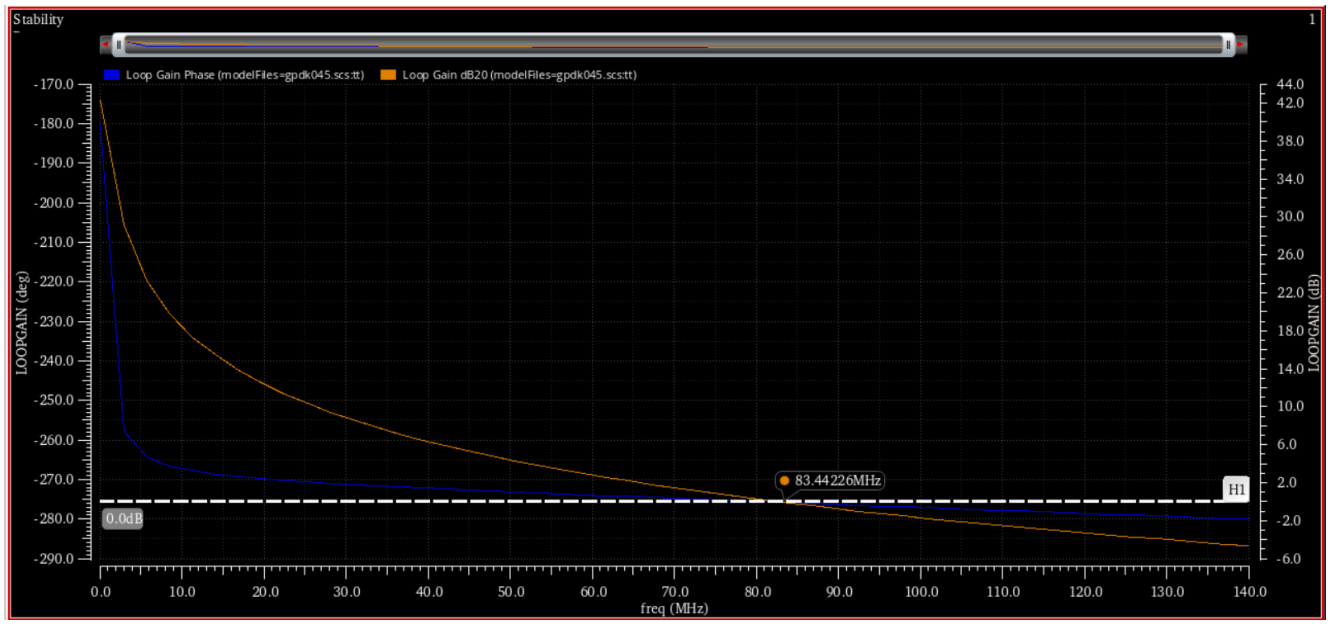
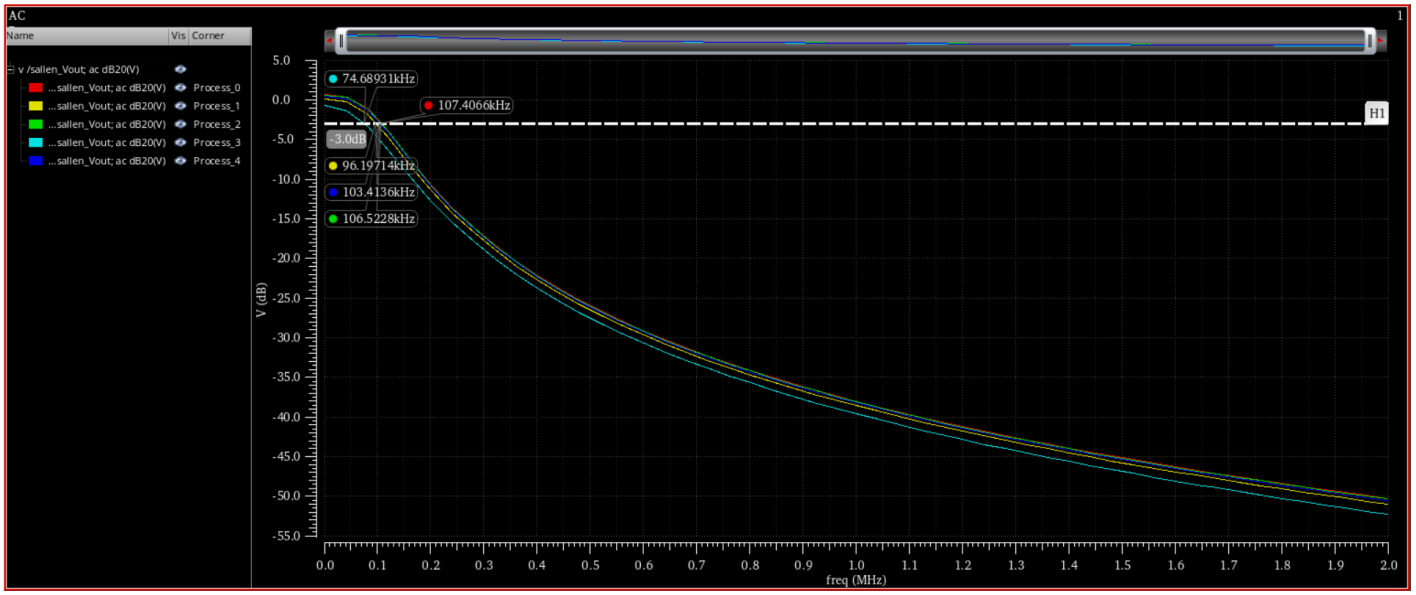
## 4.5 - sf corner




## 4.6 - ss corner




## 4.7 - tt corner



## 5. Magnitude response of Butterworth Filter