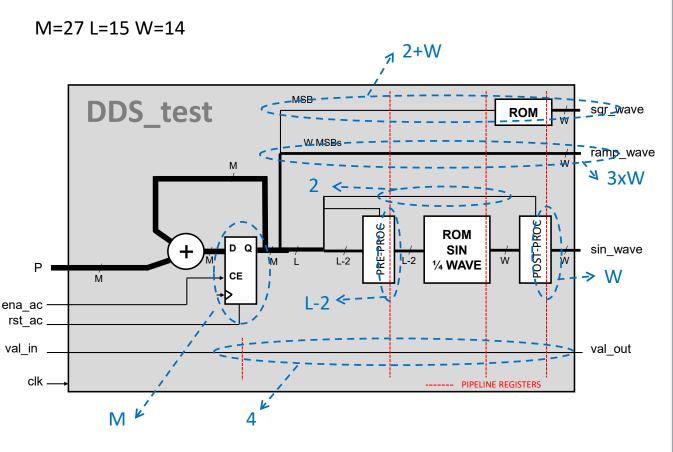
## Estimación de recursos

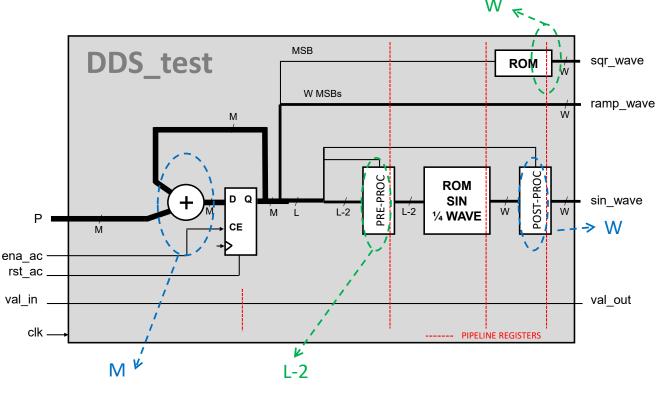


4+2+M+(L-2)+W+W\*3+2+W = 118

	Usage
▼ Total logic elements	114 / 114,480 ( < 1 % )
Combinational with no register	1
Register only	46
Combinational with a register	67
<ul> <li>Logic element usage by number of LUT inputs</li> </ul>	
4 input functions	0
3 input functions	39
<=2 input functions	29
Register only	46
✓ Logic elements by mode	
normal mode	29
arithmetic mode	39
▼ Total registers*	113 / 115,813 ( < 1 % )
Dedicated logic registers	113 / 114,480 ( < 1 % )
I/O registers	0 / 1,333 ( 0 % )
Total LABs: partially or completely used	12 / 7,155 ( < 1 % )
Virtual pins	0
✓ I/O pins	74 / 281 ( 26 % )
Clock pins	1 / 7 (14 %)
Dedicated input pins	0/9(0%)
M9Ks	14 / 432 (3 %)
	Register only Combinational with a register  V Logic element usage by number of LUT inputs 4 input functions 3 input functions <=2 input functions Register only  V Logic elements by mode normal mode arithmetic mode  V Total registers* Dedicated logic registers I/O registers  Total LABs: partially or completely used Virtual pins  V I/O pins Clock pins Dedicated input pins

## Estimación de recursos

M=27 L=15 W=14

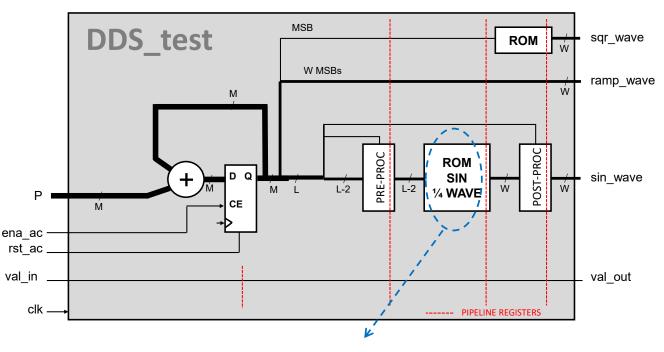


M+W = 41L-2+W = 27

	Resource	Usage
1	▼ Total logic elements	114 / 114,480 ( < 1 % )
1	Combinational with no register	1
2	Register only	46
3	Combinational with a register	67
2		
3	➤ Logic element usage by number of LUT inputs	
1	4 input functions	0
2	3 input functions	39
3	<=2 input functions	29
4	Register only	46
4		
5	✓ Logic elements by mode	
1	normal mode	29 I
2	arithmetic mode	39
6		
7	▼ Total registers*	113 / 115,813 ( < 1 % )
1	Dedicated logic registers	113 / 114,480 ( < 1 % )
2	I/O registers	0 / 1,333 (0%)
8		
9	Total LABs: partially or completely used	12 / 7,155 ( < 1 % )
10	Virtual pins	0
11	✓ I/O pins	74 / 281 ( 26 % )
1	Clock pins	1 / 7 (14 %)
2	Dedicated input pins	0/9(0%)
12		
13	M9Ks	14 / 432 (3%)

## Estimación de recursos

M=27 L=15 W=14



 $W \times 2^{(L-2)} \times 1 = 14 \times 2^{13} \times 1 = 14 M9K$ 

	Resource	Usage
1	▼ Total logic elements	114 / 114,480 ( < 1 % )
1	Combinational with no register	1
2	Register only	46
3	Combinational with a register	67
2		
3	<ul> <li>Logic element usage by number of LUT inputs</li> </ul>	
1	4 input functions	0
2	3 input functions	39
3	<=2 input functions	29
4	Register only	46
4		
5	✓ Logic elements by mode	
1	normal mode	29
2	arithmetic mode	39
6		
7	▼ Total registers*	113 / 115,813 ( < 1 % )
1	Dedicated logic registers	113 / 114,480 ( < 1 % )
2	I/O registers	0 / 1,333 ( 0 % )
8		
9	Total LABs: partially or completely used	12 / 7,155 ( < 1 % )
10	Virtual pins	0
11	✓ I/O pins	74 / 281 (26 %)
1	Clock pins	1 / 7 (14 %)
2	Dedicated input pins	0/9(0%)
12		
13	M9Ks	14 / 432 (3 %)