

Mnemonic	Op	Step	Micro-Ops	F/E	If	PC +1	Read From	Write To	ALU
JnS X	0000	00							
		01							
		10							
		11							
Load X	0001	00	MAR \leftarrow X	0	0	0	111	001	00
		01	AC \leftarrow M[MAR]	1	0	0	000	100	00
		10	-						
		11	-						
Store X	0010	00							
		01							
		10							
		11							
Add X	0011	00	MAR \leftarrow X	0	0	0	111	001	00
		01	MBR \leftarrow M[MAR]	0	0	0	000	011	00
		10	AC \leftarrow AC + MBR	1	0	0	100	100	01
		11	-						
Sub X	0100	00							
		01							
		10							
		11							
Input	0101	00							
		01							
		10							
		11							
Output	0110	00							
		01							
		10							
		11							
Halt	0111	00							
		01							
		10							
		00							

Mnemonic	Op	Step	Micro-Ops	F/E	If	PC +1	Read From	Write To	ALU
Skipcond	1000	00							
		01							
		10							
		11							
Jump X	1001	00							
		01							
		10							
		11							
Clear	1010	00							
		01							
		10							
		11							
Addl X	1011	00							
		01							
		10							
		11							
Jumpl X	1100	00							
		01							
		10							
		11							
Loadl X	1101	00							
		01							
		10							
		11							
Storel X	1110	00							
		01							
		10							
		11							