

# IE0521\_Temas&Libros

Tabla 1: Temas y libros

Tema	Libros
Repaso Jerarquías de memoria.	[2] AppendixB [3] cap1
Temas avanzados de memoria cache.	[2] cap2
Temas avanzados de memoria DRAM.	[3] cap7 & cap8 [1] cap 9.
Concepto de consistencia de memoria: modelos.	[6] cap1, cap2, cap3 [2] cap5
Introducción a la programación paralela.	[2] cap5 [5] cap1, cap2, cap3
Primitivas de sincronización.	[2] cap5 [5] cap1, cap2, cap3
Arquitecturas de memoria compartida centralizada	[2] cap5
Arquitectura de memoria compartida distribuida.	[2] cap5
Protocolos de coherencia Snooping	[2] cap5 [6] cap7
Protocolos de coherencia de directorios	[2] cap5 [6] cap6
ILP y Organización del procesador	[2] cap3
Multithreading	[2] cap3 [4] cap1, cap2, cap3, cap4 cap5

## Referencias

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- [2] Hennessy, John L. y David A. Patterson: *Computer Architecture, Fifth Edition: A Quantitative Approach*. Morgan Kaufmann Publishers Inc., San Francisco, CA, USA, 5th edición, 2011, ISBN 012383872X, 9780123838728.
- [3] Jacob, Bruce, Spencer Ng y David Wang: *Memory systems: cache, DRAM, disk*. Morgan Kaufmann, 2010.
- [4] Nemirovsky, Mario y Dean M Tullsen: *Multithreading architecture*. Synthesis Lectures on Computer Architecture, 8(1):1–109, 2013.
- [5] Scott, Michael L: *Shared-memory synchronization*. Synthesis Lectures on Computer Architecture, 8(2):1–221, 2013.
- [6] Sorin, Daniel J, Mark D Hill y David A Wood: *A Primer on Memory Consistency and Cache Coherence (Synthesis Lectures on Computer Architecture)*. Computer, 1005:30690, 2011.