F2C-ACC Workflow



- F2C-ACC translates Fortran code, with user added directives, in CUDA (relies on m4 library for interlanguages dependencies)
- Some hand coding could be needed (see results)
- Debugging and optimization Tips (e.g. Thread, block synchronization, out of memory, coalesce, occupancy....) are to be done manually

Compile and linking using CUDA libraries to create an executable to run