CP 331 Assignment 2: Parallel Merge Group 2

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0.1 Naive Implementation

This was our first approach at solving the problem. The goal was to get something working fast.

generate 2 sorted lists of size N_a , and N_b in two blocks of contiguous memory.

Each processor p calculates it's block of A to merge with the equations:

$$i_{a,start,p} = p \left\lfloor \frac{N_a}{P} \right\rfloor + \min(p, N_a \bmod P)$$
 (1)

$$n_{a,p} = \left\lfloor \frac{N_a}{P} \right\rfloor + \left\{ \begin{array}{ll} 1 & p < N_a \bmod P \\ 0 & \text{otherwise} \end{array} \right. \tag{2}$$

Where $i_{a,\text{start},p}$ is the start index of it's block into list A, and $n_{a,p}$ is the length of the block. Next process p calculates the block of B to merge with the block in A.

$$i_{b,\text{start},p} = \max\{x \in \mathbb{N} : B_x < A_{i_a,\text{start},p}^{-1}\} + 1$$
(3)

$$n_{b,p} = \max\{x \in \mathbb{N} : B_x < A_{i_{a,\text{Start},p} + n_{a,p} - 1}\} + 1 - i_{b,\text{start},p}$$

$$\tag{4}$$

We then merge these two lists with a sequential merge

This works well for small lists of sizes smaller than 2^{32} elements but becomes unwieldily with larger lists, since allocating this much contiguous memory will quickly become not practical.

0.1.1 Results

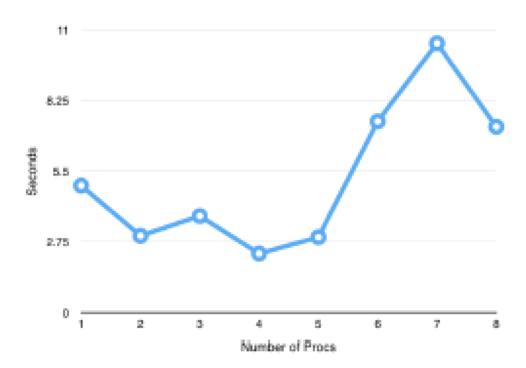


Figure 1. Performance on 10⁸ size lists

0.2 Virtual Memory Implementation

To solve the problem of allocating contiguous memory, we instead allocate a few pages of memory and and build some abstractions on top of them to give the impression of contiguous memory.

This still allows us to have fast random access into memory but without the requirement of large contiguous blocks of memory. The rest of the algorithm is exactly the same as in our naive approach.

0.2.1 Results

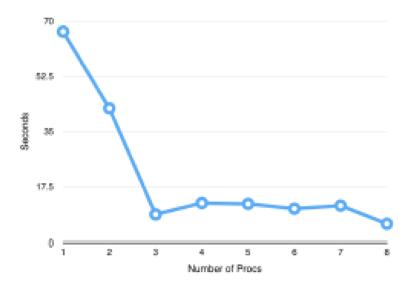


Figure 2. Performance on 10⁸ size lists

0.3 Binary Search

The linear search in the naive approach of the algorithm is suitable for most array sizes when the set is quite small. However, there are some cases where replacing it with a binary search can be more effective. Specifically, a linear search is used to find the last index of an element that is smaller (when creating partitions of the lists). The results of this are summarized below:

0.3.1 Distances are fairly equal between gaps

When the number of partitions and the gaps between the partitions are fairly equal, the binary search performs at an acceptable level. There is a small, but not significant speed up (since only about half the time is spent in this search? and this is serially, so the less the better). However, this is one of the few cases where the speed up is able to make a difference. When we use Linear Search, the search will remain fairly constant and the CPU cache is able to accurately predict what to store. When we do the same thing with binary search with consistent chucks, we see results that are similar. This would seem reasonable, considering linear search is on the order of and binary search is logarithmic in comparison. However, in reality there are some other factors that make this not always true?

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0.3.2 When gaps are not equal

If your data source gaps are not neatly compacted or the data source has suffered from abnormal randomly, you may have runs where the binary search is significantly slower. Binary search incurs some overhead from its use which is often neglected when studying from a purely Big-O standpoint. There are two things to consider when comparing it with linear search

- In Linear Search, the CPU cache is on your side. The processor has already decided to keep the memory chunks ahead of you in memory and that means access to them will be quick. In contrast, Binary Search does not have this advantage as it may be jumping around quite a few points before settling down on a range.
- There is purely computational overhead in Binary Search. Branching is often neglected and other minor things in Big-O notation which become relevant here.

So, these two things help us draw a conclusion after many benchmarks.

- 1. If the number of iterations required for linear search is relatively low (10,000 comparisons is a low-figure that is fair from testing), then it can actually be faster to use it instead of Binary Search, despite Binary Search being able to do this theoretically quicker. We have the CPU Cache to blame for this.
- 2. The other interesting thing to note however, is the implication of a parallelization this would have if it was extended to be parallelized somehow. As the processor count goes up, the effectiveness of a binary search decreases, as the range clearly shrinks. To combat this problem, further heuristics which could select between the two algorithms would be ideal.

0.4 Regarding heuristics for algorithms

A good technique for doing this often deployed in High Performance Computing which allows code to be portable across many platforms is pre-benchmarking. In a pre-benchmarking situation, the software will run a set of tests on various input sizes on the test data and measure the time required for each. Each test is an algorithm or combination of algorithms? which together make up a strategy. Then, the real execution uses the best strategy to execute on the full set of data. Since certain strategies work on different sets of data and hardware differently, having a few different ones and automatic selection is a big boon. This applies with the linear vs. binary situation as well, in these cases they are both strategies.

To maximize gains in these situations, it would be possible to setup a pre benchmark for these two different strategies and at the start of the launch, pick the best one for the current situation

0.5 Pooled Memory Implementation

Merging 2 ordered lists is a fairly simple task with contiguous lists as you can see above. The difficulty comes when trying to merge 2 lists so large that the resulting list is larger than the heap size of the system they are accessed on. This problem has implications in almost every field that uses "Big Data" and finding a solution to this problem was a vital step in completing this assignment.

To solve this we implemented a linked list of buckets where each bucket held a slice of lists A and B which will be merged. With this we can control the the size of each allocated block of contiguous memory. To take this a step further we aligned memory on a 32byte boundary each time it was allocated. Because we targeted the Intel Xeon processors on the ORCA cluster of SHARCNET we knew that each processor has a 64 byte cache line size. By investigating further we found that 32 bytes of this are reserved for instructions. By aligning allocated memory on a 32 byte boundary and controlling the size of each bucket to make sure it is divisible by 32 we limited potential L1 cache misses.

This allowed us to achieve the maximum speed when generating the lists. Since the list generation is non-trivial and only needed to be done by one processor we decided for the work division to use $2^n + 1$ cores. This allowed the master processor to spend time generating the lists then sending them out then receiving them and doing the final compilation. Using this scheme we were able to maintain a processor utilization of above 98% at all times during the run.

Since we used a linked list of buckets, dividing the tasks to the remaining processors was a trivial task. Each processor receives buckets in order and splits their lists recursively into individual elements then merges up to a final list. This final list is then sent back to the master processor which outputs the final result to a file. The result is not as fast as the naive solution however it is far more scaleable when using large sets of data.

0.5.1 The Code

```
1 #include <stdio.h>
2 #include <stdlib.h>
3 #include <stdint.h>
4 #include <mpi.h>
5 #include <time.h>
7 //DEFINITIONS
8 #define NUM ELEMENTS
                            (1 << 32) // 2^32
9 #define MASTER
      If the size of a pointer is 4 bytes (32 bit arch)
11 ///
  /// then to align the size of (bucket) on a 2^n boundry
13 /// we must reserve enough space for the pointer to the
14 /// next bucket. If it is 8 we have to do a bit more work
        SIZEOF_POINTER_ = 4
16 #if
17 #define BUCKETSIZE (1 << 4)
             SIZEOF POINTER
18 #elif
19 #define BUCKETSIZE (1 << 5)
20 #else
21 #error
           " [ - ] sizeof(void*) is FUCKED"
22 #endif
24 //TYPE DEFININTIONS
25 typedef uint32 t
                       1132:
26 typedef uint64 t
                       u64:
27
28
  //EXPLINATION:
29
       u32 = 4 bytes
30 //
       struct bucket = 4 or 8 bytes
```

```
data [BUCKETSIZE] . a = 4 * BUCKETSIZE BYTES
                         .b = "
33 //
34 //
        to align on 2^n boundry to maximize storage
35
        u32 must be added when there is an 8 byte
36 //
        pointer (64 bit arch) this adds an extra 4
        bytes of padding to keep the sturct aligned in RAM
38 //
39 //
40
41 typedef struct bucket {
42
       struct
                    bucket
                              *next;
                    count;
       u32
43
         SIZEOF\_POINTER\_\_ = 8
44 #if
       \overline{u32}
45
                    _;
46 #endif
47
       struct {
                    u32 a;
                            // list a
48
                             // list b
                    u32 b;
49
       }data[BUCKETSIZE];
50
51
52 } bucket;
53
54 #define newbucket() do { uintptr_t *start; bucket *b; \
                              b = get mem((u32) size of(bucket));
                              b->next = master\_list; b->count = 0; \setminus
56
                              master_list = b; \
                              total buckets++; \ \ \ while (0)
58
59
60 void *get_mem(u32 amt);
61 u32 *merge(u32 *sub list, u32 block size, u32 *sub list2, u32 block size2);
62 void split and merge (u32 *sub_list, u32 min, u32 max);
63
64 int main(int argc, char **argv)
65 {
66
       if (MPI_Init(&argc,&argv) != MPI_SUCCESS) {
67
68
           printf(" [ - ] Filled to initialize MPI... Exiting");
           exit(-1);
69
       };
70
71
       int id, procs;
72
       int TAG_A = 1;
73
       int TAG B = 2;
74
75
       int TAG C = 3;
76
       MPI_Status status;
77
78
       MPI_Comm_rank(MPI_COMM_WORLD,&id);
       \label{eq:mpi_comm_size} $$ MPI\_COMM\_WORLD \& procs ) ;
79
80
       double wall_time1 = MPI_Wtime();
81
82
       // count the total numbers generated and placd in buckets as well as the
83
       u64 \text{ count} = 0;
84
85
86
       // total number of buckets which have been created.
87
       u32 total buckets = 0;
88
       u32
             buckets\_per\_proc = NUM\_ELEMENTS/BUCKETSIZE/procs + 1;
89
```

```
if (id = 0)// MASTER
        {
91
                                  *master_list;
92
            bucket
93
            uintptr_t
                                  *start_addr;
            master\_list = NULL;
95
            newbucket();
96
97
            printf(" [ + ] NOTE - sizeof(void*) = %d\n", __SIZEOF_POINTER__);
98
            printf(" | + | BUCKETSIZE = \%'d\n", BUCKETSIZE);
99
            printf(" [ ? ] NUM_ELEMENTS = %u\n", NUM_ELEMENTS);
100
            srandom(time(NULL));
102
            double start = clock();
            while (count < NUM ELEMENTS)
105
106
            {
                 if ( master list->count >= BUCKETSIZE)
                     newbucket();
108
109
                u32 i = master list \rightarrow count ++;
110
                 /// ALL THE REQUIREMENTS WERE FOR RANDOM!!! AHAHAHA
112
                 u32 = NUM ELEMENTS - (count + (random() \% 2)),
113
                    b = NUM\_ELEMENTS - count;
114
115
                 master\_list->data[i].a = a;
116
                 master_list->data[i].b = b;
118
                 count++;
119
120
121
            double serial_time = clock() - start;
            printf(" [ + ] Generated %lu sorted random numbers in 2 lists\n", count*2);
            printf(" [ + ] This took a total of %.5fseconds\n", serial time/
126
       CLOCKS_PER_SEC);
127
            printf("\ [\ +\ ]\ Total\ number\ of\ buckets\ generated = \%u \backslash n", total\_buckets\ );
128
            buckets_per_proc = total_buckets/(procs - 1);
129
130
            printf(" [ + ] Total number of buckets per processor = \%u\n",
        buckets_per_proc);
132
              / SEND BUCKETS TO MPI COMM WORLD
133
            MPI_Bcast(&buckets_per_proc, 1, MPI_INT, MASTER, MPI_COMM_WORLD);
134
135
136
            u32 p;
            u32 *temp_mem_a, *temp_mem_b;
137
            temp mem a = malloc(BUCKETSIZE * sizeof(u32));
139
            temp mem b = malloc(BUCKETSIZE * sizeof(u32));
140
141
            for(p = 1; p < procs; p++)
142
143
                 if \ (temp\_mem\_a == NULL \ | \ | \ temp\_mem\_b == NULL) \{
144
```

```
fprintf(stderr, " [ - ] Error getting memory for temp lists\n");
146
                           \operatorname{exit}(-1);
147
                    u32 j;
149
                    for (j = 0; j < buckets per proc; j++) {
                          u32 i = 0;
                          for (i = 0; i < BUCKETSIZE; i++){
153
                               temp_mem_a[i] = master_list->data[i].a;
                               temp_mem_b[i] = master_list->data[i].b;
156
157
                          }
158
                          // SEND THE LISTS TO PROC P
159
                          MPI_Send(temp_mem_a, BUCKETSIZE, MPI_INT, p, TAG_A, MPI_COMM_WORLD);
                         \label{eq:mpmemb} MPI\_Send (temp\_mem\_b, BUCKETSIZE, MPI\_INT, p, TAG\_B, MPI\_COMM\_WORLD);
161
162
                          \begin{array}{ll} \textbf{if} \, (\, \texttt{master\_list} \! - \! \! > \! \texttt{next} \; \mathrel{!=} \; \texttt{NULL}) \end{array}
163
                               master list = master list -> next;
164
                    }
                    printf(" [ + ] Buckets sent to Proc %d\n", p);
168
169
               }
171
172
               free (temp_mem_a);
               free (temp_mem_b);
173
174
               free (master list);
175
176
         } else// SLAVE
177
         {
               u32
                          *list\_a \;,\; *list\_b \;,\; *list\_c \;,\; *list\_t \;;
179
180
                 / THIS IS CUTE... IT PAUSES THE SLAVES UNTIL THEY HEAR FROM THEIR MASTER
181
               \label{eq:mpi_bound} $\operatorname{MPI\_Bcast}(\&\operatorname{buckets\_per\_proc}\;,\;\;1\;,\;\;\operatorname{MPI\_INT}\;,\;\;\operatorname{MASTER}\;,\;\;\operatorname{MPI\_COMM\_WORLD})\;;
182
               u32 min = NUM ELEMENTS, max = 0;
184
185
               list\_a \ = \ malloc (BUCKETSIZE*buckets\_per\_proc \ * \ sizeof (u32));
186
               list\_b = malloc(BUCKETSIZE*buckets\_per\_proc * sizeof(u32));
187
               list_c = malloc(2 * buckets_per_proc * BUCKETSIZE * sizeof(u32));
               u32 i;
189
               u32 a=0, b=0;
190
               for(i = 0; i < buckets\_per\_proc; i++)
191
192
                    list_t = malloc(BUCKETSIZE * sizeof(u32));
193
                    u32 m;
194
                    \label{eq:mpi_recv} MPI\_Recv(list\_t\ ,BUCKETSIZE, MPI\_INT, MASTER, TAG\_A, MPI\_COMM\_WORLD, \& status) \ ;
                    split_and_merge(list_t,0,BUCKETSIZE-1);
196
                    for (m=0;m<BUCKETSIZE;m++){</pre>
197
198
                          list_a[a++] = list_t[m];
199
                    \label{eq:mpi_recv} MPI\_Recv(\,list\_t\ ,BUCKETSIZE,MPI\_INT,MASTER,TAG\_B,MPI\_COMM\_WORLD,\&\,status\,)\,;
201
```

```
split and merge(list t,0,BUCKETSIZE-1);
                 for (m=0;m<BUCKETSIZE;m++){
203
                      list_b[b++] = list_t[m];
204
205
206
                 free(list t);
             }
208
209
210
211
                      list_c = merge(list_a, a, list_b, b);
212
213
                214
215
216
217
218
219
221
222
223
224
225
226
227
228
229
230
231
232
233
234
235
                     QUACK!
237
238
239
            \label{eq:mpi_send} MPI\_Send(\,list\_c\,\,, a+b\,, MPI\_INT\,, MASTER, TAG\_C\,, MPI\_COMM\_WORLD)\;;
240
             printf("\ [\ +\ ]\ Proccessor\ \%d\ is\ sending\ its\ merged\ list\ to\ MASTER\n",id);
241
            free(list_a);
free(list_b);
242
243
             free(list_c);
244
245
246
247
        if (id == 0) // MASTER
248
        {
249
             FILE* f;
250
             f = fopen("c.list", "w");
251
252
             u32 p;
253
             for(p=1; pprocs; p++)
254
255
                 u32 * list c;
256
                 u32 c_size = 2 * buckets_per_proc * BUCKETSIZE;
257
                 list_c = malloc(c_size * sizeof(u32));
258
```

```
\label{eq:mpi_recv} MPI\_Recv(\,list\_c\;,\;\; c\_size\;,\;\; MPI\_INT,p\;, TAG\_C, MPI\_COMM\_WORLD,\;\; \&status\,)\;;
260
                 printf(" [ + ] MASTER recieved Buckets sent by Proc %d\n", p);
261
                 u32 i:
263
                 if (list c == NULL) {
                     fprintf(stderr, " [ - ] Error receiving list!\n");
265
                     exit(-1);
266
267
268
                 printf(" [ + ] PRINTING TO FILE! \n");
                 for (i = 0; i < c_size; i++){}
                     fprintf(f, "%u \setminus n", list_c[i]);
271
272
273
                 free(list_c);
275
            fclose(f);
276
            double wall time2 = MPI Wtime();
277
278
            printf(" [ + ] Program took %.5f seconds to merge %lu elements\n",
279
                     wall\_time2 \ - \ wall\_time1 \ , (\ u64) \ \ NUM\_ELEMENTS*2) \ ;
280
            printf(" [ + ] Program \ finished \ successfully ! \ 'n");
281
282
283
        MPI_Finalize();
284
285
286
        return 0;
287 }
        get memory aligned to 32 byte boundry. Intel Xeon processors on the ORCA cluster
289 ///
       have a cache-line size of 64 bytes. 32 bytes are for program memory and 32 bytes
290 ///
       are for instructions. By aligning the memory on a 32 byte boundry it makes it
292 /// easier for the processor to split and process the chunks of allocated memory.
293 void *get_mem(u32 amt)
294 {
295
        uintptr_t
                     mem_start;
296
        mem_start = (uintptr_t) malloc((size_t)(amt + 31));
297
        if ((void *) mem_start == NULL)
298
299
            fprintf(stderr," [ - ] Failed to Allocated memory in get_mem function after
300
                     allocating %fMB on the heap\n",
301
                     (\verb"double") total\_buckets*sizeof(bucket)/1024.0/1024.0);
            exit(-1);
303
304
        // accidentally had this aligned on a 33 byte boundry and it had a 2-3x slow
305
306
        // down depending on the node
        mem_start = (mem_start + 31) & ~(uintptr_t)31;
307
        return (void *)mem_start;
308
309 }
310
      // merge the two lists while keeping the integrety of the order
311
312 u32 *merge(u32 *sub_list, u32 block_size, u32 *sub_list2, u32 block_size2)
313 {
314
        u32 i1, i2, merged_index;
315
```

```
u32 *merged list;
        u32 merged_size = block_size + block_size2;
317
318
319
        i1 = 0;
        i2 = 0;
320
        merged index = 0;
322
        merged_list = (u32 *) malloc(merged_size*sizeof(u32));
323
324
        while ((i1 < block_size) && (i2 < block_size2)) {</pre>
325
326
            if (sub_list[i1] <= sub_list2[i2]) {
                merged_list[merged_index] = sub_list[i1];
327
                 merged_index++; i1++;
328
            } else {
329
                 merged_list[merged_index] = sub_list2[i2];
330
                 merged_index++; i2++;
            }
332
        }
333
334
        u32 i;
335
336
        if (i1 >= block\_size)
            for (i = merged\_index; i < merged\_size; i++, i2++)
337
                 merged_list[i] = sub_list2[i2];
        else if (i2 >= block_size2)
339
            for (i = merged index; i < merged size; i++, i1++)
340
                merged_list[i] = sub_list[i1];
341
342
        \quad \text{for } (i = 0; i < block\_size; i++)
343
            sub_list[i] = merged_list[i];
344
345
        for (i = 0; i < block size2; i++)
            sub_list2[i] = merged_list[block_size+i];
346
347
348
        return merged_list;
349 }
350
351
352
   void split_and_merge(u32 *sub_list, u32 min, u32 max)
353
        u32 *_; // stupid recursion
354
355
        u32 \text{ middle} = (\min+\max)/2,
356
            lowerCount = middle - min + 1,
357
            upperCount \, = \, max \, - \, middle \, ;
358
359
       / check if sub_list is already sorted
     \inf (max == min) {
361
        return;
362
363
     } else {
        //sort first half of sub_list
364
365
        split_and_merge(sub_list, min, middle);
        //sort second half of sub_list
366
367
        split_and_merge(sub_list, middle+1, max);
        /* Now merge the two halves */
368
         = merge(sub list + min, lowerCount, sub list + middle + 1, upperCount);
369
370
371
```

Listing 1: Parallel Merge