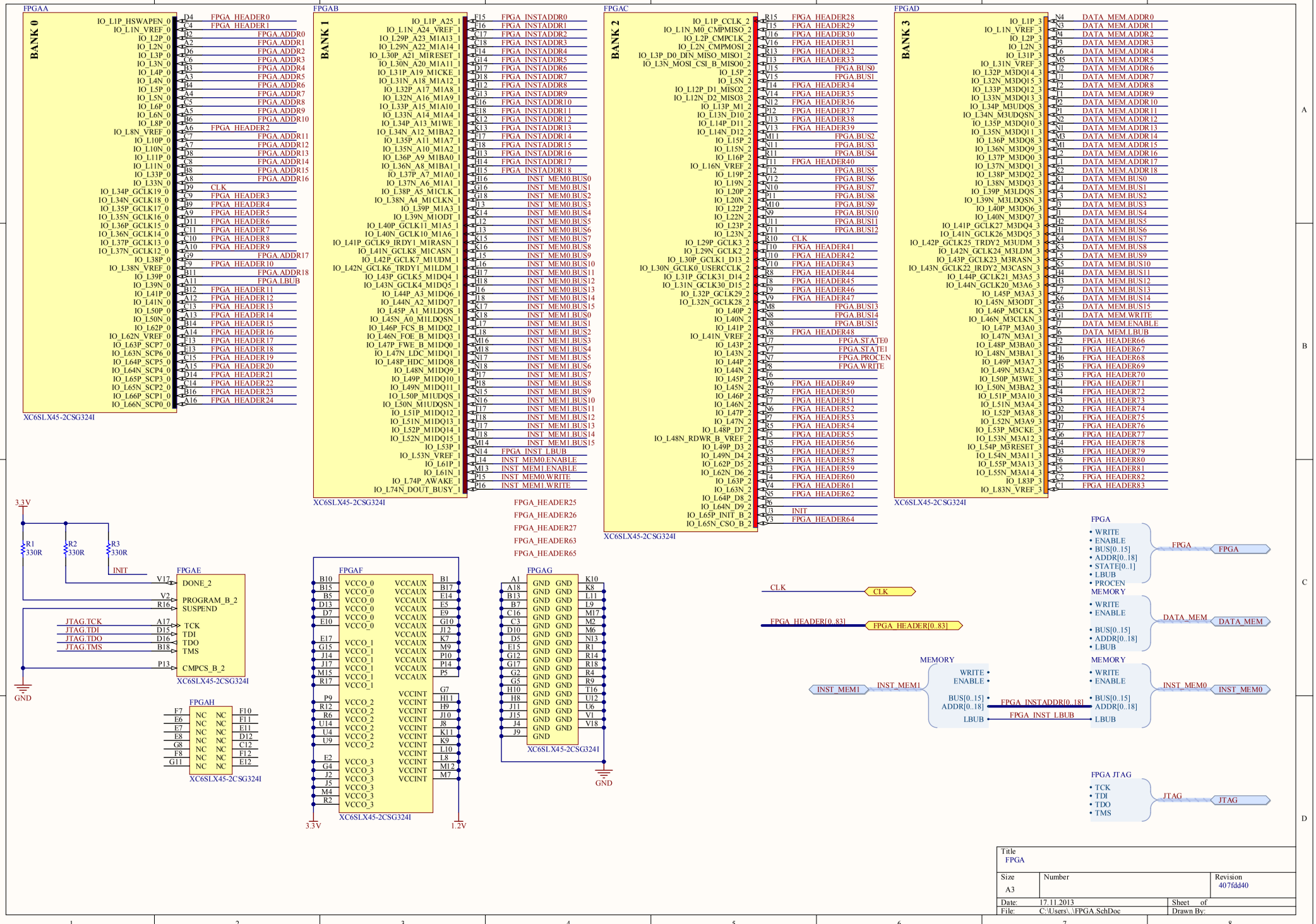
FPGAA
BANK 0
XC6SLX45-2CSG324I
FPGAB
BANK 1
XC6SLX45-2CSG324I
FPGAC
BANK 2
XC6SLX45-2CSG324I
FPGAD
BANK 3
XC6SLX45-2CSG324I
FPGAE
XC6SLX45-2CSG324I
FPGAF
XC6SLX45-2CSG324I
FPGAG
XC6SLX45-2CSG324I
FPGAH
XC6SLX45-2CSG324I
3.3V
GND
1.2V
FPGA
DATA_MEM
INST_MEM0
INST_MEM1
JTAG
FPGA JTAG
MEMORY
Title
FPGA
Size
A3
Number
Revision
407fdd40
Date: 17.11.2013
File: C:\Users\..\FPGA.SchDoc
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