

H_FPGA_BRIS0	C2	PA0
H_FPGA_BRIS1	C3	PA1
H_FPGA_BRIS2	D2	PA2
H_FPGA_BRIS3	D3	PA3
H_FPGA_BRIS4	E2	PA4
H_FPGA_BRIS5	E3	PA5
H_FPGA_BRIS6	F2	PA6
H_FPGA_BRIS7	F3	PA7
H_FPGA_BRIS8	G2	PA8
H_FPGA_BRIS9	J2	PA9
H_FPGA_BRIS10	J3	PA10
H_FPGA_BRIS11	K2	PA11
H_FPGA_BRIS12	K3	PA12
H_FPGA_BRIS13	L2	PA13
H_FPGA_BRIS14	L3	PA14
H_FPGA_BRIS15	M2	PA15
H_FPGA_ADDR0	H1	PC0
H_FPGA_ADDR1	J1	PC1
H_FPGA_ADDR2	J2	PC2
H_FPGA_ADDR3	K2	PC3
H_FPGA_ADDR4	K3	PC4
H_FPGA_ADDR5	L2	PC5
H_FPGA_ADDR6	L3	PC6
H_FPGA_ADDR7	M2	PC7
H_FPGA_ADDR8	M3	PC8
H_FPGA_ADDR9	N2	PC9
H_FPGA_ADDR10	N3	PC10
H_FPGA_ADDR11	P2	PC11
H_FPGA_ADDR12	P3	PC12
H_FPGA_ADDR13	P4	PC13
H_FPGA_ADDR14	P5	PC14
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