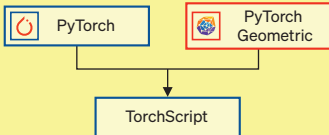
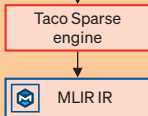


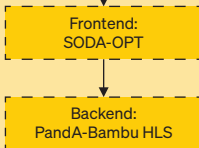
High-Level Framework



MLIR



Synthesizer



Chip design

