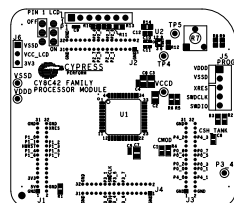


D

C

B

A





REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
01	ALPHA RELEASE	05/27/12	RKAD
02	ALPHA RELEASE	08/31/12	RKAD
03	ADDED LCD INCREASE BOARD SIZE	09/30/12	RKAD
04	REMOVED P3_6 & P3_7 NETS FROM J3.	12/04/12	RKAD
05	ROTATED P1. CHANGED BOARDOUTLINE.	01/08/13	RKAD
06	NET CHANGED P3_0,P3_2,P3_6,P3_7. ADDED C12, R14. ADDED SILK.	01/29/13	RKAD

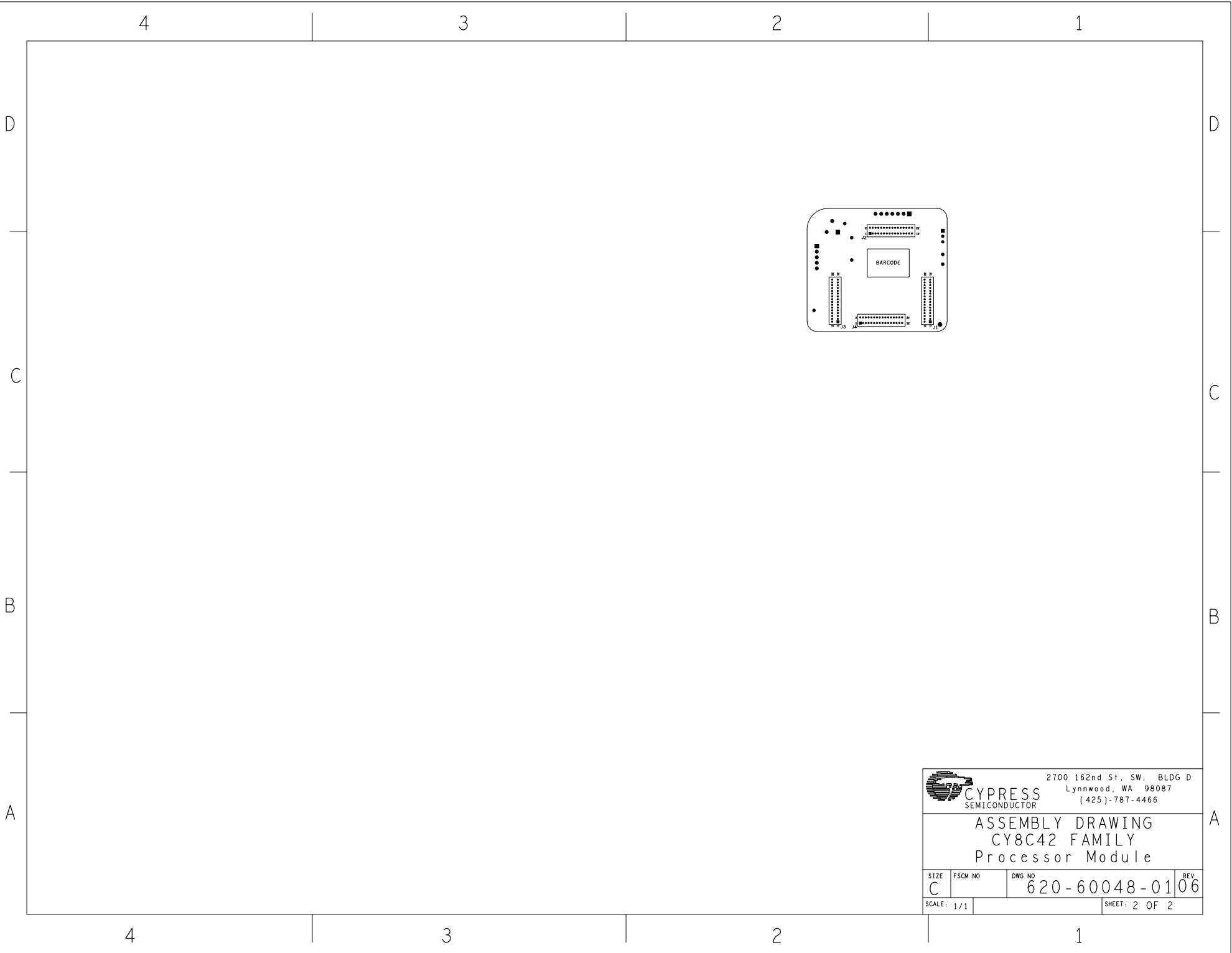
D


C

B

A

UNLESS OTHERWISE SPECIFIED		SIGNATURES	DATE	 CYPRESS SEMICONDUCTOR 2700 162nd St. SW. BLDG D Lynnwood, WA 98087 (425)-787-4466	
DIMENSIONS ARE IN INCHES		DRAWN ZMK	01/28/13		
TOLERANCES ON:		CHECKED RKAD	01/29/13		
ANGLES +/- 2°		ENGRG RKAD	01/29/13		
2 PL DECIMALS +/- .010		ISSUED		ASSEMBLY DRAWING CY8C42 FAMILY Processor Module	
3 PL DECIMALS +/- .005					
THIRD ANGLE PROJECTIONS 					
CYPRESS PROPRIETARY					
THIS DOCUMENT CONTAINS CONFIDENTIAL, PROPRIETARY INFORMATION THAT IS CYPRESS SEMICONDUCTOR PROPERTY. DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHORIZED BY CYPRESS.				SIZE C	FSCM NO
				DWG NO 620-60048-01	REV 06
				SCALE: 1/1	SHEET: 1 OF 2



		2700 162nd St. SW. BLDG D Lynnwood, WA 98087 (425)-787-4466	
ASSEMBLY DRAWING CY8C42 FAMILY Processor Module			
SIZE C	FSCM NO.	DWG NO. 620-60048-01	REV. 06
SCALE: 1/1		SHEET: 2 OF 2	