

Build Results

I. Synthesis:

1. 50MHz:

```
*****
Checking Synthesis is cleared
*****

Instance Count
-----
Leaf Instance Count      19209
Physical Instance count    0
Sequential Instance Count  6227
Combinational Instance Count 12982
Hierarchical Instance Count 0

Area
----
Cell Area                781079.040
Physical Cell Area        0.000
Total Cell Area (Cell+Physical) 781079.040
Net Area                  378698.069
Total Area (Cell+Physical+Net) 1159777.109

      Leakage      Dynamic      Total
Instance  Cells Power(nW)  Power(nW)  Power(nW)
-----
RSA_ModExp 19209  1173.870 159203942.563 159205116.433

Fmax at Syn: 65 MHz
```

	ROHM
Sequential Instance	6227
Combinational Instance	12982
Cells	19209
Cell Area	781079.04 um2
Net Area	378698.069 um2
Total Power	159.205 mW
Fmax	65 MHz

2. 100MHz

```
*****
Checking Synthesis is cleared
*****

Instance Count
-----
Leaf Instance Count      19117
Physical Instance count    0
Sequential Instance Count  6227
Combinational Instance Count 12890
Hierarchical Instance Count 0

Area
----
Cell Area                781356.442
Physical Cell Area        0.000
Total Cell Area (Cell+Physical) 781356.442
Net Area                  377067.583
Total Area (Cell+Physical+Net) 1158424.025

      Leakage      Dynamic      Total
Instance  Cells Power(nW)  Power(nW)  Power(nW)
-----
RSA_ModExp 19117  1175.120 297116540.374 297117715.494

Fmax at Syn: 94 MHz
```

	ROHM
Sequential Instance	6227
Combinational Instance	12890
Cells	19117
Cell Area	781356.442 um2
Net Area	377067.583 um2
Total Power	297.118 mW
Fmax	94 MHz