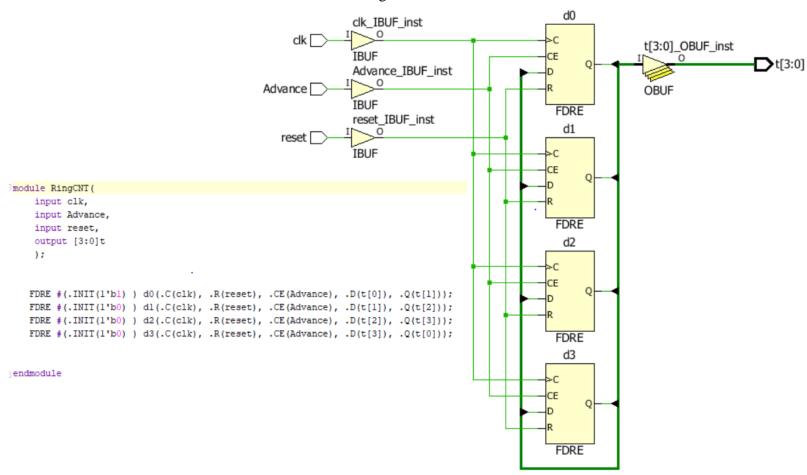
## Ring Counter



## Selector

```
10[3:0] H2_i
                                                    10[3:0] H1_i
N[15:0]
                                    0[3:0]
                       I1[3:0]
                                                                  013:01
sel[3:0]
                                                     I1[3:0]
                             RTL_AND
                                                                                   10[3:0] H0_i
                                                           RTL XOR
                                                                                                              10[3:0] H_i
                                                                                   I1[3:0]
                             H2_i
                                                                                                                           0[3:0]
                                                                  0
                                                                                         RTL XOR
                                                                                                                                    →H[3:0]
                                                                                                              I1[3:0]
                                   0[3:0]
                                                    I0[3:0]
                      I1[3:0]
                                                                 0[3:0]
                                                                                   10[3:0] H0_i_0
                                                                                                                     RTL_XOR
                                                     I1[3:0]
                             RTL_AND
                                                           RTL_AND
                                                                                               O[3:0]
                                                                                   I1[3:0]
                                                                                          RTL_AND
```

```
module Selector(
  input [3:0] sel,
  input [15:0] N,
  output [3:0] H
  );
  assign H = ((N[15:12]&{4{sel[3]}}) ^(N[11:8]&{4{sel[2]}})^(N[7:4]&{4{sel[1]}}) ^(N[3:0]&{4{sel[0]}}));
  endmodule
```