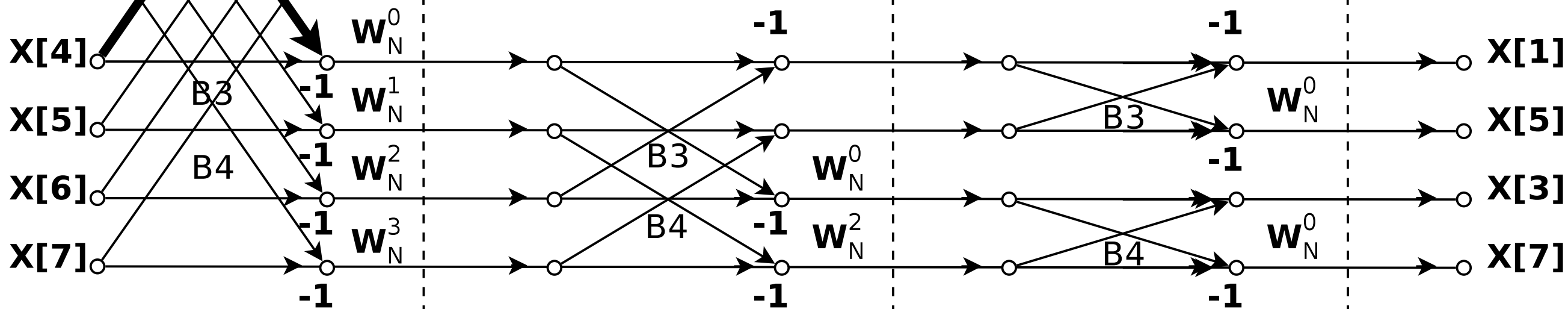
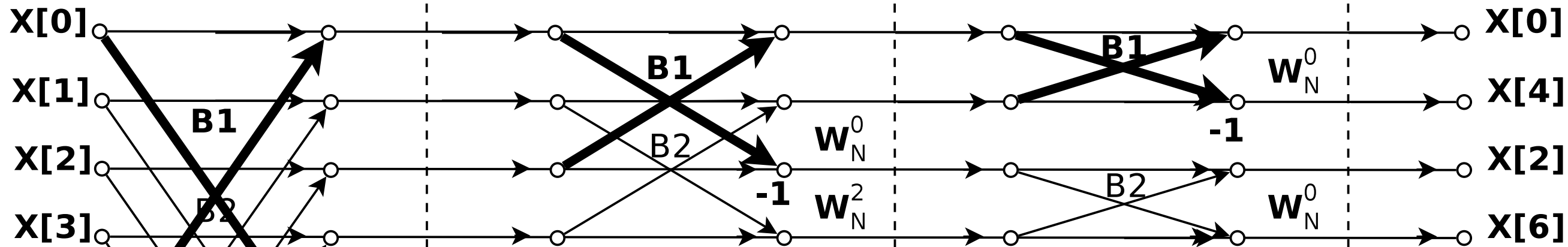


# Stage 1

# Stage 2

# Stage 3

M0 (RAM0)



M1 (RAM1)