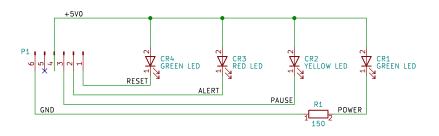
71 P 94V-0
24 - 90001790 RE V01
25 - 90001790 RE V02
3003 - 900 - 4102





Pins 1..3 are individually driven by outputs from the control register U6 and buffered by inverter U5 and current—limiting resistors.

Pin-4 is connected directly to +5V0.

Pin—5 is grounded by the reset—switch, otherwise it will have the power—on reset timing—capacitor charge voltage.

Pin-6 is connected directly to ground.

42-90001790
24-90001790 REV01
25-90001790 REV02
3003-900-4120

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File: nabu_led_pcb.kicad_sch

Title: NABU PC Front Panel Board

Size: A4 Date: June 12, 2023 Rev: A
KiCad E.D.A. kicad 7.0.2 Id: 1/1