

# 1. Description

# 1.1. Project

Project Name	h-	
	id_heater_sensor_modbus_slave_st	
	m32f401ccu6	
Board Name	custom	
Generated with:	STM32CubeMX 6.5.0	
Date	04/16/2022	

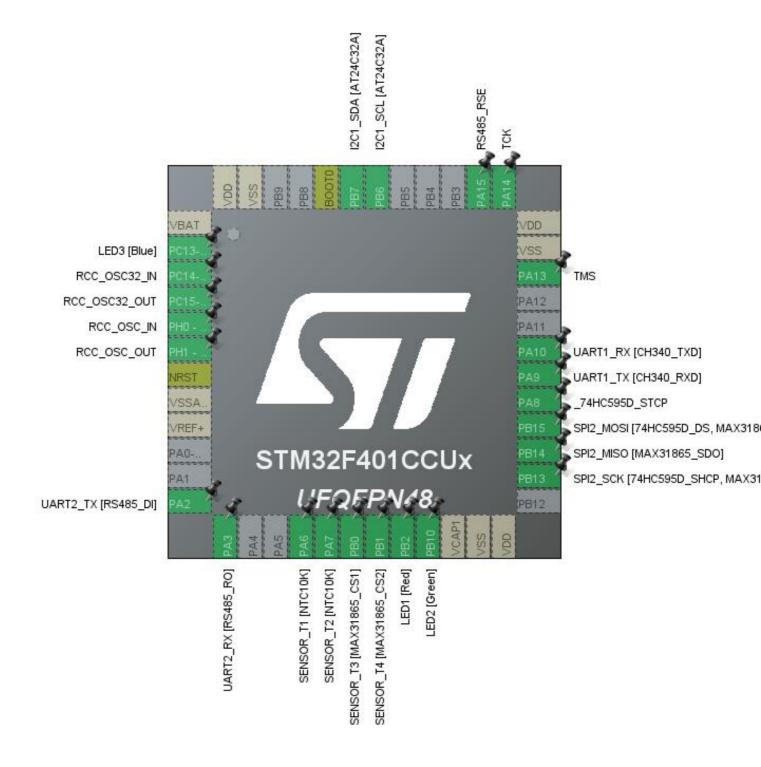
## 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F401
MCU name	STM32F401CCUx
MCU Package	UFQFPN48
MCU Pin number	48

# 1.3. Core(s) information

Core(s)	Arm Cortex-M4

# 2. Pinout Configuration



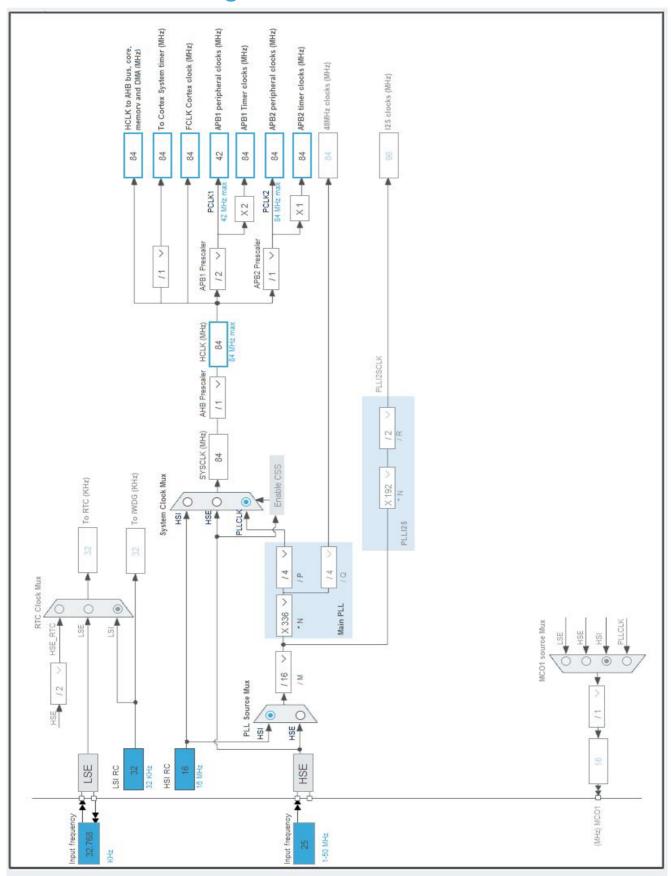
# 3. Pins Configuration

Pin Number UFQFPN48	Pin Name (function after	Pin Type	Alternate Function(s)	Label
	reset)			
1	VBAT	Power		
2	PC13-ANTI_TAMP *	I/O	GPIO_Output	LED3 [Blue]
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
5	PH0 - OSC_IN	I/O	RCC_OSC_IN	
6	PH1 - OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA/VREF-	Power		
9	VREF+	Power		
12	PA2	I/O	USART2_TX	UART2_TX [RS485_DI]
13	PA3	I/O	USART2_RX	UART2_RX [RS485_RO]
16	PA6	I/O	ADC1_IN6	SENSOR_T1 [NTC10K]
17	PA7	I/O	ADC1_IN7	SENSOR_T2 [NTC10K]
18	PB0 *	I/O	GPIO_Output	SENSOR_T3 [MAX31865_CS1]
19	PB1 *	I/O	GPIO_Output	SENSOR_T4 [MAX31865_CS2]
20	PB2 *	I/O	GPIO_Output	LED1 [Red]
21	PB10 *	I/O	GPIO_Output	LED2 [Green]
22	VCAP1	Power		
23	VSS	Power		
24	VDD	Power		
26	PB13	I/O	SPI2_SCK	SPI2_SCK [74HC595D_SHCP, MAX31865_SCLK]
27	PB14	I/O	SPI2_MISO	SPI2_MISO [MAX31865_SDO]
28	PB15	I/O	SPI2_MOSI	SPI2_MOSI [74HC595D_DS, MAX31865_SDI]
29	PA8 *	I/O	GPIO_Output	_74HC595D_STCP
30	PA9	I/O	USART1_TX	UART1_TX [CH340_RXD]
31	PA10	I/O	USART1_RX	UART1_RX [CH340_TXD]
34	PA13	I/O	SYS_JTMS-SWDIO	TMS
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	тск

Pin Number UFQFPN48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
38	PA15 *	I/O	GPIO_Output	RS485_RSE
42	PB6	I/O	I2C1_SCL	I2C1_SCL [AT24C32A]
43	PB7	I/O	I2C1_SDA	I2C1_SDA [AT24C32A]
44	воото	Boot		
47	VSS	Power		
48	VDD	Power		

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



# 5. Software Project

## 5.1. Project Settings

Name	Value
Project Name	h-id_heater_sensor_modbus_slave_stm32f401ccu6
Project Folder	C:\Users\Dobord\Documents\h-id_heater_sensor_modbus_slave_stm32f401ccu6
Toolchain / IDE	SW4STM32
Firmware Package Name and Version	STM32Cube FW_F4 V1.27.0
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

# 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	MX_GPIO_Init	GPIO
2	MX_DMA_Init	DMA
3	SystemClock_Config	RCC
4	MX_ADC1_Init	ADC1
5	MX_I2C1_Init	I2C1
6	MX_SPI2_Init	SPI2
7	MX_USART1_UART_Init	USART1
8	MX_USART2_UART_Init	USART2

# 6. Power Consumption Calculator report

## 6.1. Microcontroller Selection

Series	STM32F4
Line	STM32F401
мси	STM32F401CCUx
Datasheet	DS9716_Rev8

## 6.2. Parameter Selection

Temperature	25
Vdd	3.3

## 6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

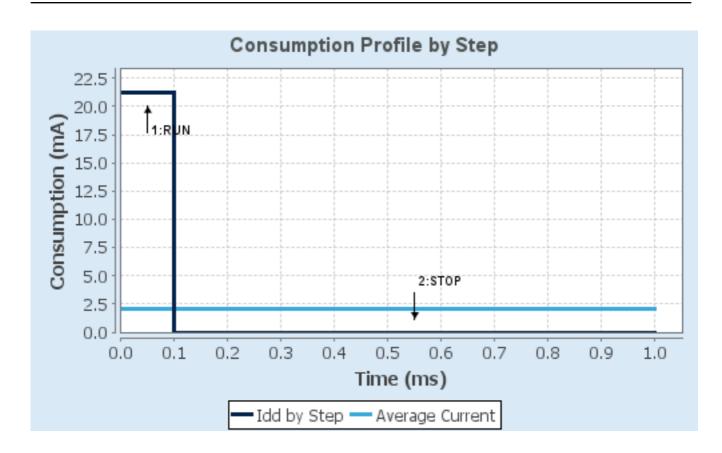
## 6.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	Scale2-Medium	No Scale
Fetch Type	FLASH/ART/PREFETCH	n/a
CPU Frequency	84 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator_LPLV Flash-
		PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	21.2 mA	10 μΑ
Duration	0.1 ms	0.9 ms
DMIPS	105.0	0.0
Ta Max	102.76	105
Category	In DS Table	In DS Table

## 6.5. Results

Sequence Time	1 ms	Average Current	2.13 mA
Battery Life	2 months, 5 days,	Average DMIPS	105.0 DMIPS
	14 hours	_	

## 6.6. Chart



# 7. Peripherals and Middlewares Configuration

7.1. ADC1 mode: IN6 mode: IN7

mode: Temperature Sensor Channel

mode: Vrefint Channel7.1.1. Parameter Settings:

ADCs\_Common\_Settings:

Mode Independent mode

ADC\_Settings:

Clock Prescaler PCLK2 divided by 4

Resolution 12 bits (15 ADC Clock cycles)

Data Alignment Right alignment
Scan Conversion Mode Enabled

Continuous Conversion Mode Enabled \*

Discontinuous Conversion Mode Disabled

DMA Continuous Requests Enabled \*

End Of Conversion Selection EOC flag at the end of all conversions \*

ADC\_Regular\_ConversionMode:

Number Of Conversion 4 \*

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None
Rank 1

Channel Vrefint \*

Sampling Time 480 Cycles \*

<u>Rank</u> **2** \*

Channel Temperature Sensor \*

Sampling Time 480 Cycles \*

<u>Rank</u> 3 \*

Channel 6

Sampling Time 480 Cycles \*

<u>Rank</u> 4 \*

Channel 7 \*
Sampling Time 480 Cycles \*

ADC\_Injected\_ConversionMode:

Number Of Conversions 0

#### WatchDog:

Enable Analog WatchDog Mode false

7.2. I2C1 I2C: I2C

### 7.2.1. Parameter Settings:

#### **Master Features:**

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

**Slave Features:** 

Clock No Stretch Mode Disabled

Primary Address Length selection 7-bit

Dual Address Acknowledged Disabled

Primary slave address 0

General Call address detection Disabled

### 7.3. RCC

High Speed Clock (HSE): BYPASS Clock Source Low Speed Clock (LSE): Crystal/Ceramic Resonator

### 7.3.1. Parameter Settings:

### **System Parameters:**

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16

TIM Prescaler Selection Disabled
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 2

### 7.4. SPI2

**Mode: Full-Duplex Master** 

### 7.4.1. Parameter Settings:

**Basic Parameters:** 

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

**Clock Parameters:** 

Prescaler (for Baud Rate) 2

Baud Rate 21.0 MBits/s \*

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

**Advanced Parameters:** 

CRC Calculation Disabled
NSS Signal Type Software

### 7.5. SYS

**Debug: Serial Wire** 

**Timebase Source: TIM1** 

### 7.6. USART1

**Mode: Asynchronous** 

### 7.6.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples

### **7.7. USART2**

### **Mode: Asynchronous**

### 7.7.1. Parameter Settings:

### **Basic Parameters:**

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples

### 7.8. FREERTOS

Interface: CMSIS\_V2

### 7.8.1. Config parameters:

#### API:

FreeRTOS API CMSIS v2

**Versions:** 

FreeRTOS version 10.3.1 CMSIS-RTOS version 2.00

MPU/FPU:

ENABLE\_MPU Disabled ENABLE\_FPU Disabled

Kernel settings:

USE\_PREEMPTION Enabled

CPU\_CLOCK\_HZ SystemCoreClock

TICK\_RATE\_HZ 1000

MAX\_PRIORITIES 56

MINIMAL\_STACK\_SIZE 128

MAX\_TASK\_NAME\_LEN 16

USE\_16\_BIT\_TICKS Disabled

IDLE\_SHOULD\_YIELD Enabled

USE\_MUTEXES Enabled

USE\_RECURSIVE\_MUTEXES Enabled
USE\_COUNTING\_SEMAPHORES Enabled

QUEUE\_REGISTRY\_SIZE 8

USE\_APPLICATION\_TASK\_TAG Disabled

ENABLE\_BACKWARD\_COMPATIBILITY Enabled
USE\_PORT\_OPTIMISED\_TASK\_SELECTION Disabled
USE\_TICKLESS\_IDLE Disabled
USE\_TASK\_NOTIFICATIONS Enabled
RECORD\_STACK\_HIGH\_ADDRESS Disabled

#### Memory management settings:

Memory Allocation Dynamic / Static

TOTAL\_HEAP\_SIZE 15360

Memory Management scheme heap\_4

#### **Hook function related definitions:**

USE\_IDLE\_HOOK Disabled
USE\_TICK\_HOOK Disabled
USE\_MALLOC\_FAILED\_HOOK Disabled
USE\_DAEMON\_TASK\_STARTUP\_HOOK Disabled
CHECK\_FOR\_STACK\_OVERFLOW Disabled

#### Run time and task stats gathering related definitions:

GENERATE\_RUN\_TIME\_STATS Disabled
USE\_TRACE\_FACILITY Enabled
USE\_STATS\_FORMATTING\_FUNCTIONS Disabled

#### Co-routine related definitions:

USE\_CO\_ROUTINES Disabled
MAX\_CO\_ROUTINE\_PRIORITIES 2

#### Software timer definitions:

USE\_TIMERS Enabled
TIMER\_TASK\_PRIORITY 2
TIMER\_QUEUE\_LENGTH 10
TIMER\_TASK\_STACK\_DEPTH 256

### Interrupt nesting behaviour configuration:

LIBRARY\_LOWEST\_INTERRUPT\_PRIORITY 15
LIBRARY\_MAX\_SYSCALL\_INTERRUPT\_PRIORITY 5

#### Added with 10.2.1 support:

MESSAGE\_BUFFER\_LENGTH\_TYPE size\_t
USE\_POSIX\_ERRNO Disabled

#### **CMSIS-RTOS V2 flags:**

USE\_OS2\_THREAD\_SUSPEND\_RESUME Enabled
USE\_OS2\_THREAD\_ENUMERATE Enabled
USE\_OS2\_EVENTFLAGS\_FROM\_ISR Enabled
USE\_OS2\_THREAD\_FLAGS Enabled
USE\_OS2\_TIMER Enabled
USE\_OS2\_MUTEX Enabled

### 7.8.2. Include parameters:

#### Include definitions:

vTaskPrioritySet Enabled uxTaskPriorityGet Enabled Enabled vTaskDelete Disabled vTaskCleanUpResources Enabled vTaskSuspend Enabled vTaskDelayUntil Enabled vTaskDelay xTaskGetSchedulerState Enabled xTaskResumeFromISR Enabled Enabled xQueueGetMutexHolder Disabled xSemaphoreGetMutexHolder Disabled pcTaskGetTaskName Enabled uxTaskGetStackHighWaterMark Enabled xTaskGetCurrentTaskHandle Enabled eTaskGetState Disabled xEventGroupSetBitFromISR xTimerPendFunctionCall Enabled Disabled xTaskAbortDelay xTaskGetHandle Disabled uxTaskGetStackHighWaterMark2Disabled

### 7.8.3. Advanced settings:

Newlib settings (see parameter description first):

USE\_NEWLIB\_REENTRANT Disabled

Project settings (see parameter description first):

Use FW pack heap file Enabled

<sup>\*</sup> User modified value

# 8. System Configuration

# 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA6	ADC1_IN6	Analog mode	No pull-up and no pull-down	n/a	SENSOR_T1 [NTC10K]
	PA7	ADC1_IN7	Analog mode	No pull-up and no pull-down	n/a	SENSOR_T2 [NTC10K]
I2C1	PB6	I2C1_SCL	Alternate Function Open Drain	No pull-up and no pull-down	Very High	I2C1_SCL [AT24C32A]
	PB7	I2C1_SDA	Alternate Function Open Drain	No pull-up and no pull-down	Very High	I2C1_SDA [AT24C32A]
RCC	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
	PH0 - OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1 - OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI2	PB13	SPI2_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High	SPI2_SCK [74HC595D_SHCP, MAX31865_SCLK]
	PB14	SPI2_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High	SPI2_MISO [MAX31865_SDO]
	PB15	SPI2_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High	SPI2_MOSI [74HC595D_DS, MAX31865_SDI]
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	TMS
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	TCK
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	UART1_TX [CH340_RXD]
	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	UART1_RX [CH340_TXD]
USART2	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	UART2_TX [RS485_DI]
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	UART2_RX [RS485_RO]
GPIO	PC13- ANTI_TAMP	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED3 [Blue]

# h-id\_heater\_sensor\_modbus\_slave\_stm32f401ccu6 Project Configuration Report

IP	Pin	Signal	GPIO mode	GPIO pull/up pull	Max	User Label
				down	Speed	
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SENSOR_T3 [MAX31865_CS1]
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SENSOR_T4 [MAX31865_CS2]
	PB2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED1 [Red]
	PB10	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED2 [Green]
	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	_74HC595D_STCP
	PA15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	RS485_RSE

## 8.2. DMA configuration

DMA request	Stream	Direction	Priority
ADC1	DMA2_Stream0	Peripheral To Memory	Low

## ADC1: DMA2\_Stream0 DMA request Settings:

Mode: Circular \*

Use fifo: Enable \*

FIFO Threshold:

Peripheral Increment:

Memory Increment:

Peripheral Data Width:

Memory Data Width:

Half Word

Memory Data Width:

Half Word

Peripheral Burst Size:

Single

Memory Burst Size:

Single

# 8.3. NVIC configuration

# 8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority		
Non maskable interrupt	true	0	0		
Hard fault interrupt	true	0	0		
·			0		
Memory management fault	true	0	-		
Pre-fetch fault, memory access fault	true	0	0		
Undefined instruction or illegal state	true	0	0		
System service call via SWI instruction	true	0	0		
Debug monitor	true	0	0		
Pendable request for system service	true	15	0		
System tick timer	true	15	0		
ADC1 global interrupt	true	5	0		
TIM1 update interrupt and TIM10 global interrupt	true	15	0		
DMA2 stream0 global interrupt	true	5	0		
PVD interrupt through EXTI line 16	unused				
Flash global interrupt	unused				
RCC global interrupt	unused				
I2C1 event interrupt	unused				
I2C1 error interrupt	unused				
SPI2 global interrupt	unused				
USART1 global interrupt	unused				
USART2 global interrupt	unused				
FPU global interrupt		unused			

## 8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	false	false
Debug monitor	false	true	false
Pendable request for system service	false	false	false
System tick timer	false	false	true
ADC1 global interrupt	false	true	true
TIM1 update interrupt and TIM10 global	false	true	true

# h-id\_heater\_sensor\_modbus\_slave\_stm32f401ccu6 Project Configuration Report

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
interrupt			
DMA2 stream0 global interrupt	false	true	true

<sup>\*</sup> User modified value

# 9. System Views

- 9.1. Category view
- 9.1.1. Current



## 10. Docs & Resources

Type Link

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_eval-

tools\_portfolio.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_stm8\_functi

onal-safety-packages.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_software\_development\_tools.pdf

Training Material https://www.st.com/resource/en/sales\_guide/sg\_sc2154.pdf

Flyers https://www.st.com/resource/en/flyer/flnucleolrwan.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32f4x1.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32nucleo.pdf

Flyers https://www.st.com/resource/en/flyer/flstmcsuite.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32trust.pdf

Product https://www.st.com/resource/en/certification\_document/stm32\_authenticat

Certifications ion\_can.pdf

Application Notes https://www.st.com/resource/en/application\_note/an1181-electrostatic-

discharge-sensitivity-measurement-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application note/an1709-emc-design-

guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2606-stm32-

microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2639-soldering-

recommendations-and-package-information-for-leadfree-ecopack-mcus-

and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2834-how-to-get-the-

best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2867-oscillator-

design-guide-for-stm8afals-stm32-mcus-and-mpus-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application\_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application\_note/an3998-pdm-audio-software-decoding-on-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4031-using-the-stm32f2-stm32f4-and-stm32f7-series-dma-controller-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application\_note/an4230-stm32-

- microcontroller-random-number-generation-validation-using-the-nist-statistical-test-suite-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4277-using-stm32-device-pwm-shutdown-features-for-motor-control-and-digital-power-conversion-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4286-spi-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4488-getting-started-with-stm32f4xxxx-mcu-hardware-development-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application\_note/an4616-migrating-from-stm32f401-and-stm32f411-lines-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4646-peripheral-interconnections-on-stm32f401-and-stm32f411-lines-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application\_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4759-using-the-hardware-realtime-clock-rtc-and-the-tamper-management-unit-tamp-with-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4776-generalpurpose-timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4838-managing-

- memory-protection-unit-in-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4850-stm32-mcusspreadspectrum-clock-generation-principles-properties-andimplementation-stmicroelectronics.pdf
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