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# DSP VERSION 1.0

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## DESIGN CONSIDERATIONS

**DESIGN NOTE:**  
Functions of this block

**DESIGN NOTE:**  
Calculation equations

**DESIGN NOTE:**  
Calculations when choosing  
this component

Title:	<b>Cover Page</b>		Author:	DODANHLUONG
Approved:			CONFIDENTIAL	*
Size:	A4	Project:	<b>DSP_VERSION_1.0</b>	
Modified Date:	8/25/2025		Revision:	1.0
Date:	10/1/2025 3:09:42 PM		SW Version:	24.1.2.44



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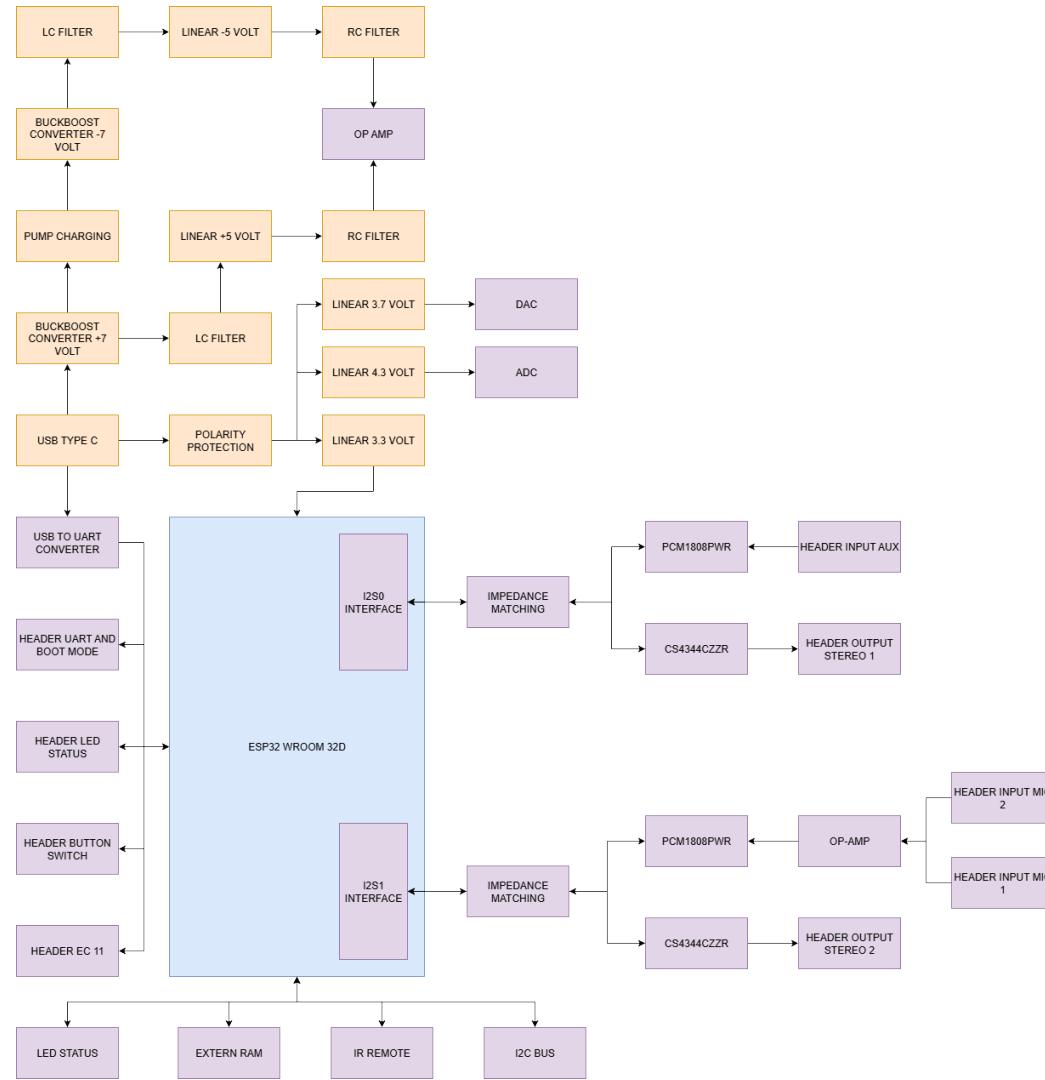
B

C

C

D

D

Title: **Block Diagram**

Size: A4 | Project: DSP\_VERSION\_1.0

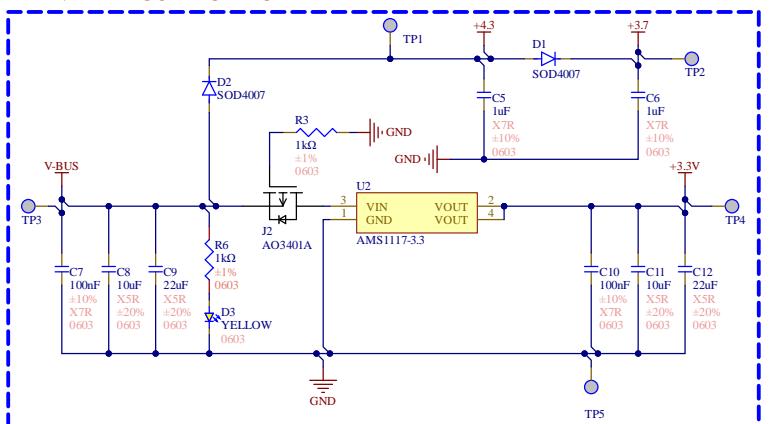
Date: 10/1/2025 3:09:42 PM | Sheet 2 of 6

Author: DODANH LUONG
Approved: <b>CONFIDENTIAL</b>
Modified Date: 10/1/2025
Revision: 1.0
SW Version: 24.1.2.44

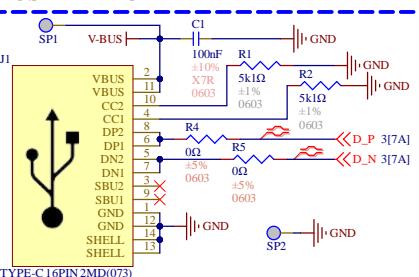


1 2 3 4 5 6 7 8

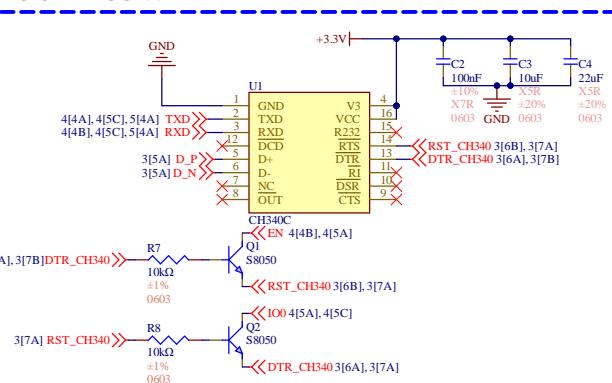
### LINEAR MCU ADC DAC



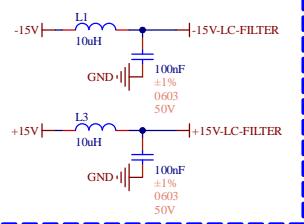
### USB TYPE C



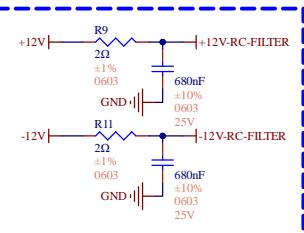
### USB TO UART CONVERTER



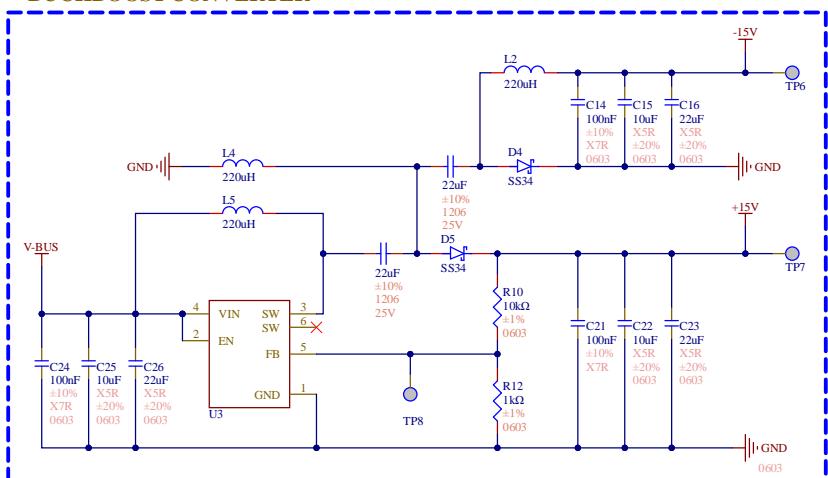
### LC FILTER



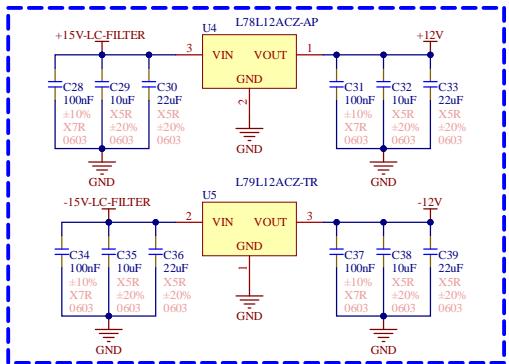
### RC FILTER



### BUCKBOOST CONVERTER



### LINEAR +12V



Title: Voltage Source

Size: A3 | Project: DSP\_VERSION\_1.0

Author: DODANHLUONG

CheckedBy: \*

Modified Date: 9/20/2025

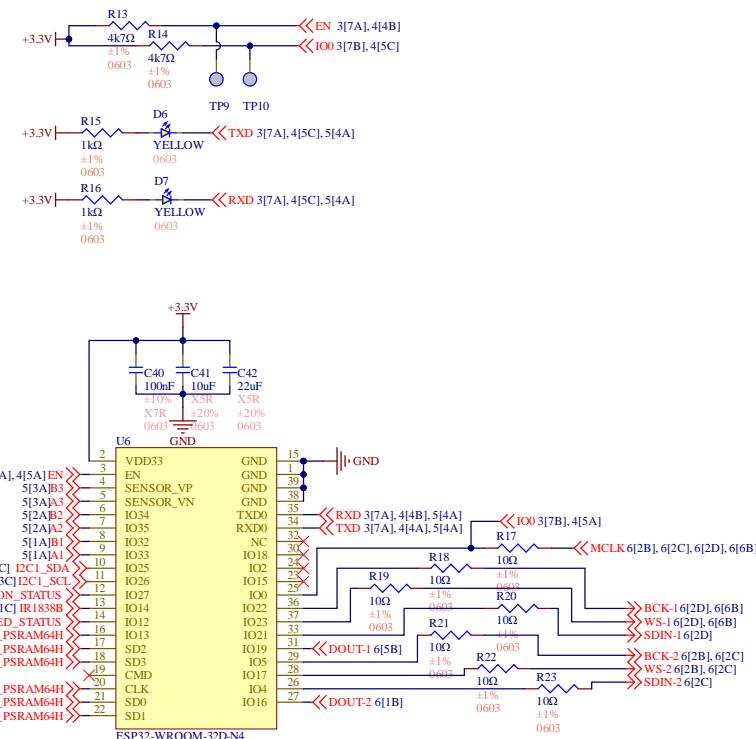
Revision: 1.0

SW Version: 24.1.2.44



1 2 3 4 5 6 7 8

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Title: <b>MCU</b>		Author:	DODANH LUONG	
Size: A3		Checked By:	*	
Project: KIT ARDUINO VERSION 1.0		Modified Date:	9/20/2025	
		Revision:	1.0	
Date: 10/1/2025	3:09:43 PM	Sheet 4 of 6	SW Version:	24.1.2.44

