TD

{{#switchcategory:MSP430=<McuHitboxHeader/>|C2000=<McuHitboxHeader/>|Stellaris=<McuHitboxHeader/>|TMS570=<McuHitboxHeader/>|MCU=<McuHitboxHeader/>|MAVRK=<MAVRKHitboxHeader/>|<HitboxHeader/>}}

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Introduction

This page describes usage of TD utility. TD utility is delivered as part of Code Composer Studio (CCS) and used to decode hardware trace data into a human readable text format. This is a command line utility is typically used in a standalone or scripting environment.

Location

This utility gets installed in CCS_DIR/ccsv5/ccs_base/emulation/analysis/bin directory as part of Code Composer Studio installation. To use this utility, open a command line shell and change directory to CCS_DIR/ccsv5/ccs_base/emulation/analysis/bin.

Parameters

Required

-bin <Full path to binary file>

This parameter is used to specify the trace binary file.

-app <Full path to out file>

This parameter is used to specify full path of the application location. Trace decoders use application information to decompress and output the trace information.

If using software messages from STM, this parameter needs to be specified with CPU qualification (i.e. CPU=file). More than one of such entries can be specified with comma(,) seperations e.g. CPUo=fileo,cpu1=file1,...

It is sufficient to give just the name after the slash (e.g., C66XX_o) as CPU name, it is not required to use full CPU name as formed in CCS (e.g., Blackhawk XDS560v2-USB Mexxanine Emulator_o/C66XX_o).

If there is only one processor generating STM SW messages, CPU qualification could be skipped

-procid <proc>

This parameter is used to specify processor or trace type. Valid options - 64x, 64x+, 66x, 55x, 55x+, arm7, arm9, arm11, cortexa8, and stm.

Note: If you are using CCSv4, this parameter name would be -cpuid.

-rcvr <receiver name>

This parameter is used to specify receiver type used for trace collection. Valid options - ETB, 56oT, 56oV2, and Pro

Required for STM (Optional for DSP and ARM/Cortex trace)

-devicekey DeviceID

This parameter is used to define the device ID or device key for identifying STM topology in the device. This is a unique ID (TAP ID) and can be read via Ro of ICEPICK or retreived from the device TRM.

A trace device XML file path could also be used by the advanced users .

Examples:

Device id for

```
OMAP4430 ES2
              - 0x1B85202F
OMAP4430 ES2.1 -- 0x3B95C02F
OMAP4460
                -- oxoB94E02F
OMAP4470
               -- oxoB97502F
              -- oxooo9E02F
C6678
C6670
              -- oxooo9Do2F
Turbo-C6670
                -- oxob94102F
               -- oxoB94402F
AM335x
AM386x
               -- oxoB96802F
               -- oxoB8F202F
AM387x
DM816x
               -- oxoB81E02F
```

Note: If you are using CCSv4, this parameter name would be -deviceid.

-dcmfile <Full path to the metadata file>

This parameter is used to pass additional metadata to help decode. Default is none.

For STM ETB, you could create a foo.dcm configuration file by copying the text in the box below into a text file and saving it with a name like foo.dcm or whatever you prefer.

```
STM_data_flip=1

STM_BUffer_Wrapped=0

HEAD_Present_0=0

HEAD_Pointer_0=

HEAD_Present_1=1

HEAD_Pointer_1=4
```

 $STM_data_flip=1 \longrightarrow This field \ tells \ decoder \ that \ the \ data \ is \ flipped \ in \ receiver \ packing. \ This \ is \ 1 \ if \ using \ ET. \ Otherwise, \ its \ o.$

STM_Buffer_Wrapped=0 --> This field tells decoder that the buffer is wrapped or not. 1 is wrapped, 0 for buffer not wrapped.

HEAD_Present_o=o --> STM ETB pointer references to efficiently detect start of the buffer (if buffer wrapped). Default to o unless you know the HEAD information.

 $HEAD_Pointer_o = o --> STM\ ETB\ pointer\ references\ to\ efficiently\ detect\ start\ of\ the\ buffer\ (if\ buffer\ wrapped).\ Default\ to\ o\ unless\ you\ know\ the\ HEAD\ information.$

HEAD_Present_1=0 --> STM ETB pointer references to efficiently detect start of the buffer (if buffer wrapped). Default to 0 unless you know the HEAD information.

HEAD_Pointer_1=0 --> STM ETB pointer references to efficiently detect start of the buffer (if buffer wrapped). Default to o unless you know the HEAD information.

Optional

-output <Output file name>

This parameter is used to specify output file name. Defaul is stdout.

-columns <coloum names>

Comma seperated column names to include in the output. Default is all the columns. You can use td -coulminfo with all the required parameters to get a list of supported columns.

-seekpos <% position>

Trace decode start potition in % with respect to the trace data binary file. Default is o.

-samples <count>

Total number of requested output samples. Default is max available.

-format <output format>

Output format id. One of the following id can be specified. Default is CSV_NO_TPOS_QUOTE.

CSV_QUOTE,

CSV,

CSV_SEMICOLON,

CSV_SEMICOLON_NO_TPOS,

CSV_NO_TPOS_QUOTE,

CSV_NO_TPOS,

CSV_SEMICOLON_NO_TPOS,

LOG

Note: TPOS -> Trace position information, which is an decoder specific and unique internal identification for trace output samples.

-deviceidfile < .dim file >

This is a CCS or cToolsTrace scripting generated metadata file to help decode context data. Default is none.

-decodetime

Prints time before and after the trace decode. No other functional imlications.

Examples

td -procid 64x+ -bin trace.bin -app may_app.out -rcvr 56oT -format CSV_NO_TPOS_QUOTE -columns "Program Address,Cycles,Trace Status" -timestamp abs -seekpos 10 -samples 100000

td -procid 64x+ -bin trace.bin -app may_app.out -rcvr 56oT -format CSV_NO_TPOS -timestamp delta

td -procid 64x+ -bin trace.bin -app may_app.out -rcvr ETB -format CSV_SEMICOLON -columns "Program Address,Cycles,Trace Status" -timestamp abs -seekpos 10 -samples 100000

td -procid 66x -bin trace.bin -app may_app.out -rcvr ETB -format CSV_SEMICOLON -columns "Program Address,Cycles,Trace Status" -timestamp abs -seekpos 10 -samples 100000

td -procid cortexa8 -bin c:/temp/ETM_etbdata.bin -app C:/Examples/bin/etm_csetb_A8.c6a816x.out -rcvr ETB -dcmfile c:/temp/ETM_etbdata.dcm -format CSV_SEMICOLON -columns "Program Address,Cycles,Trace Status" -timestamp abs

td -procid stm -bin trace.bin -app CortexA9_o=CortexA9File.out,CortexM3_o=CortexM3File.out -rcvr 560V2STM -devicekey 0x0B81E02F

td -procid stm -bin trace.bin -app CortexA9_o=CortexA9File.out -rcvr 560V2STM -deviceid device_omap4430_ES2.xml

 $td-procid stm-bin\ trace. bin\ -app\ CortexA9_o=CortexA9File.out, CortexM3_o=CortexM3File.out-rcvr\ 560V2STM\ -deviceidfile\ foo. dim$

 $td\ -procid\ stm\ -bin\ trace.bin\ -app\ Cortex A8_o = Cortex A8File.out\ -rcvr\ 56oV2STM\ -device key\ oxoB81Eo2File.out\ -rcvr\ 56oV2STM\ -device key\ oxoB81Eo2File.out\ -rcvr\ 56oV2STM\ -device key\ oxoB81Eo2File.out\ -rcvr\ -rc$

td -procid stm -bin trace.bin -app C66X_o=corepaco.out -rcvr ETB -devicekey 0x0009Do2F

Output

The out of TD is a text file or stdout with the decoded trace infirmation. Here is an example of the decoded trace output from the TD.

008022b8; 008022b8; 0191c028; 000001; — 008022c0; 008022c0; 01804068; 000001; — 008022c4; 008022c4; 023c22e4; 000001; —

Related

- CToolsLib
- Embedded Trace Buffer or ETB

Keystone= MAVRK=For For technical C2000=For OMAPL1=For technical {{ MSP430=For support on technical DaVinci=For technical OMAP35x=For support on MultiCore devices, support on technical 1. switchcategory:MultiCore= technical technical MAVRK support on please post your the C2000 support on OMAP please please post support on MSP430 support on For technical si For technical support on questions in the please OMAP please please post you DaVincoplease please post MultiCore devices, please C6000 MultiCore post your post your questions at post your your post your questions post your questions in the Forum questions questions on auestions on auestions on auestions on on The http://e2e.ti.cor. C6000 MultiCore Forum on The The OMAP For questions The MSP430 The OMAP The DaVinci MAVRK Please post on C2000 For questions related to related to the Forum. Forum. Please Forum. Forum. Please Toolbox comments abo Please post the BIOS MultiCore SDK **BIOS MultiCore** Forum. article TD here. Please post post only post only Forum. Please only (MCSDK), please use the SDK (MCSDK), comments only comments Please post }} **BIOS Forum** post only comments please use the about the comments about the only comments about the **BIOS** Forum Please post only comments related article TD about the article TD comments about the article TD article TD here. here. about the only article **TD** to the article TD here. Please post here. article TD here comments related to the here.



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- Microcontrollers (MCU)
- OMAP Applications Processors

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