

Advanced Event Triggering

```
{{#switchcategory:MSP430=<McuHitboxHeader/>|C2000=<McuHitboxHeader/>|Stellaris=<McuHitboxHeader/>|TMS570=<McuHitboxHeader/>|MCU=<McuHitboxHeader/>|MAVRK=<MAVRKHitboxHeader/>|<HitboxHeader/>}}
```

Contents

Advanced Event Triggering (AET)

- What is it?
- What can we do with it?
- What devices have what capability?

Learn More

FAQ

- Q: How do I use this?
- Q: Can I use this without CCS?
- Q: Is there a similar capability in the Simulator?
- Q: Where can I get more information?
- Q: Can I use a SW instruction to trigger the AET?
- Q: What does the "MARK0 - MARK3" mean?
- Q: What does the "AEGEVT0 - AEGEVT6"?
- Q: Where can I learn about Software Breakpoints?
- Q: Does this work on the 55x?
- Q: Are there hardware breakpoints / hardware watchpoints for C2000?

Related

Advanced Event Triggering (AET)

What is it?

- Advanced emulation capabilities can be broken into two main features: Advanced Event Trigger (AET) and Trace.
- More details on [Breakpoints](#)

What can we do with it?

AET capability can be used to debug complex problems as well as understand performance characteristics of user applications. AET provides the following capabilities:

- Hardware Program Breakpoint:** specifies addresses or address ranges that can generate events such as halting the processor or triggering the trace capture.
- Data Watchpoints:** specifies data variable addresses, address ranges, or data values that can generate events such as halting the processor or triggering the trace capture.
- Counters:** count the occurrence of an event or cycles for performance monitoring.
- State Sequencing:** allows combinations of hardware program breakpoints and data watchpoints to precisely generate events for complex sequences.
- Trace** is a debug technology that provides a detailed, historical account of application code execution, timing, and data accesses. Trace collects, compresses, and exports debug information for analysis. Trace works in real-time and does not impact the execution of the system. See: [XDS560 Trace](#)

What devices have what capability?

Most devices can be described by their core or mega-module. For the C64x+ family, there are two types of mega-modules, they are the C64x+ Mid-GEM and the C64x+ Full GEM.

Only Full-GEM devices have AET/Trace capabilities. The Mid-GEM devices do not.

Some examples (this is not a complete list) of devices which have a Full-GEM megamodule are C641x, C671x, C645x, TNETV3020, TCI6488, DM64x, C674x.

Some examples of devices which have Mid-GEM megamodules are C642x, DM643x, DM644x, OMAP35xx, DM37xx, and DM646x.

C66x devices have AET.

Learn More

- [AET Logic](#) for the C6000 family
- [Debugging With Trace](#)

FAQ

Q: How do I use this?

- A: See: [Unified Breakpoint Manager](#)

Q: Can I use this without CCS?

- A: See: [Aetlib](#)

Q: Is there a similar capability in the Simulator?

- A: See: [Data Breakpoint/Watchpoint](#)

Q: Where can I get more information?

- A: See: [AET & 560Trace Advanced Emulation Techniques](#)

Q: Can I use a SW instruction to trigger the AET?

- A: Yes, please see the section about the MARK instructions (MARK0 - MARK3).

Q: What does the "MARK0 - MARK3" mean?

- A: Mark0 - Mark3 are actually instructions that can trigger [Advanced_Event_Triggering](#) (AET). There is an intrinsic for the newer compilers (`_mark(0)`) that you can use in C code. These instructions allow you to trigger AET by executing a specific instruction. The actual execution of the instruction is equivalent to a nop. These instructions are only available on 64x+ devices, and there are 4 of them.

Q: What does the "AEG EVT0 - AEG EVT6"?

- A: AEG EVT 0-7 is a set of events that can be connected to signals from the interrupt controller, which consist of the System events, along with interrupt events. When you use one of these as an input to an AET job, you also want to configure a "Set AEG EVT/External(8)" job from the Unified Breakpoint Manager . For example, say you want to use AET to trigger whenever there is a RapidIO interrupt. RapidIO Interrupt is considered an external event, so in order to get it to come into AET, you have to go through the interrupt controller. You need to configure configure a "Set AEG EVT/External(8)" [Unified Breakpoint Manager](#) job to connect this event to one of the AEG EVT slots. For this example, connect it to AEG EVT2, then configure the AET job to trigger on AEG EVT2, which is connected to the RapidIO interrupt.

Q: Where can I learn about Software Breakpoints?

- A: [Software Breakpoints in the IDE](#)

Q: Does this work on the 55x?

- A: Yes, similar hardware exists on the 55x allowing for data watchpoints and hardware breakpoints. See [Checking for Stack Overflow using Unified Breakpoint Manager](#) for an example.

Q: Are there hardware breakpoints / hardware watchpoints for C2000?


- A: Yes, please see [Watchpoints for C28x in CCS 4](#)

Related

- [AETLib](#)
- [CToolsLib](#)
- [CTools](#)
- [Watchpoints for C28x in CCS 4](#)
- [General description of Breakpoint](#)
- [More details on Data Breakpoint/Watchpoint](#)
- [XDS560 Trace](#)
- [ETB](#)
- [AET Logic](#) for the C6000 family
- [Debugging With Trace](#)

Keystone=		C2000=For technical support on the C2000 please post your questions on The C2000 Forum. Please post only comments about the article Advanced Event Triggering here.		MSP430=For technical support on MSP430 please post your questions on The MSP430 Forum. Please post only comments about the article Advanced Event Triggering here.		OMAP35x=For technical support on OMAP please post your questions on The OMAP Forum. Please post only comments about the article Advanced Event Triggering here.		OMAPL1=For technical support on OMAP please post your questions on The OMAP Forum. Please post only comments about the article Advanced Event Triggering here.		MAVRK=For technical support on MAVRK please post your questions on The MAVRK Toolbox Forum. Please post only comments about the article Advanced Event Triggering here.	
1. switchcategory:MultiCore=		For technical support on MultiCore devices, please post your questions in the C6000 MultiCore Forum		DaVinci=For technical support on DaVincoplease post your questions on The DaVinci Forum. Please post only comments about the article Advanced Event Triggering here.		OMAP35x=For technical support on OMAP please post your questions on The OMAP Forum. Please post only comments about the article Advanced Event Triggering here.		OMAPL1=For technical support on OMAP please post your questions on The OMAP Forum. Please post only comments about the article Advanced Event Triggering here.		MAVRK=For technical support on MAVRK please post your questions on The MAVRK Toolbox Forum. Please post only comments about the article Advanced Event Triggering here.	
Please post only comments related to the article Advanced Event Triggering here.		Please post only comments related to the article Advanced Event Triggering here.		Please post only comments about the article Advanced Event Triggering here.		Please post only comments about the article Advanced Event Triggering here.		Please post only comments about the article Advanced Event Triggering here.		Please post only comments about the article Advanced Event Triggering here.	

Links

	Amplifiers & Linear	DLP & MEMS	Processors	Switches & Multiplexers
	Audio	High-Reliability	<ul style="list-style-type: none">▪ ARM Processors▪ Digital Signal Processors (DSP)▪ Microcontrollers (MCU)▪ OMAP Applications Processors	Temperature Sensors & Control ICs
	Broadband RF/IF & Digital Radio	Interface		Wireless Connectivity
	Clocks & Timers	Logic		
	Data Converters	Power Management		

{{#switchcategory:MSP430=<McuHitboxFooter/>|C2000=<McuHitboxFooter/>|Stellaris=<McuHitboxFooter/>|TMS570=<McuHitboxFooter/>|MCU=<McuHitboxFooter/>|MAVRK=<MAVRKHitboxFooter/>|<HitboxFooter/>}}

Retrieved from "https://processors.wiki.ti.com/index.php?title=Advanced_Event_Triggering&oldid=86676"

This page was last edited on 24 November 2011, at 14:11.
Content is available under [Creative Commons Attribution-ShareAlike](#) unless otherwise noted.