Direction	Address	Register	Size	Data	Delay
Write	0x6e	0x00	1	0x0f	
Write	0x6e	0x01	1	0x00	
Write	0x6e	0x02	1	0x18	
Write	0x6e	0x03	1	0x00	
Write	0x6e	0x04	1	0x00	
Write	0x6e	0x05	1	0x00	
					10ms
Write	0x6f	0x00	1	0x0f	
Write	0x6f	0x01	1	0x00	
Write	0x6f	0x02	1	0x18	
Write	0x6f	0x03	1	0x00	
Write	0x6f	0x04	1	0x00	
Write	0x6f	0x05	1	0x00	
Write	0x6e	0x03	1	0x51	
Write	0x6f	0x03	1	0x59	
Write	0x6e	0x05	1		
Read	0x6e		1	0x00	
Write	0x6e	0x05	1	0x01	
Write	0x6e	0x02	1		
Read	0x6e		1	0x18	
Write	0x6e	0x02	1	0x1c	
Write	0x6f	0x05	1		
Read	0x6f		1	0x00	
Write	0x6f	0x05	1	0x01	
Write	0x6f	0x02	1		
Read	0x6f		1	0x18	
Write	0x6f	0x02	1	0x1c	
					250ms
Write	0x68	0x16	1		
Read	0x68		1	0x80	
Write	0x68	0x17	1		
Read	0x68		1	0xff	
Write	0x68	0x16	1		
Read	0x68		1	0x00	
					34ms
Write	0x68	0x16	1		
Read	0x68		1	0x00	
Write	0x68	0x12	1	0x04	
					34ms
Write	0x68	0x1b	3	0x10,0x00,0x09	
Write	0x68	0x1e	1	0x00	
Write	0x68	0x1a	1	0x01	
Write	0x68	0x0f	1	0x92	
Write	0x68	0x12	1	0x01	
Write	0x68	0x40	1	0x00	
Write	0x68	0x41	1	0x02	
Write	0x68	0x09	1	0x01	

Write	0x68	0x13	1	0x92	
Write	0x68	0x16	1		
Read	0x68		1	0x80	
Write	0x68	0x17	1		
Read	0x68		1	0xff	
Write	0x68	0x16	1		
Read	0x68		1	0x00	
					34ms
Write	0x68	0x16	1		
Read	0x68		1	0x80	
Write	0x68	0x17	1		
Read	0x68		1	0x18	
Write	0x68	0x09	1	0x21	
Write	0x68	0x01	1	0x00	
Write	0x69	0x02	1		
Read	0x69		4	0xAD,0x10,0x21,0x01	
Write	0x69	0x0a	1	0x00	
Write	0x69	0x0b	1	0x00	
Write	0x69	0x0c	1	0x03	
Write	0x69	0x0d	1	0x08	
Write	0x69	0x0e	1	0x00	
Write	0x69	0x3f	1	0x00	
Write	0x69	0x41	1	0x00	
Write	0x69	0x42	1	0x30	
Write	0x69	0x43	1	0x00	
Write	0x69	0x46	1	0x00	
Write	0x69	0x47	1	0x17	
Write	0x69	0x48	1	0x00	
Write	0x69	0x49	1	0x00	
Write	0x69	0x4a	1	0x00	
Write	0x69	0x4d	1	0x00	
Write	0x69	0x4e	1	0x00	
Write	0x69	0x50	1	0x00	
Write	0x69	0x51	1	0x00	
Write	0x69	0x52	1	0x01	
Write	0x69	0x1b	2	0x10,0x00	
Write	0x69	0x1e	1	0x00	
Write	0x69	0x09	1	0x01	
Write	0x68	0x16	1		
Read	0x68		1	0x00	
					23ms
Write	0x68	0x01	1	0x00	
Write	0x69	0x09	1	0x01	
Write	0x69	0x1b	1		
Read	0x69		2	0x10,0x00	
Write	0x69	0x1b	2	0x10,0x00	
Write	0x68	0x13	1	0x90	
					11ms
Write	0x68	0x16	1		

Read	0x68		1	0x80
Write	0x68	0x17	1	
Read	0x68		1	0x18
Write	0x68	0x01	1	0x00
Write	0x69	0x09	1	0x21
Write	0x68	0x01	1	0x01
Write	0x69	0x02	1	
Read	0x69		4	0xAD,0x10,0x21,0x01
Write	0x68	0x01	1	0x01
Write	0x69	0x00	1	0x56
Write	0x68	0x01	1	0x21
Write	0x69	0x00	1	0x00
Read	0x69		8	0x00,0x54,0x46,0x05,0x00,0x00,0x00,0x62
Write	0x68	0x01	1	0x01
Write	0x69	0x0a	1	0x00
Write	0x69	0x0b	1	0x08
Write	0x69	0x0c	1	0x00
Write	0x69	0x0d	1	0x00
Write	0x69	0x0e	1	0x00
Write	0x69	0x3f	1	0x00
Write	0x69	0x41	1	0x01
Write	0x69	0x42	1	0x03
Write	0x69	0x43	1	0x00
Write	0x69	0x46	1	0x00
Write	0x69	0x47	1	0x10
Write	0x69	0x48	1	0x00
Write	0x69	0x49	1	0x00
Write	0x69	0x4a	1	0x00
Write	0x69	0x4d	1	0x00
Write	0x69	0x4e	1	0x00
Write	0x69	0x50	1	0x00
Write	0x69	0x51	1	0x00
Write	0x69	0x52	1	0x01
Write	0x69	0x1b	2	0x00,0x00
Write	0x69	0x1e	1	0x00
Write	0x69	0x09	1	0x01
Write	0x68	0x16	1	
Read	0x68		1	0x00
				34ms
Write	0x68	0x16	1	
Read	0x68		1	0x00
				23ms
Write	0x68	0x01	1	0x01
Write	0x69	0x4e	1	
Read	0x69		1	0x00
Write	0x69	0x4e	1	0x20
Write	0x69	0x4d	1	

Read	0x69		1	0x00	
Write	0x69	0x4d	1	0x44	
Write	0x69	0x4b	2	0x04,0x40	
					200ms
Write	0x68	0x01	1	0x01	
Write	0x69	0x00	1	0x70	
Write	0x68	0x01	1	0x21	
Write	0x69	0x01	1	0x14	
Write	0x69	0x03	1	0x88	
Write	0x68	0x01	1	0x01	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
Write	0x69	0x4b	2	0x44,0x00	
					200ms
Write	0x69	0x4e	1		
Read	0x69		2	0x20,0x00	
Write	0x68	0x01	1	0x01	
Write	0x69	0x00	1	0x70	
Write	0x68	0x01	1	0x21	
Write	0x69	0x01	1	0x16	
Write	0x68	0x01	1	0x01	

Write	0x69	0x4b	2	0x44,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00
Write	0x69	0x4e	2	
Read	0x69		2	0x20,0x00

Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x00	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x00	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x00	
Write	0x69	0x4e	2		
Read	0x69		2	0x20,0x20	
					25ms
Write	0x68	0x01	1	0x01	
Write	0x69	0x00	1	0x6c	
Write	0x68	0x01	1	0x21	
Write	0x69	0x00	1	0x0f	
Write	0x69	0x01	1	0x00	
Write	0x69	0x02	1	0x18	
Write	0x69	0x03	1	0x49	
Write	0x69	0x04	1	0x00	
Write	0x69	0x05	1	0x01	
Write	0x68	0x01	1	0x01	
Write	0x69	0x00	1	0x6e	
Write	0x68	0x01	1	0x21	
Write	0x69	0x00	1	0x5c	
Write	0x69	0x01	1	0x07	
Write	0x69	0x02	1	0x0f	
Write	0x69	0x03	1	0x00	
Write	0x69	0x04	1	0x00	
Write	0x69	0x05	1	0x11	
Write	0x69	0x06	1	0x11	
Write	0x69	0x07	1	0x11	
Write	0x69	0x08	1	0x11	
Write	0x69	0x09	1	0x00	
Write	0x69	0x0a	1	0x00	
Write	0x69	0x0b	1	0x00	
Write	0x69	0x0c	1	0x40	
Write	0x69	0x0d	1	0x00	
Write	0x69	0x0e	1	0x03	
Write	0x68	0x01	1	0x01	
Write	0x69	0x09	1	0x00	
Write	0x69	0x09	1	0x01	
Write	0x68	0x01	1	0x00	
Write	0x69	0x09	1	0x01	
Write	0x68	0x0d	1	0x08	
Write	0x68	0x0e	1	0x03	
Write	0x68	0x01	1	0x00	
Write	0x68	0x4d	1	0x00	
Write	0x68	0x4e	1	0x00	
Write	0x68	0x50	1	0x00	
Write	0x68	0x51	1	0x00	
Write	0x68	0x52	1	0x01	

Write	0x68	0x1b	3	0x10,0x00,0x09	
Write	0x68	0x1e	1	0x00	
Write	0x68	0x11	1	0x03	
Write	0x68	0x12	1	0x01	
Write	0x68	0x12	1	0x02	
					2ms
Write	0x68	0x14	1		
Read	0x68		1	0x01	
					11ms
Write	0x68	0x16	1		
Read	0x68		1	0x00	
Write	0x68	0x14	1		
Read	0x68		1	0x01	
					11ms
Write	0x68	0x14	1		
Read	0x68		1	0x01	
					11ms
Write	0x68	0x14	1		
Read	0x68		1	0x01	
					11ms
Write	0x68	0x16	1		
Read	0x68		1	0x00	
Write	0x68	0x14	1		
Read	0x68		1	0x01	
					11ms
Write	0x68	0x14	1		
Read	0x68		1	0x01	
					11ms
Write	0x68	0x14	1		
Read	0x68		1	0x01	
					11ms
Write	0x68	0x16	1		
Read	0x68		1	0x00	
Write	0x68	0x14	1		
Read	0x68		1	0x01	
					11ms
Write	0x68	0x14	1		
Read	0x68		1	0x01	
					11ms
Write	0x68	0x14	1		
Read	0x68		1	0x01	

Comments	Context
Set IB0=0x0f	
Set IB1=0x00	
Set IB2=0x18	TDA7802-0
Set IB3=0x00	
Set IB4=0x00	
Set IB5=0x00	
Set IB0=0x0f	
Set IB1=0x00	
Set IB2=0x18	TDA7802-1
Set IB3=0x00	
Set IB4=0x00	
Set IB5=0x00	
Set TDM config	TDA7802-0
Set TDM config	TDA7802-1
Query IB5	
IB5=0x00	
Set amp enable bit to 1	TDA7802-0
Query IB2	
IB2=0x18	
Set IB2=0x1c	
Query IB5	
IB5=0x00	
Set amp enable bit to 1	TDA7802-1
Query IB2	
IB2=0x18	
Set IB2=0x1c	
Query INTSRC	
INTSRC=master	
Query INTTYPE	
INTTYPE=0xff	
Query INTSRC	
No interrupts present	
Query INTSRC	
No interrupts present	
Soft reset	
Set interrupt masks:	
PWREIEN,SLVIRQ,DSCDIEN	
Zero out BECCTL	
Set INTPND to discovery done	
Set RESPCYCS=0x92	
Set NEWSTRCT bit to 1	
Zero out PLLCTL	A2B Master
Set TDMMODE to 2	
Set ENSW bit to 1	

Set DRESPCYC=0x92
Query INTSRC
INTSRC=master
Query INTTYPE
INTTYPE=0xff
Query INTSRC
No interrupts present

Query INTSRC
INTSRC=master
Query INTTYPE
INTTYPE=0x18
Set SWCTL to mode 2, ENSW
Select slave node 0
Query ID register base
Read ID registers of slave node 0
Zero out BCDNSLOTS
Set LDNSLOTS to 0
Set LUPSLOTS to 3
Set DNSLOTS to 8
Set UPSLOTS to 0
Zero out I2CCFG
Zero out I2SGCFG
Enable RX0/RX1 channels
Zero out I2SRATE
Set SYNCOFFSET to zero   A2B Slave 0
Set PDM0/PDM0SLOTS/PDM1 bits to 1   (microphone
Disable error management   array)
Zero out register 0x49
Set all GPIOs low
Disable all GPIO outputs
Disable all GPIO inputs
Disable interrupts on all pins
Disable interrupt inversion on all pins
Set pin drive strength to high
Set interrupt masks: PWREIEN
Zero out BECCTL
Set ENSW bit to 1
Query INTSRC
No interrupts present   A2B Master

Select slave node 0   A2B Master
Set ENSW bit to 1
Query INTMSK0,INTMSK1   A2B Slave 0
Interrupt mask: PWREIEN   (microphone
Set interrupt masks: PWREIEN   array)
Set DRESPCYC=0x90

Query INTSRC

INTSRC=master	
Query INTTYPE	A2B Master
INTTYPE=0x18	
Select slave node 0	
Set SWCTL to mode 2, ENSW	A2B Slave 0 (microphone array)
Select slave node 1	A2B Master
Query ID register base	A2B Slave 1
Read ID registers of slave node 1	(amp)
Select slave node 1	
Set address to EEPROM at amp	A2B Master
Set peripheral mode on	
Query EEPROM	
Read 8 bytes from EEPROM	EEPROM
Select slave node 1	A2B Master
Zero out BCDNSLOTS	
Set LDNSLOTS to 8	
Set LUPSLOTS to 0	
Set DNSLOTS to 0	
Set UPSLOTS to 0	
Zero out I2CCFG	
Set TDMMODE to 1	
Enable TX0/TX1 channels	
Set I2SRATE to zero	
Set SYNCOFFSET to zero	
Enable HPF	A2B Slave 1
Disable error management	(amp)
Zero out register 0x49	
Set all GPIOs low	
Disable all GPIO outputs	
Disable all GPIO inputs	
Disable interrupts on all pins	
Disable interrupt inversion on all pins	
Set pin drive strength to high	
Disable all interrupts	
Zero out BECCTL	
Set ENSW bit to 1	
Query INTSRC	
No interrupts present	
Query INTSRC	A2B Master
No interrupts present	
Select slave node 1	
Query GPIOEN	
GPIOEN returns zero	
Enable IO5	
Query GPIOOEN	A2B Slave 1 (amp)

GPIOOEN returns zero (amp)
Enable IO2,IO6
Set IO1,IO2 high and clear IO6

Select slave node 1
Set address to GPIO expander at amp   A2B Master
Set peripheral mode on
Set IO2,IO4 of amp GPIO expander high   GPIO
Set control register of GPIO expander at   Expander
amp node
Select slave node 1   A2B Master
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered   A2B Slave 1
Query GPIOIEN,GPIOEN status of slave 1 (amp)
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered
Enable inputs on IO2,IO6

Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Select slave node 1
Set address to GPIO expander at amp   A2B Master
Set peripheral mode on
Set IO1,IO2,IO4 of amp GPIO expander   GPIO
high   Expander
Select slave node 1   A2B Master

Enable inputs on IO2,IO6
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered   A2B Slave 1
Query GPIOIEN,GPIOEN status of slave 1 (amp)
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered

Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
No GPIOs triggered
Query GPIOIEN,GPIOEN status of slave 1
IO5 has been triggered

Select slave node 1
Set address to TDA7802 at amp   A2B Master
Set peripheral mode on

Configure TDA7802 registers   TDA7802-2

Select slave node 1
Set address to FDA2100 at amp   A2B Master
Set peripheral mode on

Configure FDA2100 registers   FDA2100

Select slave node 1   A2B Master
Reset SWCTL register on slave node 1   A2B Slave 1
Set ENSW bit to 1   (amp)
Select master node
Set ENSW bit to 1
Set DNSLOTS to 8 for amp
Set UPSLOTS to 3 for mics
Select master node
Disable all GPIO outputs
Disable all GPIO inputs
Disable interrupts on all pins
Disable interrupt inversion on all pins
Set pin drive strength to high

Set interrupt masks: PWREIEN,
SLVIRQEN, DSCDIEN
Zero out BECCTL
Set upstream/downstream data enable
Set NEWSTRCT bit to 1
End discovery

Query SWSTAT
SWSTAT returns ok

Query INTSRC
No interrupts present
Query SWSTAT
SWSTAT returns ok

Query SWSTAT
SWSTAT returns ok   A2B Master

Query SWSTAT
SWSTAT returns ok

Query INTSRC
No interrupts present
Query SWSTAT
SWSTAT returns ok

Query SWSTAT
SWSTAT returns ok

Query SWSTAT
SWSTAT returns ok

Query INTSRC
No interrupts present
Query SWSTAT
SWSTAT returns ok

Query SWSTAT
SWSTAT returns ok

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