

PS

Boot
ROM

CPU

On-Chip
RAM

AXI Top-Switch

MDDR
Flash

DDR
Memory

NAND

NOR

QSPI

SD

IOP

Device
Configuration

Security
Management

AXI-PCAP Bridge

AXI Master Interface

TX FIFO

RX FIFO

PCAP Interface

XADC
Interface

AXI BUS

PL

Enhanced
Anderson PUF

PL

